# imall

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433 MHz Impedance Matched Balun + LPF Integrated Front-End SMD Passive Component for SiLabs Si4455, Si4460, Si4461, Si4463, and Si4464 Chipsets

5/30/2017 Detail Specification:

P/N 0433BM41A0019

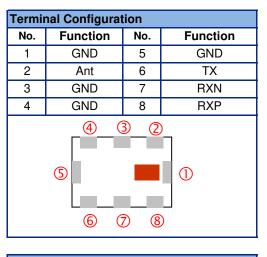
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| General Specifications                           |  |                           |   |
|--|--|---------------------------|---|
| Part Number                                      | 0433BM41A0019  |                           |   |
| Frequency Range(MHz)                             | 424-444  |                           |   |
| Balanced Impedance                               | Si4455, Si4460, Si4461,<br>Si4463, Si4464                        |                           |   |
| Unbalanced impedance                             | 50 $\Omega$ (single ended)                                       | Terr 1                    | and there                                     |
| Average Insertion Loss when                      | 0.9dB Typ@25C<br>1.5dB max. (-45 to +85C)                        |                           |   |
| connected to Si44XX chipset<br>(Active OP Tx/Rx) |  | Phase Diff. (deg.)        | 180° ± 10                                     |
|  |  | VSWR @ BW                 | 2.0 max.                                      |
| Insertion Loss when                              | 1.6dB Typ in Rx, 1.4dB Typ in Tx<br>@25C 2.2dB max. in Rx, 1.9dB | Amplitude Difference (dB) | 2.0 max.                                      |
| component measured by                            |  | Reel Quanity              | 4,000   |
| itself(passive insertion loss)                   | max. in Tx (-45 to +85C)   | Operating Temperature     | -40 to +85°C                                  |
|  | 35 min. @ 2xfo MHz   | Recommended Storage       |   |
| Attenuation<br>dB (min.)                         | 35 min. @ 3xfo MHz   | Conditions for Unused     | +5 ~ +35 °C, Humidity<br>45~75%RH, 18 months. |
|  | 35 min. @ 4xfo MHz   | product on T&R            |   |
|  | 35 min. @ 5xfo MHz   | Input Power               | 500mW max. (CW)                               |

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| Part Number Explanation |                   |       |               |                           |  |  |
|-------------------------|-------------------|-------|---------------|---------------------------|--|--|
| P/N Suffix              | Packing Style     | Bulk  | Suffix = S    | eg. 0433BM41A0019S        |  |  |
|                         |                   | T & R | Suffix = E    | eg. 0433BM41A0019E        |  |  |
|                         | Termination style | Ni/Sn | Suffix = None | eg. 0433BM41A0019(E or S) |  |  |

| Mechanical Dimensions   |       |    |       |             |  |  |  |
|---|-------|----|-------|-------------|--|--|--|
|   | In    |    |       | mm          |  |  |  |
| L   | 0.126 | ±  | 0.008 | 3.20 ± 0.20 |  |  |  |
| W   | 0.098 | ±  | 0.008 | 2.50 ± 0.20 |  |  |  |
| Т   | 0.059 | ±  | 0.006 | 1.50 ± 0.15 |  |  |  |
| a1  | 0.022 | ±  | 0.006 | 0.55 ± 0.15 |  |  |  |
| a2  | 0.028 | ±  | 0.008 | 0.70 ± 0.20 |  |  |  |
| b   | 0.0   | 04 | min.  | 0.1 min.    |  |  |  |
| С   | 0.012 | +  | 0.008 | 0.30 + 0.20 |  |  |  |
| g   | 0.018 | +  | 0.006 | 0.45 + 0.15 |  |  |  |
| р   | 0.039 | +  | 0.008 | 1.00 + 0.20 |  |  |  |
| a2<br>$\downarrow$ $\downarrow$ $\downarrow$ $\downarrow$ $\downarrow$ $\downarrow$ $\downarrow$ $\downarrow$ $\downarrow$ $\downarrow$ |       |    |       |             |  |  |  |



| OC Connection for Pins        |  |  |  |  |  |  |
|-------------------------------|--|--|--|--|--|--|
| (1) Pin1 – Pin3 – Pin4 – Pin5 |  |  |  |  |  |  |

(2) Pin2 - Pin7- Pin8

Do you need to download the reference design/layout files? Go to: https://www.johansontechnology.com/silabs

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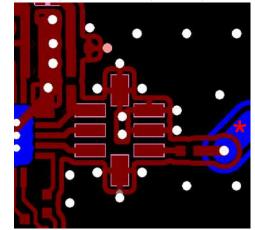
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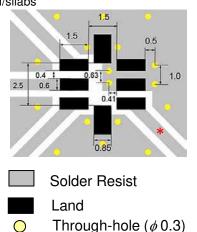
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#### **Mounting Considerations**

Line/Via width should be designed to match 500hm characteristic impedance, depending on PCB material and thickness.

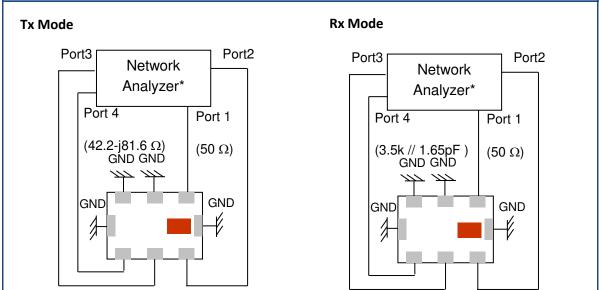
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#### Measuring Diagram



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#### Measuring Diagram (cont.)

#### Tx Mode:

Port1: Antenna Port Port1 Terminate impedance: 50ohm Ports 2 and 3: Rx Balanced Port Port 2 and 3 Terminate impedance: 1/2 x (the loading impedance ZIC,RX off of Si4455) Port4: Tx Port Port4 Terminate impedance: Complex conjugate to ZIC,TX on of Si4455

IL = S41 RL = S11 / S44

#### RXMode:

Port 1: Antenna Port Port1 Terminate impedance: 500hm Ports 2 and 3: Rx Balanced Port Port 2 and 3 Terminate impedance: Complex conjugate to 1/2 x (Balance impedance of ZIC,RX on of Si4455) Port4: Tx Port Port4 Terminate impedance: The loading impedance ZIC,TX off of Si4455

IL=Sds21 RL=Sss11 / Sdd22 Amp\_balance = dB(S(3,1)/S(2,1))

#### Typical Electrical Characteristics (T=25oC) TX mode: Insertion and Return Loss 0 -10 -20 -20 -20 -20 -20 -20 -20 -20 -20 -20 -20 -20 -20 -40 -40 -50 -60 0.3 1.3 2.3 3.3freq, GHz

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Typical Electrical Characteristics (T=25oC) **RX mode:** Insertion and Return Loss -10 -20 -30 -40 -50 -60-0.3 1.3 2.3 3.3 RX mode: freq, GHz **Amplitude and Phase Balance** 2 200 1 100 Phase\_difi Amp\_diff 0 -0 -1 -100 -200 -2 380 410 440 470 350 500 freq, MHz

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#### Application Notes, Layout Files, and more

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**RoHS Compliance** 

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**Soldering Information** 

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Antenna layout and tuning techniques

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Antenna layout review, tuning, and characterization services

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MSL Info

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Recommneded Storage Condition and Max Shelf Life

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Packaging information

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