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Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

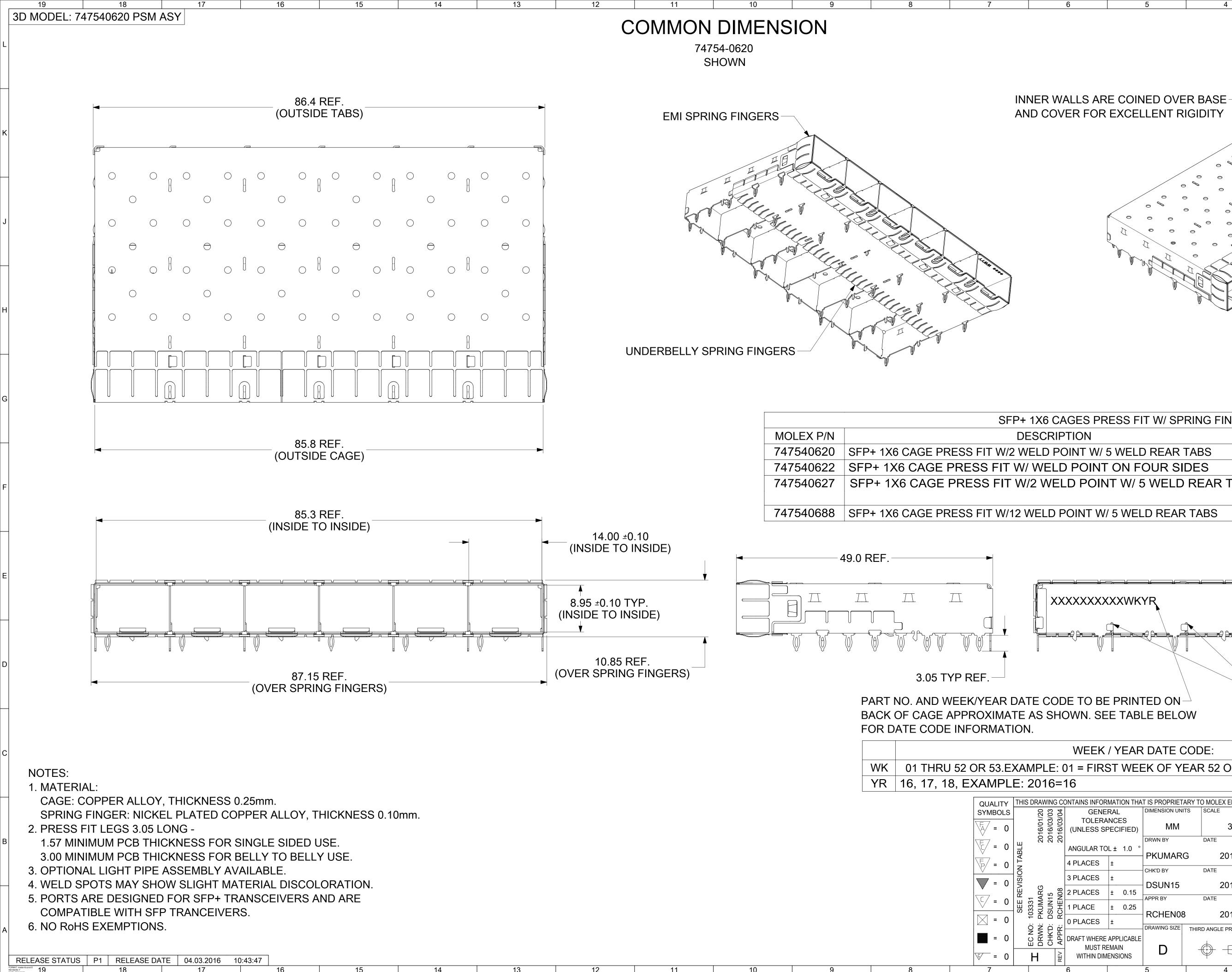
We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832 Email & Skype: info@chipsmall.com Web: www.chipsmall.com Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China



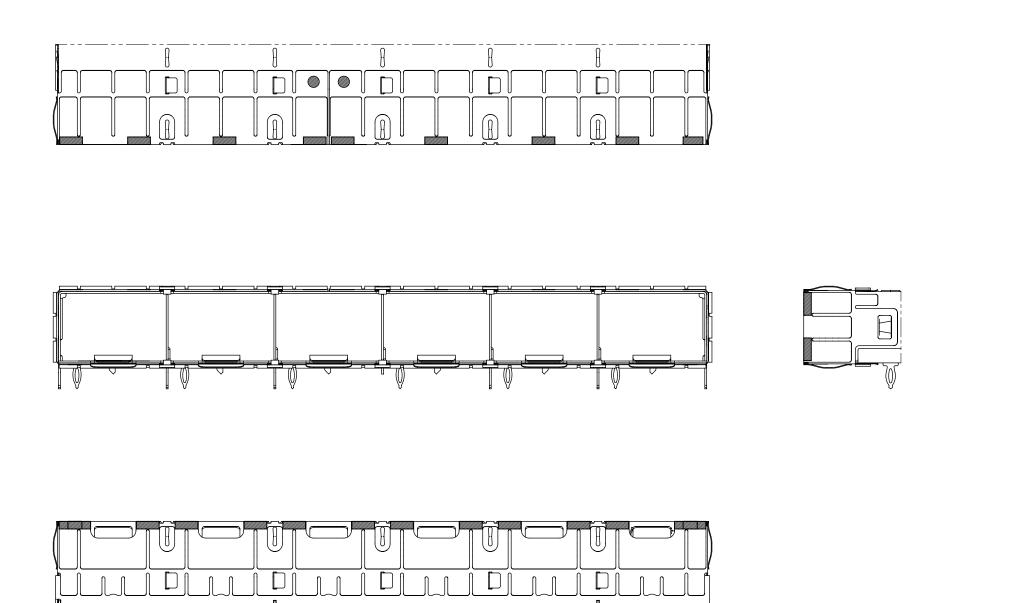


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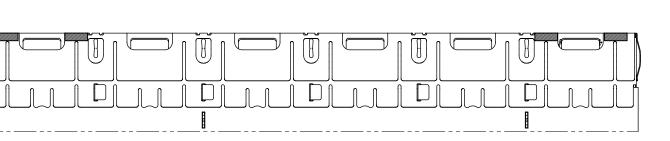
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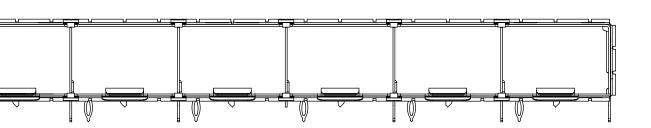
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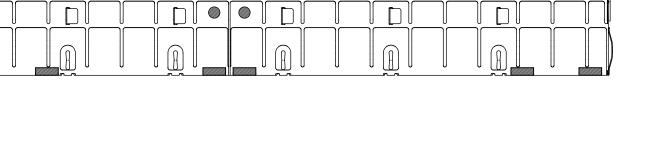


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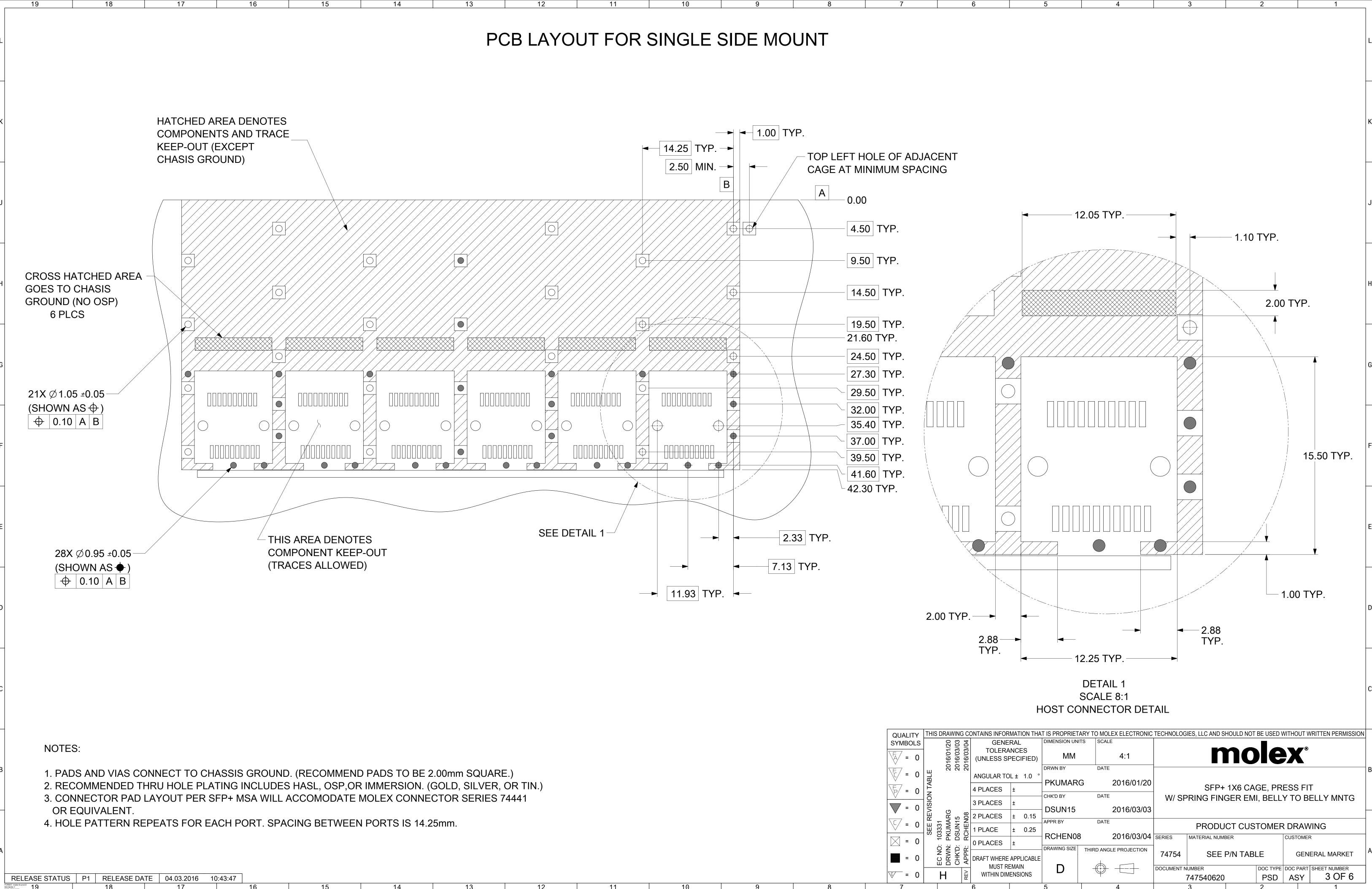
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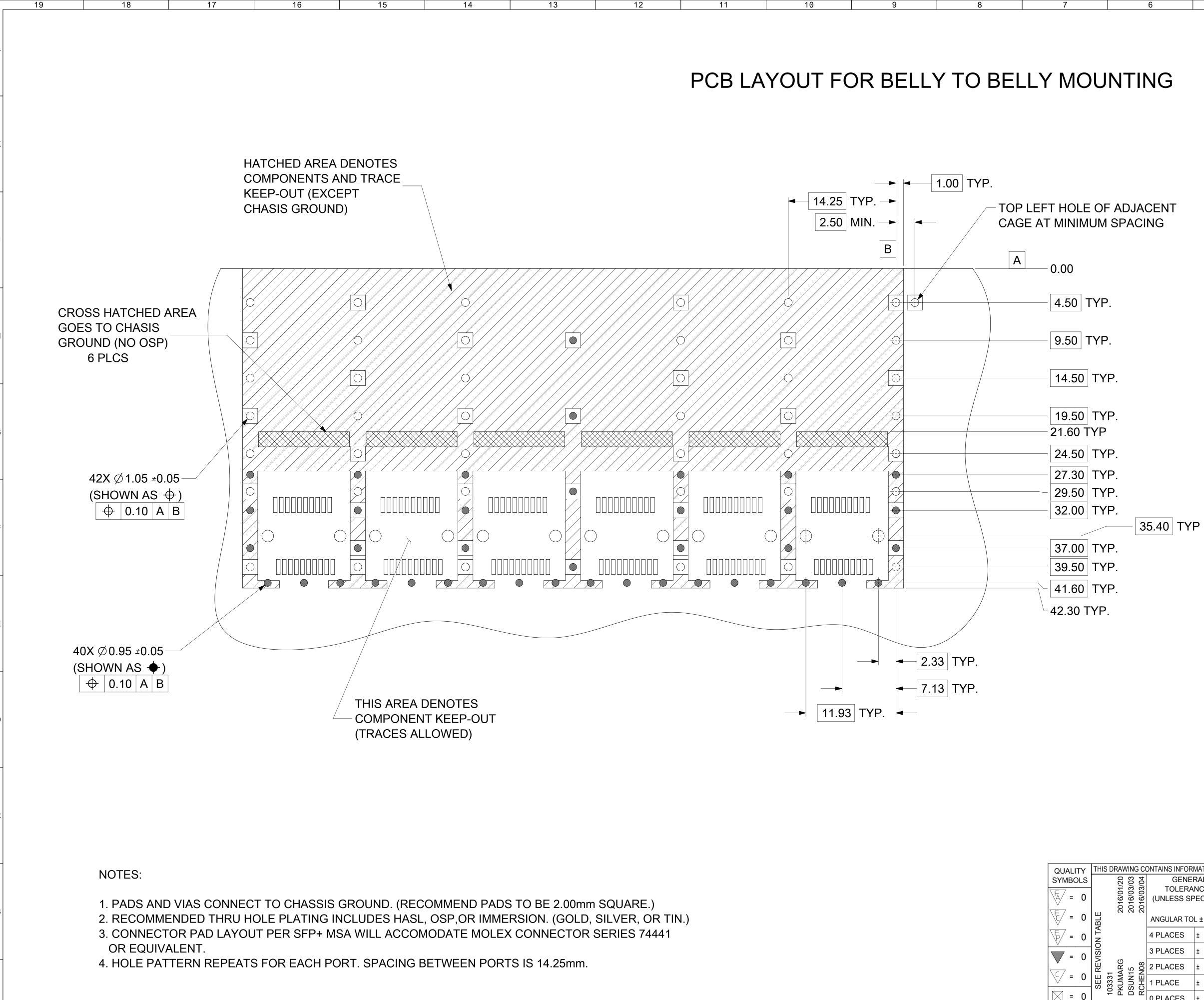






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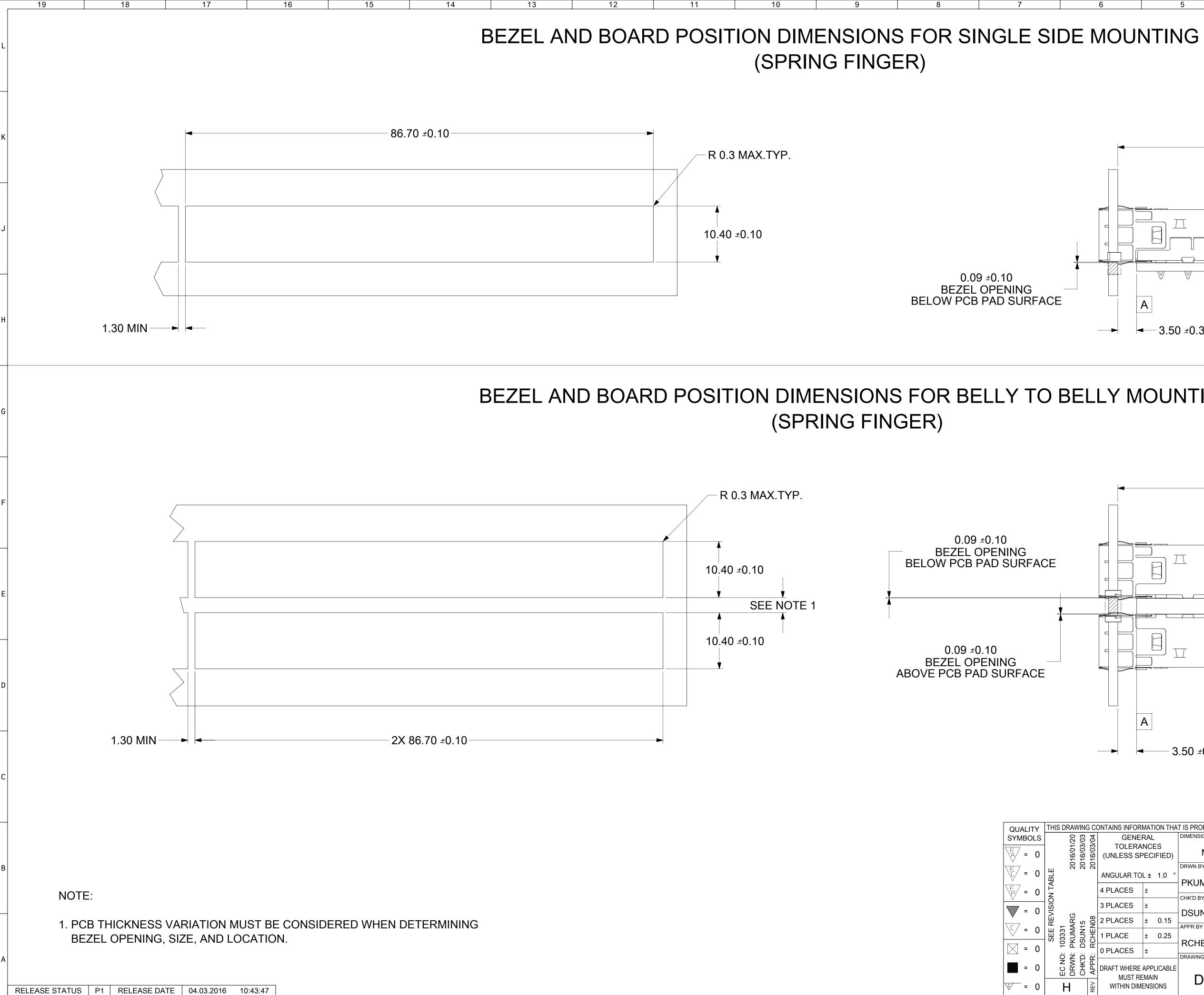
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NOTE: SEE SHEET 3 FOR HOST CONNECTOR DETAIL

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45.5 REF.

DATE	REV	DESCRIPTION
2009/04/13	С	INITIAL RELEASE
2012/10/29	D	1. ADDED P/N 747540622 AND 747540627. [SHT 1]
2012/12/21	E	 ADDED 747540627 TO WELD LOCATION CALLOUT. [SHT 1] REMOVED TYP. FROM DIM 85.3. [SHT 1] ADDED "MM 3D MODEL: EM-74754-0620-ASSY" MODEL NAME ON LEF TOP CORNER. [SHT 1] CHANGED NOTE 6 AS "NO ROHS EXEMPTIONS". [SHT 1] ADDED BACK VIEW FOR DATE CODE INFORMATION. [SHT 1] ADDED REF TO DIM 86.4, 85.8, 85.3, 49.0, AND 3.05 [SHT 1] REMOVED "WELDED REAR TABS (X5)". [SHT 1] CHANGED "14.25 TYP" & "2.5 MIN" TO A BASIC DIMENSION. [SHT 2 & 3] FILLED IN HOLES IN DETAIL 1. [SHT 2] REMOVED INCH DIMS. [SHT 2 & 3] REMOVED "GEE NOTE 1)" X4. [SHT 4] CHANGED "BELOW" TO "ABOVE" IN BOTTOM 0.09±0.10 DIM. [SHT 4]
2014/09/11	F	1. ADDED 74754-0627 PLATING SPEC. INTO TABLE. [SHT 1]
2015/07/27	G	 "MODIFIED PCB LAYOUT PER SFF-8433" SHEET 2: @H19 : ADDED TOL OF ±0.05 TO DIM Ø1.05 @H19 : CHANGED TRUE POSITION OF PCB HOLES FROM 0.05 TO 0.1 @E19 : ADDED TOL OF ±0.05 TO DIM Ø0.95 @E19 : CHANGED TRUE POSITION OF PCB HOLES FROM 0.05 TO 0.1 SHEET 3: @H18 : CHANGED TRUE POSITION OF PCB HOLES FROM 0.05 TO 0.1 @E18 : CHANGED TRUE POSITION OF PCB HOLES FROM 0.05 TO 0.1
2016/01/20	H	REMASTERED FROM EM-74754-0620-ASSY 'G' TO 747540620 PSD REV 'H' IN NX. SHEET 1: J11: ADDED TEXT "COMMON DIMENSIONS 747540620 SHOWN". REMOVED WELD LOCATION AND WELD RELATED TEXT FROM ALL THE VIEWS. A19: ADDED P/N 747540688 TO PART NUMBER TABLE. SHEET 2: ADDED A SHEET TO SHOW FRONT SPRING FINGER WELDING LOCATION FOR P/N 747540620,747540622,747540627 & 747540688.

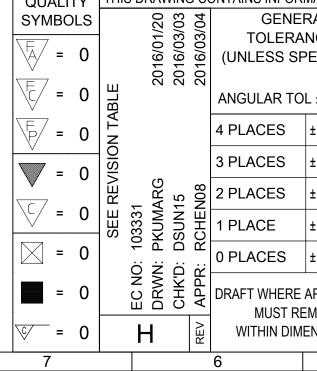
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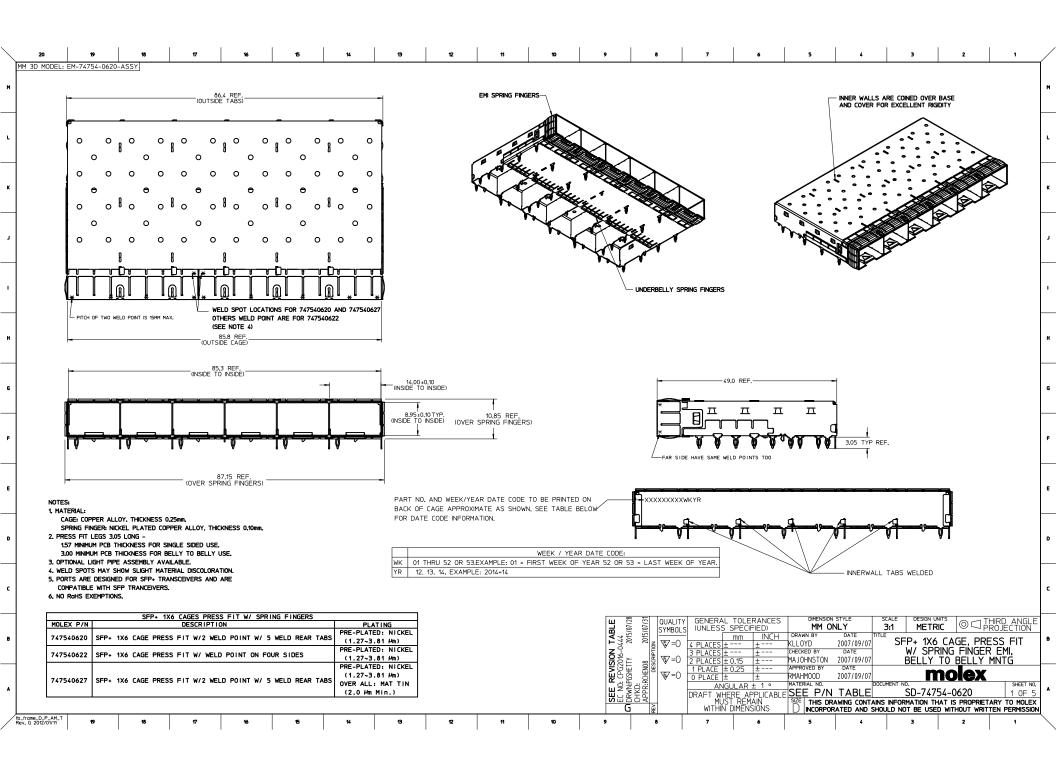
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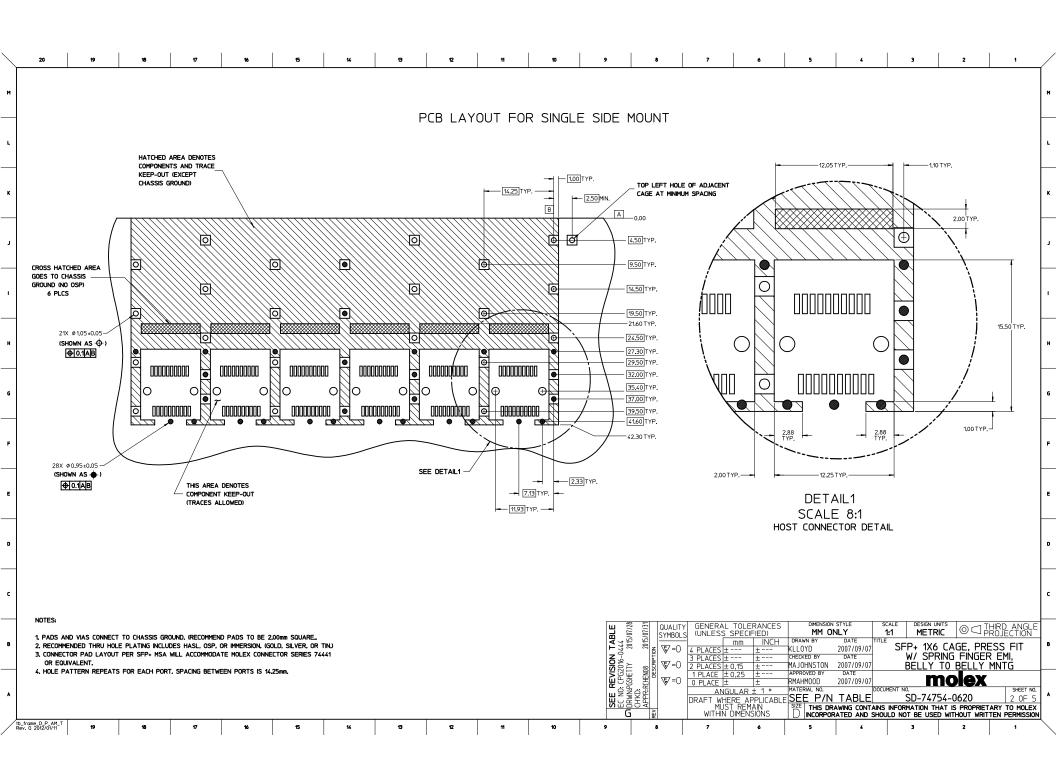


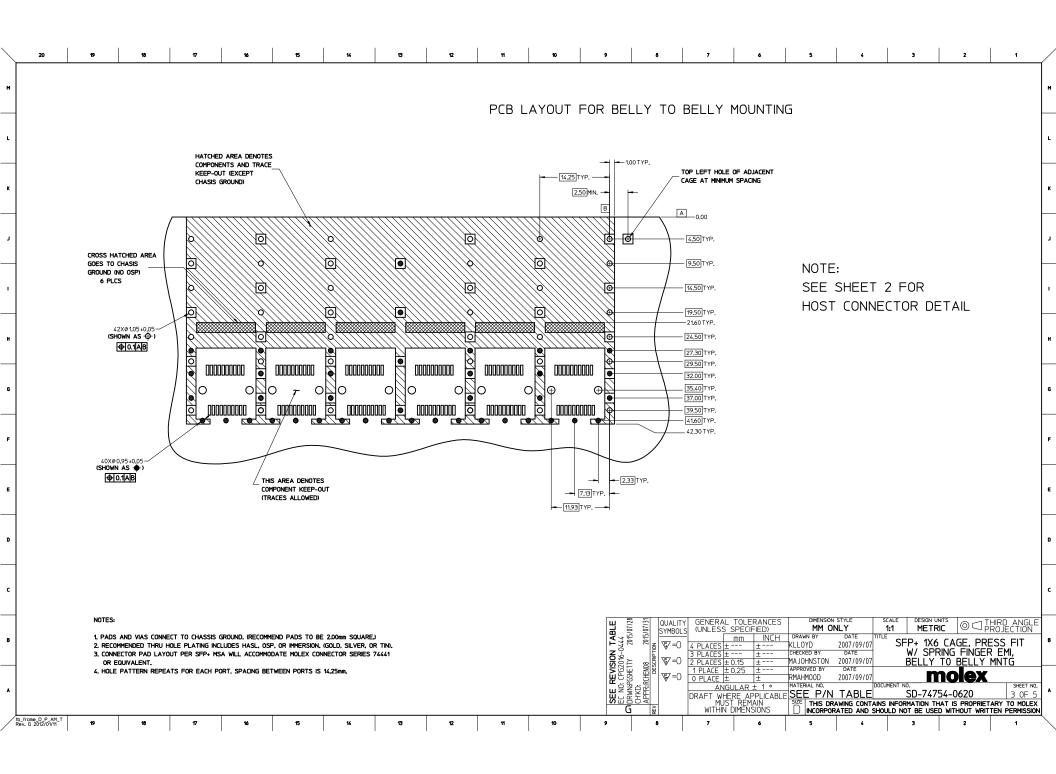
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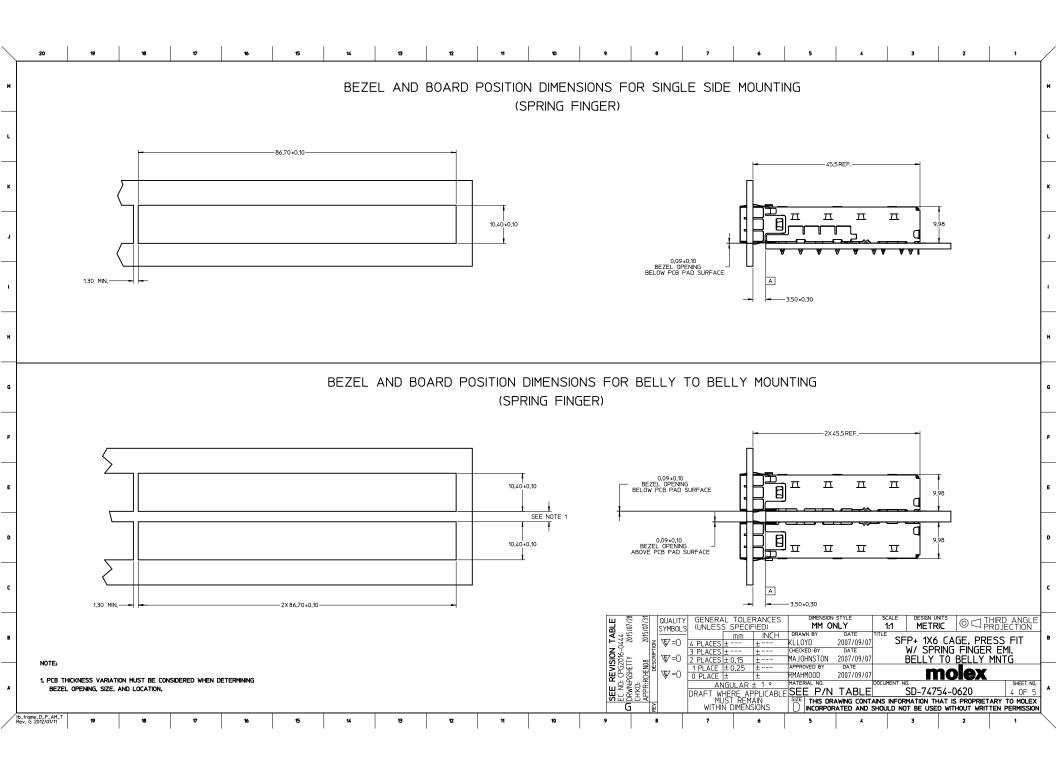
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		REVISION HISTORY
REV	DATE	
С	2009/04/13	INITIAL RELEASE
D	2012/10/29	1. ADDED P/N 747540622 AND 747540627. [SHT 1]
Е	2012/12/21	1. ADDED 747540627 TO WELD LOCATION CALLOUT. [SHT 1]
		2. REMOVED TYP. FROM DIM 85.3. [SHT 1]
		3. ADDED "MM 3D MODEL: EM-74754-0620-ASSY" MODEL NAME ON LEF TOP CORNER. [SHT 1
		4. CHANGED NOTE 6 AS "NO ROHS EXEMPTIONS". [SHT 1]
		5. ADDED BACK VIEW FOR DATE CODE INFORMATION. [SHT 1]
		6. ADDED REF TO DIM 86.4, 85.8, 85.3, 49.0, AND 3.05 [SHT 1]
		7. REMOVED "WELDED REAR TABS (X5)". [SHT 1]
		8. CHANGED "14.25 TYP" & "2.5 MIN" TO A BASIC DIMENSION. [SHT 2 & 3]
		9. FILLED IN HOLES IN DETAIL 1. [SHT 2] 10. REMOVED INCH DIMS. [SHT 2 & 3]
		11. REMOVED THE DIFIS. [SHT 2 & 3]
		12. CHANGED "BELOW" TO "ABOVE" IN BOTTOM 0.09±0.10 DIM. [SHT 4]
F	2014/09/11	1. ADDED 74754-0627 PLATING SPEC. INTO TABLE. [SHT 1]
•		
G	2015/07/27	"MODIFIED PCB LAYOUT PER SFF-8433" SHEET 2: @H19 : ADDED TOL OF ±0.05 TO DIM Ø1.05
		0H19 : CHANGED TRUE POSITION OF PCB HOLES FROM 0.05 TO 0.1
		$0E19$: ADDED TOL OF ±0.05 TO DIM ϕ 0.95
		0E19 : CHANGED TRUE POSITION OF PCB HOLES FROM 0.05 TO 0.1
		SHEET 3: @H18 : CHANGED TRUE POSITION OF PCB HOLES FROM 0.05 TO 0.1
		@E18 : CHANGED TRUE POSITION OF PCB HOLES FROM 0.05 TO 0.1