## imall

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

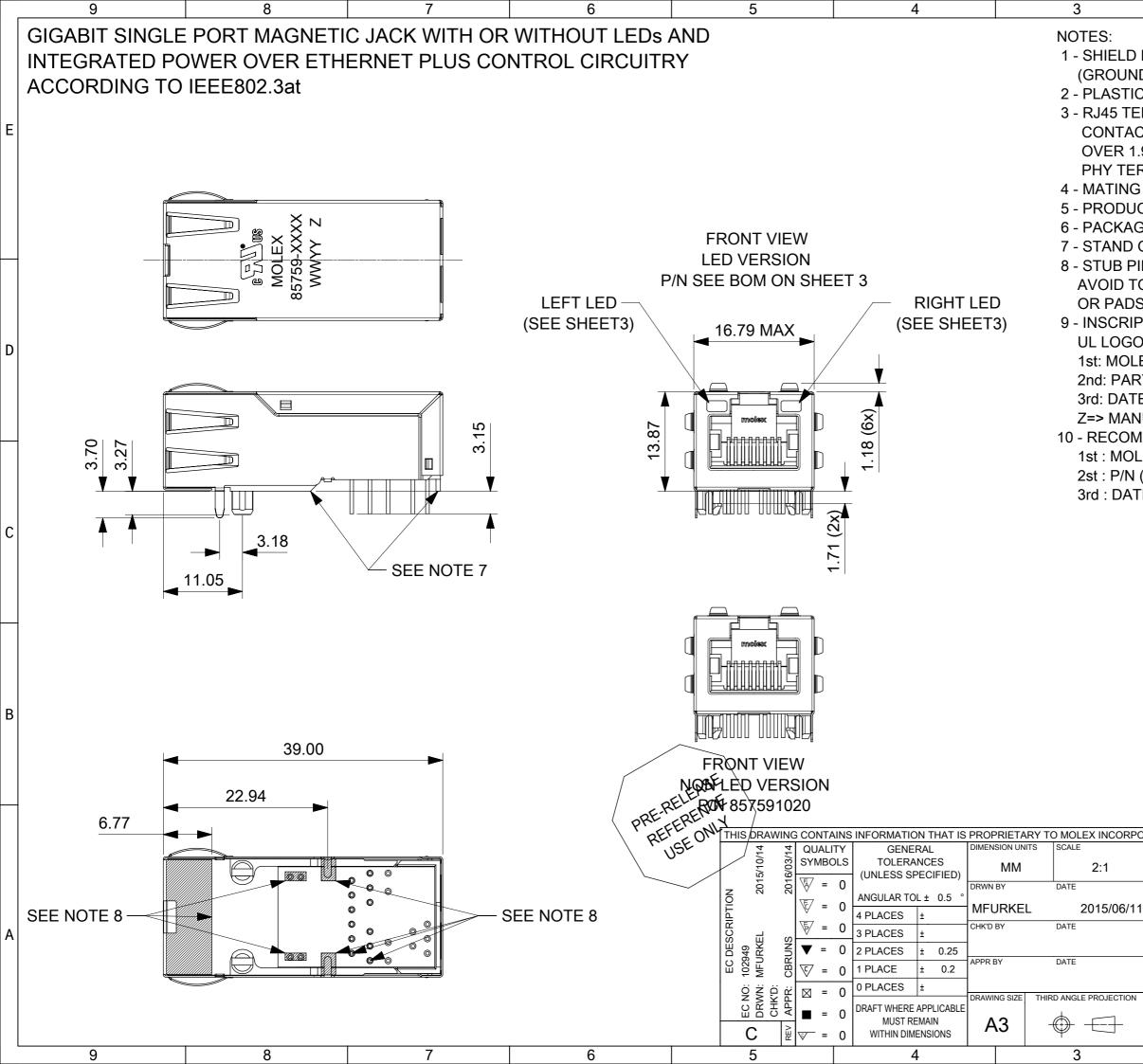
We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



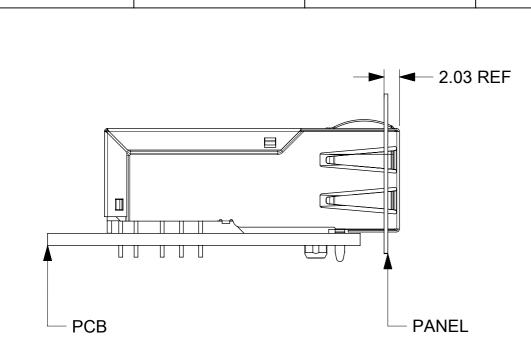
## Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832 Email & Skype: info@chipsmall.com Web: www.chipsmall.com Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China

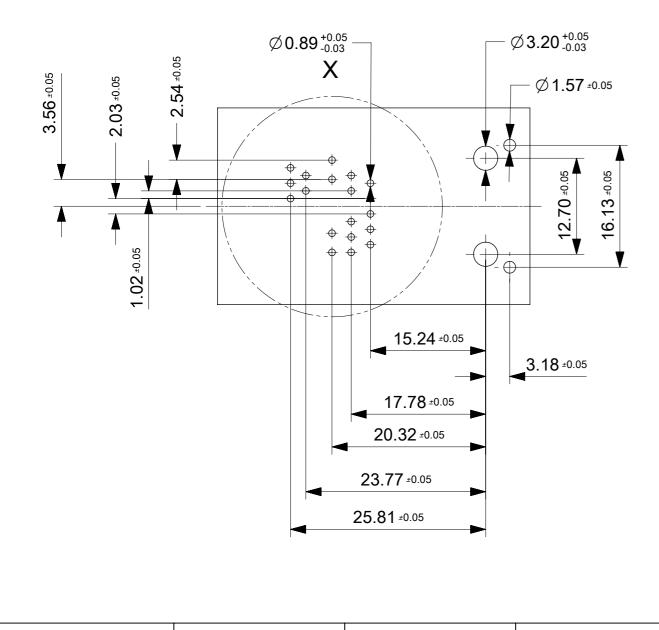




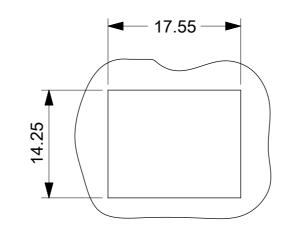
	2 1								
O MATERIAL: STAINLESS STEEL ND PINS ARE SOLDER DIPPED) IC MATERIAL: PBT, BLACK, UL 94V-0 ERMINALS MATERIAL: COPPER ALLOY CT PLATING: 0.76 MICROMETER GOLD I.9 MICROMETER NICKEL ERMINALS: TIN PLATED G INTERFACE ACCORDING TO IEC 60603-7 JCT SPECIFICATION: PS-85759-001 GING SPECIFICATION: PK-85759-001 OFF TO SYSTEM BOARD									
FC P O LE R1 FE	NS, SHIELD AND SHIELD LATCHES: PROUTE TRACES OR TO PLACE ANY VIAS IN THIS AREA FION MARKED BY LASER X NUMBER CODE (WEEK / YEAR) JFACTURER CODE MENDED PCB THICKNESS: 1.6mm / 0.067inch	D							
l (	EX SEE BOM) E CODE(WEEK/YEAR)	С							
	SCALE 1:1	В							
<sup>2</sup> 01	GIGABIT MAGNETIC JACK POE PLUS ICM 1X1								
N	PRODUCT CUSTOMER DRAWING   SERIES MATERIAL NUMBER   85759 SEE BOM / SHEET 3   DOCUMENT NUMBER DOC TYPE DOC PART   857591001 PSD 000 1 OF 3   2 1								



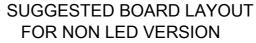
SUGGESTED BOARD LAYOUT - COMPONENT SIDE

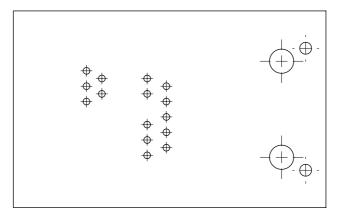


SUGGESTED PANEL CUTOUT

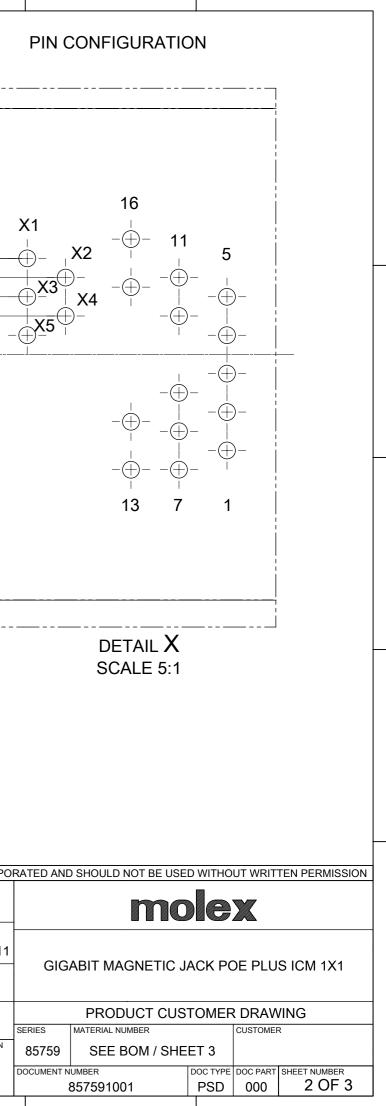


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t		4	4	QL	JALI	TY	GENE	RAL		DIMEN	SION UN	ITS	SCALE	
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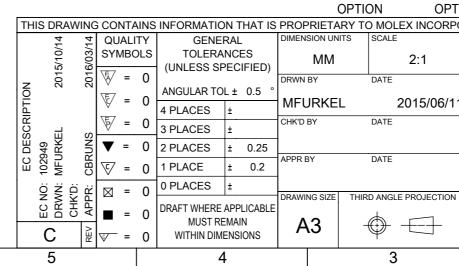


	9	8	7	6		5	4	-	3			2			1
	Electrical Specifications @25°C			PHY SIDE / PCB SI	DE	ETHERNET MAGNETICS		WIRE SIDE / RJ4	5						
	Operating temperature (0°C to +70°C)							MBER A	IBER AND LED OPTIONS						
	Description	VALUE		7 ()				——————————————————————————————————————							1
	OCL POE+TRANSF. 20mA bias	050 11 1		VCC		<u> </u>			PART		GHT LED				
	(0°C to +70°C)	350µH min.		TD 0+				O 7 MX 3+	NUMBER	PIN15		DLOR PIN13			
	OCL NONPOE TRANSF. 8mA bia	S		80	+	~~" " <u>+</u>		——————————————————————————————————————	857591001	-	+ G	SRN -	+	GRN	COLOR 1
E	(0°C to +70°C)	350µH min.		TD 1-				0 6 MX 1-						0.511	COLOR 2
	Turns Ratio	1CT:1CT		20-					857591003	-		RN -	+	GRN	COLOR 1
	Insertion Loss								(A)	+		RG			COLOR 2
	Frequency (MHz)	Limits (dB max.)	Typical Values (dB max.)	⋛ TD 1+   ∰		······································		O 5 MX 2-	857591006	-	+ G	SRN -	+	YW	COLOR 1
	1.0-9.9 MHz	0.4+0.1*log(F)	0.5 @ 10MHz			┉┈┐╻┍┼┼╴		——————————————————————————————————————							COLOR 2
	10-49.9 MHz	0.5+0.3*log(F/10)	0.7 @ 50MHz	H ₩ ₩ TD 3-					857591020			NON			
	50-79.9 MHz	1+1.4*log(F/80)	1.0 @ 80MHz	불 10〇				——————————————————————————————————————	2 857591020			NON I	ED		
	80-100 MHz	1.3+3*log(F/100)	1.3 @ 100MHz	□ TD 2+ ♥ 40											
	Return Loss					┉──┐╓┼┼	·     ·	O 2 MX 0-							
	Frequency (MHz)	Limits (dB min.)	TYPICAL Values (dB min.)	TD 3+	++_w	₩_									
	1-9.9 MHz	27dB min.	27 @ 10MHz	TD 2-		j \		——————————————————————————————————————							
<b>_</b>	10-100 MHz	27-17*log(F/10)	10 @ 100MHz	50											
D	CMR														
	Frequency (MHz)	Limits (dB min.)	TYPICAL Values (dB min.)												
	1-9.9 MHz	34dB min.	34 @ 10MHz					0.1 µF x 4							
	10-79.9 MHz	27dB min.	27 @ 80MHz												
	80-199.9 MHz	27-14.5*log(F/80)	21.5 @ 200MHz	Vmain pos X1 (		└●⊤●		-●_●_'							
	200-399.9 MHz	21.5-39*log(F/200	) 10 @ 400MHz	=1		PSE	† 1nF	F / 3 kV							
	400-1000 MHz	10	10 @ 1000MHz	DNC X3 (	$\rightarrow$	Chokes	//// Shie	h							
	NEXT			RESET X4 ( Vmain_neg X5 (			current								
	Frequency (MHz)	Limits (dB min.)	TYPICAL Values (dB min.)	vinalit_lieg X3 C			ive Device								
	1-5.9 MHz	50	50 @ 6MHz												
C	6-49.9 MHz	45-16*log(F/10)	34 @ 50MHz												
	50-100 MHz	25-30*log(F/100)	25 @ 100MHz												
	Isolation PHY to Wire side	2.25kVDC/60sec													

## CONTROL PINS

B

	PIN	NAME	FUNCTION	N	PARAMETERS						
	X1	Vmain_pos	Vmain 51 to 56V DC IN min. 470mA (Class0-3), min. 850m								
	X2	Max_Class / Port Status	INPUT (po	wer up): Set Max Class	To be set by resistors to Vmain_neg						
			Class 1 on	ly	10k						
A			Class 1 or	2 only	20k						
			All except	class 4	30k						
			All classes	including class 4	Open						
			OUTPUT (	powered): Power indication	high if PD is powered up by 1	x1 PSE					
	X3	DNC	reserved		remain floating						
	X4	Reset	Reset		Reset or to disable PSE						
	X5	Vmain_neg	Vmain Ref	erence							
9				8	7	f	6				



LEFT LED

-0 13

-0 14

 $\downarrow$ 

BICOLOR

						С			
			-0 15 1/2 -0 16	HT LED	0 15 7 0 16 E COLOR	В			
		OPTIO				1			
:1		mo		X					
5/06/11									
	PRODUCT CUSTOMER DRAWING								
DJECTION	85759								
	DOCUMENT N	UMBER 857591001	DOC TYPE PSD	DOC PART	SHEET NUMBER 3 OF 3				
		2			1				

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