# imall

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



## Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832 Email & Skype: info@chipsmall.com Web: www.chipsmall.com Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China

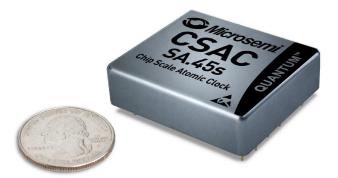


DATASHEET



## Quantum<sup>™</sup> SA.45s CSAC

Chip Scale Atomic Clock



#### **Key Features**

- Power consumption <120 mW
- Less than 17 cc volume, 1.6" x 1.39" x 0.45"
- 10 MHz CMOS-compatible output
- 1 PPS output and 1 PPS input for synchronization
- RS-232 interface for monitoring and control
- Short term stability (Allan Deviation) of 2.5E-10@ TAU =1 sec

#### **Applications\***

- Underwater sensor systems
- GPS receivers
- Backpack radios
- Anti-IED jamming systems
- Autonomous sensor networks
- Unmanned vehicles

\*The Chip Scale Atomic Clock is not tested, qualified, and rated for space applications.

With an extremely low power consumption of <120 mW and a volume of <17 cc, the Microsemi® SA.45s Chip Scale Atomic Clock (CSAC) brings the accuracy and stability of an atomic clock to portable applications for the first time.

The SA.45s provides 10 MHz and 1 PPS outputs at standard CMOS levels, with short-term stability (Allan Deviation) of 2.5E-10 @ TAU =1 sec, long-term aging of <9E-10/month, and maximum frequency change of 5E-10 over an operating temperature range of -10°C to +35°C. The SA.45s CSAC accepts a 1 PPS input that may be used to synchronize the unit's 1 PPS output to an external reference clock with  $\pm 100$  ns accuracy. The CSAC can also use the 1 PPS input to discipline its phase and frequency to within 1 ns and 1.0E-12, respectively.

A standard CMOS-level RS-232 serial interface is built in to the SA.45s. This is used to control and calibrate the unit and also to provide a comprehensive set of status monitors. The interface is also used to set and read the CSAC's internal timeof-day clock.



Microsemi invented portable atomic timekeeping with QUANTUM<sup>™</sup>, the world's first family of miniature and chip scale atomic clocks.

Choose QUANTUM<sup>™</sup> class for best-in-class stability, Size, Weight and Power consumption (SWAP).





## Quantum<sup>™</sup> SA.45s CSAC

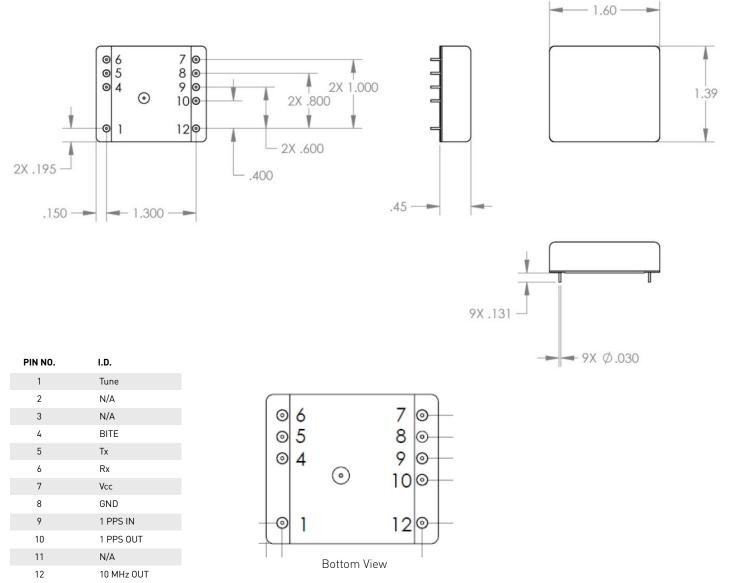
### **Options to Meet a Wider Range of Applications**

The standard SA.45s CSAC option 001 provides an output frequency of 10MHz. However, other output frequencies are available: option 003 provides 16.384 MHz, and option 004 provides 10.24 MHz and option 006 provides a 5 MHz output.

For other output frequencies please contact Microsemi for details.

The Chip Scale Atomic Clock is not tested, qualified, and rated for space applications.

### **Mechanical Interface**





Power Matters."

## Quantum<sup>™</sup> SA.45s CSAC Option 001

## Part number 090-00218-001

## **Specifications**

All specifications at 25°C, Vcc =3.3V DC unless otherwise specified

#### **ELECTRICAL SPECIFICATIONS**

#### **RF** Output

- Frequency:	10 MHz
- Format:	CMOS
- Amplitude:	OV to Vcc
- Load impedance:	1 MΩ
- Quantity:	1

#### 1 PPS Output

• • • •	
<ul> <li>Rise/fall time (10%-90%) at load capacitance 10pF:</li> </ul>	<10 ns
- Pulse width:	100 µs
- Level:	OV to Vcc
- Logic High (VoH) min:	2.80 V
- Logic Low (VoL) max:	0.30 V
- Load impedance:	1 MΩ
- Quantity:	1

### 1 PPS Input

Format:	Rising edge
Low level:	<0.5 V
High level:	2.5 V to Vcc
Input impedance:	1 MΩ
Quantity:	1

#### Serial Communications

- Protocol:	RS232
- Format:	CMOS OV to Vcc
- Tx/Rx impedance:	1 ΜΩ
- Baud rate:	57600

#### Built-in Test Equipment (BITE) output

- Format:	CMOS OV to Vcc
<ul> <li>Load impedance:</li> </ul>	1 ΜΩ
- Logic:	0 = Normal operation
	1 = Alarm

#### **Power Input**

- Operating:	<120 mW
- Warmup:	<140 mW
- Input voltage (Vcc):	3.3 ± 0.1 VDC

#### PHYSICAL SPECIFICATIONS

- Size:	1.6" x 1.39" x 0.45"
- Weight:	<35 g
- MTBF:	>100,000 hours

#### **ENVIRONMENTAL SPECIFICATIONS**

Operating:	
Operating	to

- Operating temperature:	-10°C to +35°C
- Maximum frequency change over operating temp range (max. rate of change 0.5 °C/minute):	±5x10 <sup>-10</sup>
<ul> <li>Frequency change over allowable input voltage range:</li> </ul>	±4x10 <sup>-10</sup>

±4x10-10

#### ENVIRONMENTAL SPECIFICATIONS (Continued)

- Magnetic sensitivity (≼2.0 Gauss):	±9x10 <sup>-11</sup> /Gauss
- Radiated emissions.	Compliant to FCC part 15, Class B, when mounted properly onto host PCB.
- Vibration:	Maintains lock under MIL-STD-810, Method 514.5, Procedure 1, 7.7 grms
- Humidity:	0 to 95% RH per MIL-STD-810, Method 507.4.
Storage and Transport (non-o	perating): -55°C to +40°C
- Temperature:	
- Shock (1 ms half-sine):	1000 g
- Vibration:	MIL-STD-810, Method 514.5, Procedure 1, 7.7 grms
PERFORMANCE PARAM	ETERS
Stability (Allan Deviation)	
ADEV	
TAU = 1 sec	2.5x10 <sup>-10</sup>
TAU = 10 sec	8x10 <sup>-11</sup>
TAU = 100 sec	2.5x10 <sup>-11</sup>
TAU = 1000 sec	8x10 <sup>-12</sup>
RF Output Phase Noise (SSB)	

1 Hz	<-50 dBc/Hz
10 Hz	<-70 dBc/Hz
100 Hz	<-113 dBc/Hz
1000 Hz	<-128 dBc/Hz
10000 Hz	<-135 dBc/Hz
100,000 Hz	<-140 dBc/Hz

#### **Frequency Accuracy**

- Maximum offset at shipment:	±5x10 <sup>-11</sup>
- Maximum retrace (48 hrs off):	±5x10 <sup>-10</sup>
- Aging, monthly*:	<9x10 <sup>-10</sup> typical
- Aging, yearly*:	<1x10 <sup>-8</sup> typical
- 1 PPS Sync.:	±100 ns
(*After 20 days of continuous energies	-)

(\*After 30 days of continuous operation)

#### **Digital Tuning**

- Range:	±2x10 <sup>-8</sup>
- Resolution:	1x10 <sup>-12</sup>

#### **Analog Tuning** - Rango

Warm-up Time	<180 s
- Input:	0-2.5V into 100 kΩ
- Resolution:	1x10 <sup>-11</sup>
- Range:	±2.2x10 <sup>-8</sup>

#### Solder

Hand solder using 63/37 Tin/Lead Solder with maximum soldering tip of 329°C (625°F)

±9x10<sup>-11</sup>/Gauss

Compliant to FCC part 15, Class B, when mounted properly onto host PCB

Maintains lock under MIL-STD-810, method 514.5, procedure 1, 7.7 grms

0 to 95% RH per MIL-

-55°C to +40°C

1000 g

STD-810, method 507.4

MIL-STD-810, method 514.5, procedure 1, 7.7 grms



## Quantum<sup>™</sup> SA.45s CSAC Option 003

Part number 090-00218-003

## **Specifications**

voltage range:

All specifications at 25°C, Vcc = 3.3V DC unless otherwise specified

### ELECTRICAL SPECIFICATIONS

ELECTRICAL SPECIFICATIONS	
RF Output	
- Frequency:	16.384 MHz
- Format:	CMOS
- Amplitude:	OV to Vcc
- Load impedance:	1 MΩ
- Quantity:	1
1 PPS Output	
- Rise/fall time (10%-90%) at load capacitance 10pF:	<10 ns
- Pulse width:	97.656 µs
- Level:	0V to Vcc
- Logic High (VOH) min:	2.80 V
- Logic Low (VOL) max:	0.30 V
- Load impedance:	1 MΩ
- Quantity:	1
	I
1 PPS Input	<b>D</b>
- Format:	Rising edge
- Low level:	<0.5 V
- High level:	2.5 V to Vcc
- Input impedance:	1 MΩ 1
- Quantity:	I
Serial Communications	
- Protocol:	RS-232
- Format:	CMOS OV to Vcc
- Tx/Rx impedance:	1 MΩ
- Baud rate:	57600
Built-in Test Equipment (BITE) output	
- Format:	CMOS OV to Vcc
- Load impedance:	1 ΜΩ
- Logic:	0 = Normal operation 1 = Alarm
Power Input	100
- Operating:	<120 mW
- Warmup:	<140 mW
- Input Voltage (Vcc):	3.3 ± 0.1 VDC
PHYSICAL SPECIFICATIONS	
- Size:	1.6" x 1.39" x 0.45"
- Weight:	<35 g
- MTBF:	>100,000 hours
ENVIRONMENTAL SPECIFICATIONS	
Operating:	
- Operating temperature:	-10°C to +35°C
- Maximum frequency change	
over operating temp range	
(max. rate of change 0.5°C/minute):	±5x10 <sup>-10</sup>
- Frequency change over	
allowable input	
sealthe mean and	. /10-10

±4x10-10

#### ENVIRONMENTAL SPECIFICATIONS (Continued)

- Magnetic sensitivity
- (≼2.0 Gauss): - Radiated emissions:

- Vibration:

- Humidity:

#### Storage and Transport (non-operating):

- Temperature:
  - Shock (1 ms half-sine):
  - Vibration:

#### PERFORMANCE PARAMETERS

#### Stability (Allan Deviation)

ADEV	
TAU = 1 sec	2.5x10 <sup>-10</sup>
TAU = 10 sec	8x10 <sup>-11</sup>
TAU = 100 sec	2.5x10 <sup>-11</sup>
TAU = 1000 sec	8x10 <sup>-12</sup>

#### RF Output Phase Noise (SSB)

1 Hz	<-46 dBc/Hz
10 Hz	<-66 dBc/Hz
100 Hz	<-110 dBc/Hz
1000 Hz	<-128 dBc/Hz
10000 Hz	<-135 dBc/Hz
100,000 Hz	<-140 dBc/Hz

#### Frequency Accuracy

- Maximum offset at shipment:	±5x10 <sup>-11</sup>
- Maximum retrace (48 hrs off):	±5x10 <sup>-10</sup>
- Aging, monthly*:	<9x10 <sup>-10</sup> typical
- Aging, yearly*:	<1x10 <sup>-8</sup> typical
- 1 PPS Sync.:	±100 ns
(*After 30 days of continuous operation)	

#### **Digital Tuning**

- Range:	±2x10-8
- Resolution:	1x10 <sup>-12</sup>
Analog Tuning	
- Range:	±2.2x10-8
- Resolution:	1x10 <sup>-11</sup>
- Input:	0-2.5V into 100 kΩ

#### Solder

Warm-up Time

Hand solder using 63/37 Tin/Lead Solder with maximum soldering tip of 329°C (625°F)

<180 s

±9x10<sup>-11</sup>/Gauss

Compliant to FCC part 15, Class B, when mounted properly onto host PCB

Maintains lock under MIL-STD-810, method 514.5, procedure 1, 7.7 grms 0 to 95% RH per MIL-

STD-810, method 507.4

MIL-STD-810, method

514.5, procedure 1, 7.7 grms

-55°C to +40°C

1000 g



Power Matters."

## Quantum<sup>™</sup> SA.45s CSAC Option 004

Part number 090-00218-004

## **Specifications**

All specifications at 25°C, Vcc = 3.3V DC unless otherwise specified

### ELECTRICAL SPECIFICATIONS

RF Output	10.24 MHz
- Frequency: - Format:	CMOS
- Amplitude:	OV to Vcc
	1 ΜΩ
- Load impedance:	1
- Quantity:	1
1 PPS Output	
- Rise/fall time (10%-90%)	<10 ns
at load capacitance 10pF: - Pulse width:	100 µs
- Level:	OV to Vcc
- Level: - Logic High (VOH) min:	2.80 V
	0.30 V
- Logic Low (VOL) max: - Load impedance:	0.30 V 1 MΩ
•	1
- Quantity:	1
1 PPS Input	
- Format:	Rising edge
- Low level:	<0.5 V
- High level:	2.5 V to Vcc
- Input impedance:	1 ΜΩ
- Quantity:	1
Serial Communications	
- Protocol:	RS-232
- Format:	CMOS 0V to Vcc
- Tx/Rx impedance:	1 ΜΩ
- Baud rate:	57600
Built-in Test Equipment (BITE) output	
- Format:	CMOS 0V to Vcc
- Load impedance:	1 ΜΩ
- Logic:	0 = Normal operation
-	1 = Alarm
Power Input	
- Operating:	<120 mW
- Warmup:	<140 mW
- Input Voltage (Vcc):	3.3 ± 0.1 VDC
PHYSICAL SPECIFICATIONS	
- Size:	1.6" x 1.39" x 0.45"
- Weight: - MTBF:	<35 g >100,000 hours
	>100,000 110015
ENVIRONMENTAL SPECIFICATIONS	
Operating:	1000 . 0500
- Operating temperature:	-10°C to +35°C
<ul> <li>Maximum frequency change over operating temp range</li> </ul>	
(max. rate of change	
0.5°C/minute):	±5x10 <sup>-10</sup>
- Frequency change over	
allowable input	. (10-10
voltage range:	±4x10 <sup>-10</sup>

## ENVIRONMENTAL SPECIFICATIONS (Continued)

<ul> <li>ENVIRONMENTAL SPEC</li> <li>Magnetic sensitivity (&lt;2.0 Gauss):</li> <li>Radiated emissions:</li> </ul>	IFICATIONS
- Vibration:	
- Humidity:	
Storage and Transport (non-og - Temperature: - Shock (1 ms half-sine): - Vibration:	perating):
PERFORMANCE PARAM	ETERS
Stability (Allan Deviation) ADEV TAU = 1 sec TAU = 10 sec TAU = 100 sec TAU = 1000 sec	2.5x10 <sup>-10</sup> 8x10 <sup>-11</sup> 2.5x10 <sup>-11</sup> 8x10 <sup>-12</sup>

#### RF Output Phase Noise (SSB)

1 Hz	<-50 dBc/Hz
10 Hz	<-70 dBc/Hz
100 Hz	<-113 dBc/Hz
1000 Hz	<-128 dBc/Hz
10000 Hz	<-135 dBc/Hz
100.000 Hz	<-140 dBc/Hz

#### **Frequency Accuracy**

- Maximum offset at shipment:	±5x10 <sup>-11</sup>	
- Maximum retrace (48 hrs off):	±5x10 <sup>-10</sup>	
- Aging, monthly*:	<9x10 <sup>-10</sup> typical	
- Aging, yearly*:	<1x10 <sup>-8</sup> typical	
- 1 PPS Sync.: ±100 ns		
(*After 30 days of continuous operation)		

#### 

Digital luning	
- Range:	±2x10-8
- Resolution:	1x10 <sup>-12</sup>
<del>.</del> .	
Analog Tuning	
- Range:	±2.2x10-8
- Resolution:	1x10 <sup>-11</sup>
- Input:	0-2.5V into 100 kΩ
Warm-up Time	<180 s

#### Solder

Hand solder using 63/37 Tin/Lead Solder with maximum soldering tip of 329°C (625°F)



## Quantum<sup>™</sup> SA.45s CSAC Option 006

## Part number 090-00218-006

### **Specifications**

All specifications at 25°C, Vcc = 3.3V DC unless otherwise specified

### **ELECTRICAL SPECIFICATIONS**

RF Outptut	
- Frequency:	5 MHz
- Format:	CMOS
- Amplitude:	OV to Vcc
- Load impedance:	1 ΜΩ
- Quantity:	1
1 PPS Output	
- Rise/fall time (10%-90%)	
at load capacitance 10pF:	<10 ns
- Pulse width:	100 µs
- Level:	OV to Vcc
- Logic High (VOH) min:	2.80 V
- Logic Low (VOL) max:	0.30 V
- Load impedance:	1 ΜΩ
- Quantity:	1
1 PPS Input	
- Format:	Rising edge
- Low level:	<0.5 V
- High level:	2.5 V to Vcc
- Input impedance:	1 ΜΩ
- Quantity:	1
Serial Communications	
- Protocol:	RS-232
- Format:	CMOS OV to Vcc
- Tx/Rx impedance:	1 ΜΩ
- Baud rate:	57600
Built-in Test Equipment (BITE) output	
- Format:	CMOS 0V to Vcc
- Load impedance:	1 ΜΩ
- Logic:	0 = Normal operation 1 = Alarm
Power Input	
- Operating:	<120 mW
- Warmup:	<140 mW
- Input Voltage (Vcc):	3.3 ± 0.1 VDC
PHYSICAL SPECIFICATIONS	
- Size:	1.6" x 1.39" x 0.45"
- Weight:	<35 q
- MTBF:	>100,000 hours
ENVIRONMENTAL SPECIFICATIONS	
Operating:	
- Operating temperature:	-10°C to +35°C
- Maximum frequency change	
over operating temp range	
(max. rate of change 0.5°C/minute):	±5x10 <sup>-10</sup>
	IJXIU
- Frequency change over allowable input	
voltage range:	±4x10 <sup>-10</sup>

#### ENVIRONMENTAL SPECIFICATIONS (Continued)

- Magnetic sensitivity
(≤2.0 Gauss):
- Radiated emissions:

- Vibration:

- Humidity:

#### Storage and Transport (non-operating):

- Temperature:
- Shock (1 ms half-sine):
- Vibration:

#### PERFORMANCE PARAMETERS

#### Stability (Allan Deviation)

ADEV	
TAU = 1 sec	2.5x10 <sup>-10</sup>
TAU = 10 sec	8x10 <sup>-11</sup>
TAU = 100 sec	2.5x10 <sup>-11</sup>
TAU = 1000 sec	8x10 <sup>-12</sup>

#### RF Output Phase Noise (SSB)

1 Hz	<-53 dBc/Hz
10 Hz	<-73 dBc/Hz
100 Hz	<-116 dBc/Hz
1000 Hz	<-131 dBc/Hz
10000 Hz	<-138 dBc/Hz
100,000 Hz	<-140 dBc/Hz

#### Frequency Accuracy

- Maximum offset at shipment:	±5x10 <sup>-11</sup>	
- Maximum retrace (48 hrs off):	±5x10 <sup>-10</sup>	
- Aging, monthly*:	<9x10 <sup>-10</sup> typical	
- Aging, yearly*:	<1x10 <sup>-8</sup> typical	
- 1 PPS Sync.:	±100 ns	
(*After 30 days of continuous operation)		

#### **Digital Tuning**

Warm-up Time	<180 s
Analog Tuning - Range: - Resolution: - Input:	±2.2x10 <sup>-8</sup> 1x10 <sup>-11</sup> 0-2.5V into 100 kΩ
- Range: - Resolution:	±2x10 <sup>-8</sup> 1x10 <sup>-12</sup>

#### Solder

Hand solder using 63/37 Tin/Lead Solder with maximum soldering tip of 329°C (625°F)

±9x10<sup>-11</sup>/Gauss Compliant to FCC part 15, Class B, when mounted properly onto host PCB Maintains lock under MIL-STD-810, method 514.5, procedure 1, 7.7 grms 0 to 95% RH per MIL-STD-810, method 507.4

-55°C to +40°C 1000 g MIL-STD-810, method 514.5, procedure 1, 7.7 grms



**Microsemi Corporate Headquarters** One Enterprise, Aliso Viejo, CA 92656 USA

Within the USA: +1 (800) 713-4113 Outside the USA: +1 (949) 380-6100 Sales: +1 (949) 380-6136 Fax: +1 (949) 215-4996

#### E-mail: sales.support@microsemi.com

© 2015 Microsemi Corporation. All rights reserved. Microsemi and the Microsemi logo are trademarks of Microsemi Corporation. All other trademarks and service marks are the property of their respective owners. Microsemi Corporation (Nasdaq: MSCC) offers a comprehensive portfolio of semiconductor and system solutions for communications, defense & security, aerospace and industrial markets. Products include high-performance and radiation-hardened analog mixed-signal integrated circuits, FPGAs, SoCs and ASICs; power management products; timing and synchronization devices and precise time solutions, setting the world's standard for time; voice processing devices; RF solutions; discrete components; security technologies and scalable anti-tamper products; Ethernet Solutions; Power-over-Ethernet ICs and midspans; as well as custom design capabilities and services. Microsemi is headquartered in Aliso Viejo, Calif., and has approximately 3,600 employees globally. Learn more at www.microsemi.com.

Microsemi makes no warranty, representation, or guarantee regarding the information contained herein or the suitability of its products and services for any particular purpose, nor does Microsemi assume any liability whatsoever arising out of the application or use of any product or circuit. The products sold hereunder and any other products sold by Microsemi have been subject to limited testing and should not be used in conjunction with mission-critical equipment or applications. Any performance specifications are believed to be reliable but are not verified, and Buyer must conduct and complete all performance and other testing of the products, alone and together with, or installed in, any end-products. Buyer shall not rely on any data and performance specifications or parameters provided by Microsemi. It is the Buyer's responsibility to independently determine suitability of any products and to test and verify the same. The information provided by Microsemi hereunder is provided "as is, where is" and with all faults, and the entire risk associated with such information is entirely with the Buyer. Microsemi does not grant, explicitly or implicitly, to any party any patent rights, licenses, or any other IP rights, whether with regard to such information itself or anything described by such information. Information provided in this document is proprietary to Microsemi, and Microsemi reserves the right to make any changes to the information in this document or to any products and services at any time without notice.