# imall

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



### Contact us

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## C0510X5R1C104M030AC TDK Item Description : C0510X5R1C104MT\*\*\*\*

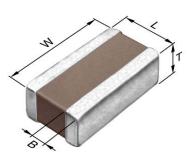


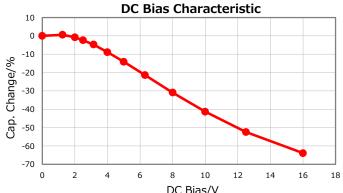
#### Application & Main Feature

Commercial Grade ( Low ESL Reverse Geometry )

#### Series

C0510 [EIA 0204]





Dimensions		DC Bias/V
L 0.52mm +/-0.05mm W 1.00mm +/-0.05mm T 0.30mm +/-0.05mm B 0.10mm Min.	IZI, ESR vs. Freq. Characteristics	Temperature Characteristics
<ul> <li>Temperature Characteristic X5R (-55 to 85 degC +/-15%)</li> <li>Rated Voltage 1C (16Vdc)</li> </ul>	Uppuer 000 10 10 10 10 10 10 10 10 10	2-10 2-20 3-30 4-40 5-50 6-60 -70 -80
Capacitance 100nF Capacitance Tolerance	0.001 0.001 0.01 0.1 1 10 100 1000 Frequency/MHz  Z  ESR	-90 -80 -60 -40 -20 0 20 40 60 80 100 Temperature/degC Temp.Chara. TCBias(1/2Rated)
M (+/-20%)	10000 Cap. vs. Freq. Characteristic	<sup>25</sup> Ripple Temperature Rise Characteristics
Dissipation Factor 5% Max.	1000	<sup>h</sup> 20
5% Max. Insulation Resistance 1Gohm Min	Cap./Jr Cap./Jr 000 000 000 000 000 000	20 15 10
Insulation Resistance	<sup>1000</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup> <sup>100</sup>	D 20 15 10 5 0 0 0 0 0 0 0 0 0 0 0 0 0