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Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

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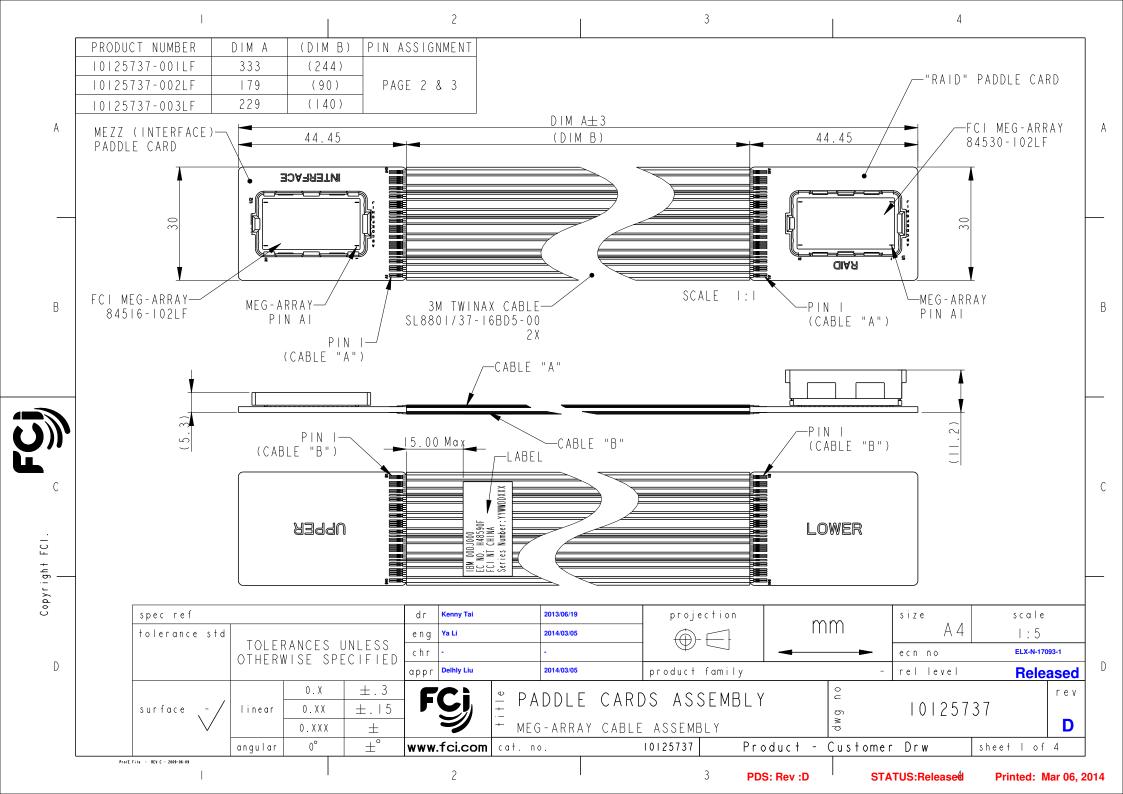
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| AID TwinAx Lower P1 A1 A2 A3 A4 A5 A6 A7 A8 A9 A10 A11 A12 A13 A14 A15 A16 A17 A18 A19 A22 A23 | "A" Cable (same side as MegArray) Pin Description HS_PCIE16 RAID_TX_IF_RX_3_N HS_PCIE16 RAID_TX_IF_RX_3_P SE_12V0 SE_GND LS_RAID_IF_DEVICE_DIS_1_N HS_PCIE16 RAID_TX_IF_RX_2_P GND HS_PCIE16 RAID_TX_IF_RX_2_P GND HS_PCIE16 RAID_TX_IF_RX_1_P SE_GND HS_PCIE16 RAID_TX_IF_RX_1_P SE_GND HS_PCIE16 RAID_TX_IF_RX_0_N HS_PCIE16 RAID_TX_IF_RX_0_P LS_IF_RAID_PRESENCE_OUT_N LS_RAID_IF_ALL_PGOOD LS_IF_RAID_PGOOD | Upper P2 A1 A2 A3 A4 A5 A6 A7 A8 A9 A10 A11 A12 A13 A14 A15 A16 | A5 B5 Plane Shape Plane Shape 117 C6 D6 Plane Shape A7 B7 Plane Shape Plane Shape Plane Shape Plane Shape | RAID MegArray A3 B3 Plane Shape Plane Shape G2 C4 D4 Plane Shape A5 B5 Plane Shape Plane Shape G1 | Lower P1 B1 B2 B3 B4 B5 B6 B7 B8 B9 B10 B11 | "B" Cable Pin Description HS_PCIE16_RAID_TX_IF_RX_7_N HS_PCIE16_RAID_TX_IF_RX_7_P SE_12V0 SE_GND LS_RAID_IF_PCIE_RESET_N HS_PCIE16_RAID_TX_IF_RX_6_P GND HS_PCIE16_RAID_TX_IF_RX_6_P GND HS_PCIE16_RAID_TX_IF_RX_5_N HS_PCIE16_RAID_TX_IF_RX_5_P SE_GND | B2 B3 B4 B5 B6 B7 B8 B9 | A1 B1 Plane Shape Plane Shape G19 C2 D2 Plane Shape A3 |
|--|--|---|---|--|---|--|--|---|
| P1 A1 A2 A3 A4 A5 A6 A7 A8 A9 A10 A11 A12 A13 A14 A15 A16 A17 A18 A19 A20 A21 A22 | HS_PCIE16_RAID_TX_IF_RX_3_N HS_PCIE16_RAID_TX_IF_RX_3_P SE_12V0 SE_GND LS_RAID_IF_DEVICE_DIS_1_N HS_PCIE16_RAID_TX_IF_RX_2_N HS_PCIE16_RAID_TX_IF_RX_2_P GND HS_PCIE16_RAID_TX_IF_RX_1_P SE_GND SE_12V0 SE_12V | P2 A1 A2 A3 A4 A5 A6 A7 A8 A9 A10 A11 A12 A13 A14 A15 A16 | B5 Plane Shape Plane Shape 117 C6 D6 Plane Shape A7 B7 Plane Shape | B3 Plane Shape G2 C4 D4 Plane Shape A5 B5 Plane Shape Plane Shape Plane Shape | P1 B1 B2 B3 B4 B5 B6 B7 B8 B7 B8 B9 B10 B11 | HS_PCIE16_RAID_TX_IF_RX_7_N HS_PCIE16_RAID_TX_IF_RX_7_P SE_12V0 SE_GND LS_RAID_IF_PCIE_RESET_N HS_PCIE16_RAID_TX_IF_RX_6_N HS_PCIE16_RAID_TX_IF_RX_6_N HS_PCIE16_RAID_TX_IF_RX_5_N HS_PCIE16_RAID_TX_IF_RX_5_P | P2 I B1 B2 B3 B4 B5 B5 B6 B7 B8 B8 B9 | B1 Plane Shape G19 C2 D2 Plane Shape A3 |
| A1 A2 A3 A4 A5 A6 A7 A8 A9 A10 A11 A12 A13 A14 A15 A14 A15 A16 A17 A18 A19 A20 A21 A22 | HS_PCIE16_RAID_TX_IF_RX_3_N HS_PCIE16_RAID_TX_IF_RX_3_P SE_12V0 SE_GND LS_RAID_IF_DEVICE_DIS_1_N HS_PCIE16_RAID_TX_IF_RX_2_N HS_PCIE16_RAID_TX_IF_RX_2_P GND HS_PCIE16_RAID_TX_IF_RX_1_P SE_GND SE_12V0 SE_12V | A1 A2 A3 A4 A5 A6 A7 A8 A9 A10 A11 A11 A12 A13 A14 A15 A16 | B5 Plane Shape Plane Shape 117 C6 D6 Plane Shape A7 B7 Plane Shape | B3 Plane Shape G2 C4 D4 Plane Shape A5 B5 Plane Shape Plane Shape Plane Shape | B1 B2 B3 B4 B5 B6 B7 B8 B8 B9 B10 B11 | HS_PCIE16_RAID_TX_IF_RX_7_N HS_PCIE16_RAID_TX_IF_RX_7_P SE_12V0 SE_GND LS_RAID_IF_PCIE_RESET_N HS_PCIE16_RAID_TX_IF_RX_6_N HS_PCIE16_RAID_TX_IF_RX_6_N HS_PCIE16_RAID_TX_IF_RX_5_N HS_PCIE16_RAID_TX_IF_RX_5_P | B1 B2 B3 B4 B5 B6 B7 B8 B9 | B1 Plane Shape G19 C2 D2 Plane Shape A3 |
| A2 A3 A4 A5 A6 A7 A8 A9 A10 A11 A12 A13 A14 A15 A16 A17 A18 A19 A20 A21 A22 | HS_PCIE16 RAID_TX_IF_RX_3_P SE_12V0 SE_GND LS_RAID_IF_DEVICE_DIS_1_N HS_PCIE16_RAID_TX_IF_RX_2_N HS_PCIE16_RAID_TX_IF_RX_2_P GND HS_PCIE16_RAID_TX_IF_RX_1_P SE_GND SE_12V0 SE | A2 A3 A4 A5 A6 A7 A8 A9 A10 A11 A12 A13 A14 A15 A16 | B5 Plane Shape Plane Shape 117 C6 D6 Plane Shape A7 B7 Plane Shape | B3 Plane Shape G2 C4 D4 Plane Shape A5 B5 Plane Shape Plane Shape Plane Shape | B2 B3 B4 B5 B6 B7 B8 B9 B10 B11 | HS_PCIE16_RAID_TX_IF_RX_7_P SE_12V0 SE_GND LS_RAID_IF_PCIE_RESET_N HS_PCIE16_RAID_TX_IF_RX_6_N HS_PCIE16_RAID_TX_IF_RX_6_P GND HS_PCIE16_RAID_TX_IF_RX_5_N HS_PCIE16_RAID_TX_IF_RX_5_P | B2 B3 B4 B5 B6 B7 B8 B9 | B1 Plane Shape G19 C2 D2 Plane Shape A3 |
| A3 A4 A5 A6 A7 A8 A9 A10 A11 A12 A13 A14 A15 A16 A17 A18 A19 A20 A21 | SE_12V0 SE_GND LS_RAID_IF_DEVICE_DIS_1_N HS_PCIE16_RAID_TX_IF_RX_2_N HS_PCIE16_RAID_TX_IF_RX_2_P GND HS_PCIE16_RAID_TX_IF_RX_1_N HS_PCIE16_RAID_TX_IF_RX_1_P SE_GND SE_12V0 SE_12V0 SE_12V0 SE_12V0 SE_GND HS_PCIE16_RAID_TX_IF_RX_0_N HS_PCIE16_RAID_TX_IF_RX_0_P LS_IF_RAID_PRESENCE_OUT_N LS_RAID_IF_ALL_PGOOD LS_IF_RAID_PGOOD | A3 A4 A5 A6 A7 A8 A9 A10 A11 A12 A13 A14 A15 A16 | Plane Shape Plane Shape 117 C6 D6 Plane Shape A7 B7 Plane Shape | Plane Shape Plane Shape G2 C4 D4 Plane Shape A5 B5 Plane Shape Plane Shape | B3 B4 B5 B6 B7 B8 B9 B10 B11 | SE_12V0 SE_GND LS_RAID_IF_PCIE_RESET_N HS_PCIE16_RAID_TX_IF_RX_6_N HS_PCIE16_RAID_TX_IF_RX_6_P GND HS_PCIE16_RAID_TX_IF_RX_5_N HS_PCIE16_RAID_TX_IF_RX_5_P | B3 B4 B5 B6 B7 B7 B8 B8 I B9 | Plane Shape Plane Shape G19 C2 D2 Plane Shape A3 |
| A4 A5 A6 A7 A8 A9 A10 A11 A12 A13 A14 A15 A16 A17 A18 A19 A20 A21 | SE_GND LS_RAID_IF_DEVICE_DIS_1_N HS_PCIE16_RAID_TX_IF_RX_2_N HS_PCIE16_RAID_TX_IF_RX_2_P GND HS_PCIE16_RAID_TX_IF_RX_1_N HS_PCIE16_RAID_TX_IF_RX_1_P SE_GND SE_12V0 SE_12V0 SE_12V0 SE_GND HS_PCIE16_RAID_TX_IF_RX_0_N HS_PCIE16_RAID_TX_IF_RX_0_P LS_IF_RAID_PRESENCE_OUT_N LS_RAID_IF_ALL_PGOOD LS_IF_RAID_PGOOD | A4 A5 A6 A7 A8 A9 A10 A11 A12 A13 A14 A15 A16 | Plane Shape 117 C6 D6 Plane Shape A7 B7 Plane Shape Plane Shape Plane Shape Plane Shape Plane Shape | Plane Shape G2 C4 Plane Shape A5 B5 Plane Shape Plane Shape | B4 B5 B6 B7 B8 B9 B10 B11 | SE_GND LS_RAID_IF_PCIE_RESET_N HS_PCIE16_RAID_TX_IF_RX_6_N HS_PCIE16_RAID_TX_IF_RX_6_P GND HS_PCIE16_RAID_TX_IF_RX_5_N HS_PCIE16_RAID_TX_IF_RX_5_P | B4 B5 B6 B7 B7 B8 B8 I B9 | Plane Shape G19 C2 D2 Plane Shape A3 |
| A5 A6 A7 A8 A9 A10 A11 A12 A13 A14 A15 A16 A17 A18 A19 A20 A21 | LS_RAID_IF_DEVICE_DIS_1_N HS_PCIE16_RAID_TX_IF_RX_2_N HS_PCIE16_RAID_TX_IF_RX_2_P GND HS_PCIE16_RAID_TX_IF_RX_1_N HS_PCIE16_RAID_TX_IF_RX_1_P SE_GND SE_12V0 SE_12V0 SE_12V0 SE_GND HS_PCIE16_RAID_TX_IF_RX_0_N HS_PCIE16_RAID_TX_IF_RX_0_P LS_IF_RAID_PRESENCE_OUT_N LS_RAID_IF_ALL_PGOOD LS_IF_RAID_PGOOD | A5 A6 A7 A8 A9 A10 A11 A12 A13 A14 A15 A16 | I17 C6 D6 Plane Shape A7 B7 Plane Shape | G2 C4 D4 Plane Shape A5 B5 Plane Shape Plane Shape | B5 B6 B7 B8 B9 B10 B11 | LS_RAID_IF_PCIE_RESET_N HS_PCIE16_RAID_TX_IF_RX_6_N HS_PCIE16_RAID_TX_IF_RX_6_P GND HS_PCIE16_RAID_TX_IF_RX_5_N HS_PCIE16_RAID_TX_IF_RX_5_P | B5 B6 B7 B8 B8 J B9 | G19 C2 D2 Plane Shape A3 |
| A6 A7 A8 A9 A10 A11 A12 A13 A14 A15 A16 A17 A18 A19 A20 A21 | HS_PCIE16_RAID_TX_IF_RX_2_N HS_PCIE16_RAID_TX_IF_RX_2_P GND HS_PCIE16_RAID_TX_IF_RX_2_P HS_PCIE16_RAID_TX_IF_RX_1_N HS_PCIE16_RAID_TX_IF_RX_1_P SE_GND SE_12V0 SE_12V0 SE_GND HS_PCIE16_RAID_TX_IF_RX_0_N HS_PCIE16_RAID_TX_IF_RX_0_P LS_IF_RAID_PRESENCE_OUT_N LS_RAID_IF_ALL_PGOOD LS_IF_RAID_PGOOD | A6 A7 A8 A9 A10 A11 A12 A13 A14 A15 A16 | C6 D6 Plane Shape A7 B7 Plane Shape Plane Shape Plane Shape Plane Shape | C4 D4 Plane Shape A5 B5 Plane Shape Plane Shape | B6 B7 B8 B9 B10 B11 | HS PCIE16 RAID TX IF RX 6 N HS PCIE16 RAID TX IF RX 6 P GND HS PCIE16 RAID TX IF RX 5 N HS PCIE16 RAID TX IF RX 5 P | I B6 B7 B8 I B9 | C2 D2 Plane Shape A3 |
| A7 A8 A9 A10 A11 A12 A13 A14 A15 A14 A15 A16 A17 A18 A19 A20 A21 A22 | HS_PCIE16_RAID_TX_IF_RX_2_P GND HS_PCIE16_RAID_TX_IF_RX_1_N HS_PCIE16_RAID_TX_IF_RX_1_P SE_GND SE_12V0 SE_12V0 SE_12V0 SE_GND HS_PCIE16_RAID_TX_IF_RX_0_N HS_PCIE16_RAID_TX_IF_RX_0_P LS_IF_RAID_PRESENCE_OUT_N LS_RAID_IF_ALL_PGOOD LS_IF_RAID_PGOOD | A7 A8 A9 A10 A11 A12 A13 A14 A15 A16 | D6 Plane Shape A7 B7 Plane Shape Plane Shape Plane Shape Plane Shape | D4 Plane Shape A5 B5 Plane Shape Plane Shape | B7 B8 B9 B10 B11 | HS_PCIE16_RAID_TX_IF_RX_6_P GND HS_PCIE16_RAID_TX_IF_RX_5_N HS_PCIE16_RAID_TX_IF_RX_5_P | B7 B8 B8 B9 | D2 Plane Shape A3 |
| A8 A9 A10 A11 A12 A13 A14 A15 A16 A17 A18 A19 A20 A21 | GND HS_PCIE16_RAID_TX_IF_RX_1_N HS_PCIE16_RAID_TX_IF_RX_1_P SE_GND SE_12V0 SE_GND HS_PCIE16_RAID_TX_IF_RX_0_N HS_PCIE16_RAID_TX_IF_RX_0_P LS_IF_RAID_PRESENCE_OUT_N LS_RAID_IF_ALL_PGOOD LS_IF_RAID_PGOOD | A8 A9 A10 A11 A12 A13 A14 A15 A16 | Plane Shape A7 B7 Plane Shape Plane Shape Plane Shape Plane Shape | Plane Shape A5 B5 Plane Shape Plane Shape | B8 B9 B10 B11 | GND HS_PCIE16_RAID_TX_IF_RX_5_N HS_PCIE16_RAID_TX_IF_RX_5_P | B8 I B9 | Plane Shape A3 |
| A9 A10 A11 A12 A13 A14 A15 A16 A17 A18 A19 A20 A21 | HS_PCIE16_RAID_TX_IF_RX_1_N HS_PCIE16_RAID_TX_IF_RX_1_P SE_GND SE_12V0 SE_12V0 SE_GND HS_PCIE16_RAID_TX_IF_RX_0_N HS_PCIE16_RAID_TX_IF_RX_0_P LS_IF_RAID_PRESENCE_OUT_N LS_RAID_IF_ALL_PGOOD LS_IF_RAID_PGOOD | A9 A10 A11 A12 A13 A14 A15 A16 | A7 B7 Plane Shape Plane Shape Plane Shape Plane Shape | A5 B5 Plane Shape Plane Shape | B9 B10 B11 | HS_PCIE16_RAID_TX_IF_RX_5_N HS_PCIE16_RAID_TX_IF_RX_5_P | I B9 | A3 |
| A10 A11 A12 A13 A14 A15 A16 A16 A17 A18 A19 A20 A21 A22 | HS_PCIE16_RAID_TX_IF_RX_1_P SE_GND SE_12V0 SE_12V0 SE_GND HS_PCIE16_RAID_TX_IF_RX_0_N HS_PCIE16_RAID_TX_IF_RX_0_P LS_IF_RAID_PRESENCE_OUT_N LS_RAID_IF_ALL_PGOOD LS_IF_RAID_PGOOD | A10 A11 A12 A13 A14 A15 A16 | B7 Plane Shape Plane Shape Plane Shape Plane Shape | B5 Plane Shape Plane Shape | B10 B11 | HS_PCIE16_RAID_TX_IF_RX_5_P | | |
| A11 A12 A13 A14 A15 A16 A17 A18 A19 A20 A21 A22 | SE_GND SE_12V0 SE_12V0 SE_GND HS_PCIE16_RAID_TX_IF_RX_0_N HS_PCIE16_RAID_TX_IF_RX_0_P LS_IF_RAID_PRESENCE_OUT_N LS_RAID_IF_ALL_PGOOD LS_IF_RAID_PGOOD | A11 A12 A13 A14 A15 A16 | Plane Shape Plane Shape Plane Shape Plane Shape | Plane Shape Plane Shape | B11 | | B10 | |
| A12 A13 A14 A15 A16 A17 A18 A19 A20 A21 A22 | SE_12V0 SE_12V0 SE_GND HS_PCIE16_RAID_TX_IF_RX_0_N HS_PCIE16_RAID_TX_IF_RX_0_P LS_IF_RAID_PRESENCE_OUT_N LS_RAID_IF_ALL_PGOOD LS_IF_RAID_PGOOD | A12 A13 A14 A15 A16 | Plane Shape Plane Shape Plane Shape | Plane Shape | | SE CND | | B3 |
| A13 A14 A15 A16 A17 A18 A19 A20 A21 A22 | SE_12V0 SE_GND HS_PCIE16_RAID_TX_IF_RX_0_N HS_PCIE16_RAID_TX_IF_RX_0_P LS_IF_RAID_PRESENCE_OUT_N LS_RAID_IF_ALL_PGOOD LS_IF_RAID_PGOOD | A12 A13 A14 A15 A16 | Plane Shape Plane Shape | | D 10 | SE GND | B11 | Plane Shape |
| A13 A14 A15 A16 A17 A18 A19 A20 A21 A22 | SE_12V0 SE_GND HS_PCIE16_RAID_TX_IF_RX_0_N HS_PCIE16_RAID_TX_IF_RX_0_P LS_IF_RAID_PRESENCE_OUT_N LS_RAID_IF_ALL_PGOOD LS_IF_RAID_PGOOD | A13 A14 A15 A16 | Plane Shape Plane Shape | | B12 | SE 3V3 | B12 | Plane Shape |
| A14 A15 A16 A17 A18 A19 A20 A21 A22 | SE_GND HS_PCIE16_RAID_TX_IF_RX_0_N HS_PCIE16_RAID_TX_IF_RX_0_P LS_IF_RAID_PRESENCE_OUT_N LS_RAID_IF_ALL_PGOOD LS_IF_RAID_PGOOD | A14 A15 A16 | Plane Shape | | B13 | LS_RAID_IF_SLOT_ID_0 | B13 | G20 |
| A15 A16 A17 A18 A19 A20 A21 A22 | HS_PCIE16_RAID_TX_IF_RX_0_N HS_PCIE16_RAID_TX_IF_RX_0_P LS_IF_RAID_PRESENCE_OUT_N LS_RAID_IF_ALL_PGOOD LS_IF_RAID_PGOOD | A15 A16 | | H1 | B14 | LS RAID IF SLOT ID 1 | B14 | H20 |
| A16 A17 A18 A19 A20 A21 A22 | HS_PCIE16_RAID_TX_IF_RX_0_P LS_IF_RAID_PRESENCE_OUT_N LS_RAID_IF_ALL_PGOOD LS_IF_RAID_PGOOD | A16 | C8 | C6 | B15 | HS PCIE16 RAID TX IF RX 4 N | | C4 |
| A17 A18 A19 A20 A21 A22 | LS_IF_RAID_PRESENCE_OUT_N LS_RAID_IF_ALL_PGOOD LS_IF_RAID_PGOOD | | D8 | D6 | B16 | HS_PCIE16_RAID_TX_IF_RX_4_P | | D4 |
| A18 A19 A20 A21 A22 | LS_RAID_IF_ALL_PGOOD LS_IF_RAID_PGOOD | | B18 | G3 | B10 | LS_IF_RAID_I2C_INT_N | B10 | G18 |
| A19 A20 A21 A22 | LS_IF_RAID_PGOOD | A17 | | | | | | |
| A20 A21 A22 | | A18 | H18 | D1 | B18 | LS_RAID_IF_I2C_RESET_N | B18 | D20 |
| A21 A22 | | A19 | 118 | E1 | B19 | LS_RAID_IF_I2C_SDA | B19 | E20 |
| A22 | LS_IF_RAID_AUXPGOOD | A20 | C19 | F1 | B20 | LS_RAID_IF_I2C_SCL | B20 | F20 |
| | HS_RAID_IF_PCIE_REFCLK1_N | A21 | C18 | G9 | B21 | HS_PCIE16_IF_TX_RAID_RX_4_P | | G5 |
| A23 | HS_RAID_IF_PCIE_REFCLK1_P | A22 | D18 | H9 | B22 | HS_PCIE16_IF_TX_RAID_RX_4_N | | H5 |
| | GND | A23 | Plane Shape | Plane Shape | B23 | GND | B23 | Plane Shape |
| A24 | HS_PCIE16_IF_TX_RAID_RX_0_P | A24 | G1 | 110 | B24 | HS_PCIE16_IF_TX_RAID_RX_5_P | | 16 |
| A25 | HS_PCIE16_IF_TX_RAID_RX_0_N | A25 | H1 | J10 | B25 | HS_PCIE16_IF_TX_RAID_RX_5_N | | JG |
| A26 | SE_GND | A26 | Plane Shape | Plane Shape | B26 | SE_GND | B26 | Plane Shape |
| A27 | SE_12V0 | A27 | Plane Shape | Plane Shape | B27 | SE_12V0 | B27 | Plane Shape |
| A28 | LS_RAID_IF_LEDPWR | A28 | B19 | H4 | B28 | LS_RAID_IF_PRESENCE_IN_N | B28 | H17 |
| A29 | LS_RAID_IF_ERR_LED_CATHODE | A29 | H19 | Plane Shape | B29 | SE_GND | B29 | Plane Shape |
| A30 | HS_PCIE16_IF_TX_RAID_RX_1_P | A30 | 12 | G11 | B30 | HS_PCIE16_IF_TX_RAID_RX_6_P | P B30 | G7 |
| A31 | HS PCIE16 IF TX RAID RX 1 N | A31 | J2 | H11 | B31 | HS PCIE16 IF TX RAID RX 6 N | | H7 |
| A32 | GND | A32 | Plane Shape | Plane Shape | B32 | GND | B32 | Plane Shape |
| A33 | HS_PCIE16_IF_TX_RAID_RX_2_P | A33 | G3 | 112 | B33 | HS_PCIE16_IF_TX_RAID_RX_7_P | | 18 |
| A34 | HS PCIE16 IF TX RAID RX 2 N | A34 | H3 | J12 | B34 | HS PCIE16 IF TX RAID RX 7 N | | J8 |
| A35 | SE_GND | A35 | Plane Shape | Plane Shape | B35 | SE GND | B35 | Plane Shape |
| | | and the second se | | | | | | Plane Shape |
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| | | | | | | | | Test Point |
| A40 | HS_FCIE I6_IF_IX_RAID_RA_3_N | A40 | J 4 | Test Follin | B40 | DIFF_KSKVD | D40 | Test Follin |
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| 1 | "A2" OF "T | A36 SE_3V3 A37 SE_GND A38 LS_IF_RAID_BIFURCATE_N A39 HS_PCIE16_IF_TX_RAID_RX_3_P A40 HS_PCIE16_IF_TX_RAID_RX_3_N GE 3 FOR "PLANE SHAPE". "A2" OF "TwinAx" MEAN WIRES OF "B2" OF "TwinAx" MEAN WIRES OF | A36 SE_3V3 A36 A37 SE_GND A37 A38 LS_IF_RAID_BIFURCATE_N A38 A39 HS_PCIE16_IF_TX_RAID_RX_3_P A39 A40 HS_PCIE16_IF_TX_RAID_RX_3_N A40 A50 HS_PCIE16_IF_TX_RAID_RX_3_N A40 A40 HS_PCIE16_IF_TX_RAID_RX_3_N A40 A40 HS_PCIE16_IF_TX_RAID_RX_3_N A40 A40 HS_PCIE16_IF_TX_RAID_RX_3_N A40 A51 MA40 HS_PCIE16_IF_TX_RAID_RX_3_N A40 A52 OF "TwinAx" MEAN WIRES OF CABLE "A" "B2" OF "TwinAx" MEAN WIRES OF CABLE "B" dr Kenny Tai eng Ya Li eng Ya Li chr appr Delthy Liu FFF FFF | A36 SE_3V3 A36 Plane Shape A37 SE_GND A37 Plane Shape A38 LS_IF_RAID_BIFURCATE_N A38 A18 A39 HS_PCIE16_IF_TX_RAID_RX_3_P A39 I4 A40 HS_PCIE16_IF_TX_RAID_RX_3_N A40 J4 AGE 3 FOR "PLANE SHAPE". PIN_A A40 "A2" OF "TwinAx" MEAN WIRES OF CABLE "A" PIN_A "B2" OF "TwinAx" MEAN WIRES OF CABLE "B" dr Kenny Tai 2013/06/19 eng YaLi 2014/03/05 chr - - @ppr <delihyliu< td=""> 2014/03/05 Chr - - MEG - ARRA MEG - ARRA MEG - ARRA - -</delihyliu<> | A36 SE_3V3 A36 Plane Shape Plane Shape A37 SE_GND A37 Plane Shape Plane Shape A38 LS_IF_RAID_BIFURCATE_N A38 A18 Plane Shape A39 HS_PCIE16_IF_TX_RAID_RX_3_P A39 I4 Test Point A40 HS_PCIE16_IF_TX_RAID_RX_3_N A40 J4 Test Point AGE 3 FOR "PLANE SHAPE". "A2" OF "TwinAx" MEAN WIRES OF CABLE "A" PIN_ASSIGNME "B2" OF "TwinAx" MEAN WIRES OF CABLE "A" "B" PIN_ASSIGNME dr Kenny Tai 2013/06/19 eng Va Li 2014/03/05 eng Va Li 2014/03/05 pro appr Delihy Liu 2014/03/05 pro FFQU * PADDLE CARDS * MEG-ARRAY CABLE AS | A36 SE_3V3 A36 Plane Shape Plane Shape B36 A37 SE_GND A37 Plane Shape Plane Shape B37 A38 LS_IF_RAID_BIFURCATE_N A38 A18 Plane Shape B38 A39 HS_PCIE16_IF_TX_RAID_RX_3_P A39 I4 Test Point B39 A40 HS_PCIE16_IF_TX_RAID_RX_3_N A40 J4 Test Point B40 NGE 3 FOR "PLANE SHAPE". "A2" OF "TwinAx" MEAN WIRES OF CABLE "A" PIN ASS IGNMENT "B2" OF "TwinAx" MEAN WIRES OF CABLE "B" @I Kenny Tai 2013/06/19 projection @r Kenny Tai 2014/03/05 @r dui 2014/03/05 projection @r Main PADDLE CARDS ASSEMBLY PADDLE CARDS ASSEMBLY MEG-ARRAY CABLE ASSEMBLY | A36 SE_3V3 A36 Plane Shape Plane Shape Plane Shape B36 SE_12V0 A37 SE_GND A37 Plane Shape Plane Shape B38 SE_12V0 A38 LS_IF_RAID_BFURCATE_N A38 A18 Plane Shape B38 SE_GND A39 HS_PCIE16_IF_TX_RAID_RX_3_P A39 14 Test Point B39 DIFF_RSRVD A40 HS_PCIE16_IF_TX_RAID_RX_3_N A40 J4 Test Point B40 DIFF_RSRVD A56 Y Y Y Test Point B40 DIFF_RSRVD A67 Y Y Y Y Test Point B40 DIFF_RSRVD A66 Y Y Y Y Y Y Y Y Y | A36 SE_3V3 A36 Plane Shape Plane Shape B36 SE_12V0 B36 A37 SE_GND A37 Plane Shape B37 SE_0ND B37 A38 LS_JF_RAID_BIFURCATE_N A38 A18 Plane Shape B33 SE_GND B37 A39 HS_PCIE16 IF_TX_RAID_RX_3_P A39 H4 Test Point B39 DIFF_RSRVD B39 A40 HS_PCIE16 IF_TX_RAID_RX_3_N A40 J4 Test Point B39 DIFF_RSRVD B39 A40 HS_PCIE16 IF_TX_RAID_RX_3_N A40 J4 Test Point B40 DIFF_RSRVD B40 MGE 3 FOR "PLANE SHAPE". PIN ASS IGNMENT Isster Plane Shape B30 Size A40 M32" OF "TwinAx" MEAN WIRES OF CABLE "A" PIN ASS IGNMENT Isster Plane Shape Isster Plane Shape Projection Isster Plane Shape A40 Image: Plane Shape B38 DIFF_RSRVD B40 Image: Plane Shape B39 Image: Plane Shape B30 Image: Plane Shape B30 Image: Plane Shape Image: Plane Shape Image: Plane Shape Ima |

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| | | J18 | | | | | J1 | |
| | | J19 | | | | | J2 | |
| _ | | J20 | SE_12V0 Plane Shape | | | J3 | | |
| | | A19 | | | 0 - 0 | A1 | | |
| _ | | A20 | e | /3 Plane Shap | SE_3V | | A2 | |
| | | | | | | | | |
| | A12, A14, A16, | A2, A4, A6, A8, A10, A | | | | | | |
| 3 | | B2, B4, B6, B8, B10, | | | | | | |
| | 5, | C1,C3,C | | | | | | |
| | ,C15,C17 | C7,C9,C11,C13 | | | | | | |
| | | D1,D3,D | | | | | | |
| | | D7,D9, D11,D13 | | | | | A4,A6,A8,A10,A12,A14,A | |
| | 0 | E1-E19 | | | | ,B16,B18,B20 | B4,B6,B8,B10,B12,B14, | |
| | | F1-F16 | | | | | , | |
| | | F18,F1 | | | | | C5,C7,C9,C11,C13,C1 | |
| | 0, G12, G14, | G2, G4, G6, G8, G1 | | | | 15,017,019, | D5,D7,D9,D11,D13,D1 E2-E20 | |
| | | G16, H2, H4, H6, H8, H10, | | | | | F2, F5-F20 | |
| , | | 12, 114, 110, 110, 110, 11, 13, 15 | | | | 100 million and a second s | G4,G6,G8,G10,G12,G14 | |
| | | 17,19, 111,113 | | | | | 20,H6,H8, | |
| | | J1,J3,J5 | | | | Contraction of the contraction o | H10,H12,H14,H16,F | |
| | | J7, J9, J1 | | | | 4 2451 488 | 15,17,19,111,113,115, | |
| | | J13,J15 | £ | Plane Shape | GND | 5,J17, J19 | J5, J7, J9, J11, J13, J1 | |
| | ND | Same as C | SE_GND Plane Shape | | | Same as GND | | |
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NOTE : I.The electrical requirements, at the assembly level, as follows: (0-2ns is test vehicle) I.I Differential Impedance; 100ps (20-80%) RAID PI Side I4mm stack height. - 2.0 to 2.6 ns 80 to 130 ohms - 2.6 to 4.0 ns 70 to 115 ohms Interface P2 Side 4mm stack height. - 2.0 to 2.5 ns 75 to 120 ohms - 2.5 to 4.0 ns 70 to 115 ohms I.2 Single-end Impedance 100ps (20-80%) RAID PI Side 14mm stack height - AVE Single Ended Impedance not to exceed . - 2.0 to 2.6 ns 45 to 85 ohms - 2.6 to 4.0 ns 45 to 65 ohms Interface P2 Side 4mm stack height - AVE Ended Impedance not to exceed . - 2.0 to 2.5 ns 45 to 70 ohms - 2.5 to 4.0 ns 45 to 65 ohms 1.3 Differential Insertion loss of finished assembly without test board loss -4.3 dB @ I.5GHz -7.6 dB @ 3 GHz -15.4 dB @ 5 GHz -30 dB @ 8 GHz -41 dB @ 10 GHz 1.4 Maximum paddle card thickness = 50 mils 1.5 Maximum paddle card via stub length = 15 mils 1.6 Raw Twinax conductors must be 32 Gauge 1.7 There must be no fewer than four (4) discrete GND "sideband" wire connections within the raw 3M twinax cable 1.8 All sidebands must be tied to a common GND plane at each paddle card. 2. Package specification: GS-14-2352 3. Pin to Pin Resistance not allow to exceed 2 ohms - HiPot test: 300V dc - 2 Mega-ohms - 10ms .

4. The detail of the label: IBM P/N: 00DJ000;IBM EC NUMBER: H48590F;FCI Location:FCT NT CHINA;

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Series Number:YYMMDDXXX.

| dr ĸ | Kenny Tai | 2013/06/19 | | projectio | n n | $\sim \infty$ | size | scale | | |
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