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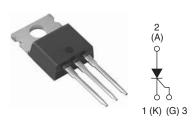






Vishay High Power Products

Phase Control SCR, 10 A



TO-220AB

PRODUCT SUMMARY			
V _T at 6.5 A	< 1.15 V		
I _{TSM}	140 A		
V _{RRM}	800 V		

DESCRIPTION/FEATURES

The 10TTS08 High Voltage Series of silicon controlled rectifiers are specifically designed for medium power switching and phase control applications. The glass passivation technology used has reliable operation up to 125 °C junction temperature.

Typical applications are in input rectification and crow-bar (soft start) and these products are designed to be used with Vishay HPP input diodes, switches and output rectifiers which are available in identical package outlines.

Also available in SMD-220 package (series 10TTS08S).

This product has been designed and qualified for industrial level.

OUTPUT CURRENT IN TYPICAL APPLICATIONS				
APPLICATIONS	SINGLE-PHASE BRIDGE	THREE-PHASE BRIDGE	UNITS	
Capacitive input filter T _A = 55 °C, T _J = 125 °C, common heatsink of 1 °C/W	13.5	17	А	

MAJOR RATINGS AND CHARACTERISTICS				
PARAMETER	TEST CONDITIONS	VALUES	UNITS	
I _{T(AV)}	Sinusoidal waveform	6.5	A	
I _{T(RMS)}		10	A	
V _{RRM} /V _{DRM}		800	V	
I _{TSM}		140	Α	
V _T	6.5 A, T _J = 25 °C	1.15	V	
dV/dt		150	V/µs	
dl/dt		100	A/μs	
T _J	Range	- 40 to 125	°C	

VOLTAGE RATINGS			
PART NUMBER	V _{RRM} , MAXIMUM PEAK REVERSE VOLTAGE V	V _{DRM} , MAXIMUM PEAK DIRECT VOLTAGE V	I _{RRM} /I _{DRM} AT 125 °C mA
10TTS08	800	800	1.0

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ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average on-state current	I _{T(AV)}	T _C = 112 °C, 180° conduction half sine wave		6.5	
Maximum RMS on-state current	I _{T(RMS)}	1 _C = 112 C, 160 Collac	action half sine wave	10	Α
Maximum peak, one-cycle,		10 ms sine pulse, rated	V _{RRM} applied, T _J = 125 °C	120	A
non-repetitive surge current	I _{TSM}	10 ms sine pulse, no volt	age reapplied, T _J = 125 °C	140	
Maximum 12+ for fusing	l ² t	10 ms sine pulse, rated	V _{RRM} applied, T _J = 125 °C	72	A ² s
Maximum I ² t for fusing	I ² t	10 ms sine pulse, no volt	age reapplied, $T_J = 125 ^{\circ}\text{C}$	100	A-S
Maximum I ² √t for fusing	I²√t	$t = 0.1$ to 10 ms, no voltage reapplied, $T_J = 125$ °C		1000	A²√s
Maximum on-state voltage drop	V_{TM}	6.5 A, T _J = 25 °C		1.15	V
On-state slope resistance	r _t	T _J = 125 °C		17.3	mΩ
Threshold voltage	V _{T(TO)}			0.85	V
Maximum varyage and divest leakage accurant	1 /1	T _J = 25 °C	V _R = Rated V _{RRM} /V _{DRM}	0.05	
Maximum reverse and direct leakage current	I _{RM} /I _{DM}	T _J = 125 °C		1.0	
Typical holding current	I _H	Anode supply = 6 V, resistive load, initial I _T = 1 A		30	mA
Maximum latching current	ΙL	Anode supply = 6 V, resistive load		50	
Maximum rate of rise of off-state voltage	dV/dt	T _J = 25 °C		150	V/µs
Maximum rate of rise of turned-on current	dl/dt			100	A/µs

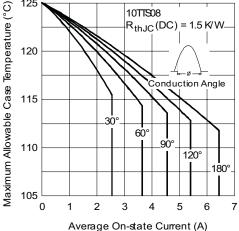
TRIGGERING				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum peak gate power	P_{GM}		8.0	w
Maximum average gate power	P _{G(AV)}		2.0	VV
Maximum peak positive gate current	+I _{GM}		1.5	Α
Maximum peak negative gate voltage	-V _{GM}		10	V
Maximum required DC gate current to trigger	l _{GT}	Anode supply = 6 V, resistive load, T _J = - 65 °C	20	mA
		Anode supply = 6 V, resistive load, T _J = 25 °C	15	
		Anode supply = 6 V, resistive load, T _J = 125 °C	10	
Maximum required DC gate voltage to trigger	V _{GT}	Anode supply = 6 V, resistive load, T _J = - 65 °C	1.2	
		Anode supply = 6 V, resistive load, T _J = 25 °C	1	V
		Anode supply = 6 V, resistive load, T _J = 125 °C	0.7	
Maximum DC gate voltage not to trigger	V_{GD}	T _J = 125 °C, V _{DRM} = Rated value 0.2 0.1		
Maximum DC gate current not to trigger	I _{GD}			mA

SWITCHING				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Typical turn-on time	t _{gt}	T _J = 25 °C	0.8	
Typical reverse recovery time	t _{rr}	T 105 °C	3	μs
Typical turn-off time	tq	T _J = 125 °C	100	



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THERMAL AND MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range		T _J , T _{Stg}		- 40 to 125	°C
Maximum thermal resistance, junction to case		R _{thJC}	DC operation	1.5	
Maximum thermal resistance, junction to ambient		R_{thJA}		62	°C/W
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.5	
Approximate weight			2	g	
				0.07	OZ.
Mounting torque —	minimum			6 (5)	kgf · cm
	maximum			12 (10)	(lbf · in)
Marking device			Case style TO-220AC	10TTS08	



Average On-state Current (A)
Fig. 1 - Current Rating Characteristics

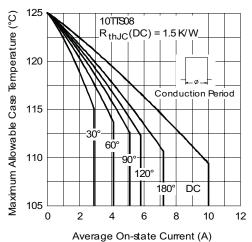


Fig. 2 - Current Rating Characteristic

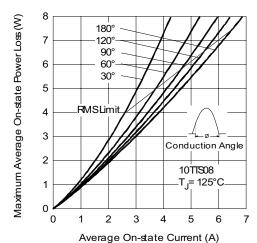


Fig. 3 - On-State Power Loss Characteristics

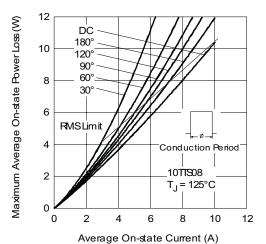
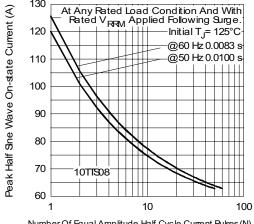


Fig. 4 - On-State Power Loss Characteristics

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Number Of Equal Amplitude Half Cycle Current Pulses (N) Fig. 5 - Maximum Non-Repetitive Surge Current

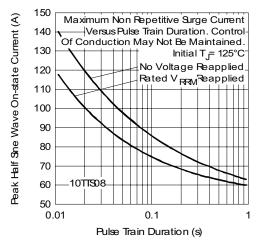


Fig. 6 - Maximum Non-Repetitive Surge Current

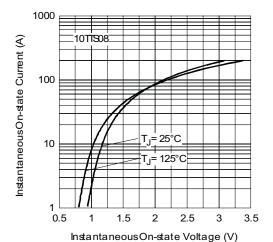


Fig. 7 - On-State Voltage Drop Characteristics

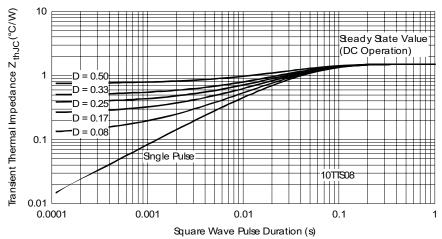


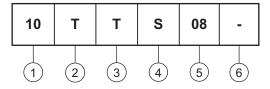
Fig. 8 - Thermal Impedance Z_{thJC} Characteristics

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ORDERING INFORMATION TABLE

Device code



1 - Current rating

2 - Circuit configuration:

T = Single thyristor

3 - Package:

T = TO-220AC

4 - Type of silicon:

S = Converter grade

5 - Voltage code x 100 = V_{RRM}

None = Standard production

• PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS				
Dimensions http://www.vishay.com/doc?95222				
Part marking information	http://www.vishay.com/doc?95225			

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