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# **Description**

The IMX15, IMS15, and IMY15 Series of board mountable 15 Watt DC-DC converters have been designed according to the latest industry requirements and standards. The converters are particularly suitable for use in mobile or stationary applications in transport, railways, industry, or telecommunication, where variable input voltages or high transient voltages are prevalent.

Covering a total input voltage range from 8.4 up to 150 V with different models, the converters are available with single and electrically-isolated double outputs from 3.3 up to 48 V, externally adjustable, with flexible load distribution on double-output models. A shutdown input allows for remote on/off.

Features include efficient input and output filtering and consistently high efficiency over the entire input voltage range, high reliability, and excellent dynamic response to load and line changes.

The converters have been approved by CSA. 20IMS/20IMX15 and 40IMS/40IMX15 models exhibit a basic insulation. The 110IMY15 models provide double insulation and are CE

#### **Features**

- Wide input voltage ranges up to 150 VDC
- 1 or 2 isolated outputs up to 48 V
- RoHS lead-free-solder and lead-solder-exempted products are available.
- 5 year warranty for RoHS lead-free-solder products with temperature index -8.
- 1500 to 3000 VAC I/O voltage withstand test
- · Emissions EN 55011, group 1, level A
- Immunity to EN 50155 and 55121-3-2
- High efficiency (typ. 86%)
- · Input undervoltage lockout
- · Shutdown input, adjustable output voltages
- Flex power: Flexible load distribution on outputs
- · Outputs no-load, overload, and short-circuit proof
- Operating ambient temperature -40 to 85 °C
- · Thermal protection
- 2" x 1.6" case with 10.5 mm profile
- Basic insulation: 20/40IMX15 models; double or reinforced insulation: 110IMY15 models

Safety-approved according to the latest edition of EN/IEC 60950-1 and UL/CSA 60950-1.





marked. They may be connected to a rectified 110 VAC source or a 110 V battery without any further insulation barrier.

The circuitry is comprised of integrated planar magnetics. All components are automatically assembled and solidly soldered onto a single PCB without any wire connection. Magnetic feedback ensures maximum reliability and repeatability in the control loop over all operating conditions. Careful consideration of possible thermal stress ensures the absence of hot spots providing long life in environments, where temperature cycles are frequent. The thermal design allows operation at full load up to an ambient temperature of 71 °C in free air without using any potting material. For extremely high vibration environments the case has holes for screw fastening.

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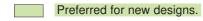
# **Model Selection**

Table 1: Model Selection

Outp	ut 1	Outp	out 2	Output	Input voltage	Efficie	ency	Model	Options <sup>2</sup>
V <sub>o nom</sub> [VDC]	<i>I</i> <sub>o nom</sub> <sup>1</sup> [A]	V <sub>o nom</sub> [VDC]	I <sub>o nom</sub> 1 [A]	power Po nom [W]	V <sub>i min</sub> to V <sub>i max</sub> [VDC]	η <sub>min</sub> 6 [%]	η <sub>typ</sub> <sup>6</sup> [%]		
3.3	4.5	-	-	14.9	8.4 to 36 <sup>5</sup>	80	82	20IMX15-03-8RG-G SR	i, Z, non-G
3.3	4.0	-	-	13.2	16.8 to 75 <sup>3</sup>	78	80	40IMX15-03-8RG	non-G
3.3	4.5	-	-	14.9	16.8 to 75 <sup>3</sup>	81	83	40IMX15-03-8RG-G <sup>SR</sup>	i, Z, non-G
3.3	4.5	-	-	14.9	50 to 150 <sup>4</sup>	77	79	110IMY15-03-8RG-G <sup>SR</sup>	i, Z, non-G
5.1	3.5	-	-	17.5	8.4 to 36 <sup>5</sup>	83	86	20IMX15-05-8RG-G SR	i, Z, non-G
5.1	3.5	-	-	17.5	16.8 to 75 <sup>3</sup>	85	87	40IMX15-05-8RG-G <sup>SR</sup>	i, Z, non-G
5.1	3.5	-	-	17.5	50 to 150 <sup>4</sup>	79.5	82	110IMY15-05-8RG-G SR	i, Z, non-G
5.1	2.3	-	-	11.7	8.4 to 36 <sup>5</sup>	82	84	20IMX15-05-8R-G	Z, non-G
5.1	2.7	-	-	13.8	14 to 36	81.5	84	24IMS15-05-9R	
5.1	2.5	-	-	12.8	16.8 to 75 <sup>3</sup>	81.5	83	40IMX15-05-8R-G	
5.1	2.5	-	-	12.8	50 to 150 <sup>4</sup>	78	81	110IMY15-05-8R-G	Z, non-G
5.1	1.35	3.3	1.35	11.3	8.4 to 36 <sup>5</sup>	82	84	20IMX15-0503-8RG	i, Z, non-G
5.1	1.6	3.3	1.6	13.5	14 to 36	81	83	24IMS15-0503-9R	
5.1	1.5	3.3	1.5	12.6	16.8 to 75 <sup>3</sup>	82	84	40IMX15-0503-8RG	i, Z, non-G
5.1	1.6	3.3	1.6	13.5	36 to 75	79	82	48IMS15-0503-9R	
5.1	1.5	3.3	1.5	12.6	50 to 150 <sup>4</sup>	79	82	110IMY15-0503-8RG	i, Z, non-G
5	1.3	5	1.3	13.0	8.4 to 36 <sup>5</sup>	82	85	20IMX15-05-05-8G	i, Z, <mark>non-G</mark>
5	1.4	5	1.4	14.0	14 to 36	81	84	24IMS15-05-05-9	
5	1.4	5	1.4	14.0	16.8 to 75 <sup>3</sup>	81	84	40IMX15-05-05-8G	i, Z, non-G
5	1.4	5	1.4	14.0	50 to 150 <sup>4</sup>	80	82	110IMY15-05-05-8G	i, Z, <mark>non-G</mark>
12	0.65	12	0.65	15.6	8.4 to 36 <sup>5</sup>	83	86	20IMX15-12-12-8G	i, Z, non-G
12	0.7	12	0.7	16.8	14 to 36	83	86	24IMS15-12-12-9	
12	0.7	12	0.7	16.8	16.8 to 75 <sup>3</sup>	84	87	40IMX15-12-12-8G	i, Z, non-G
12	0.7	12	0.7	16.8	50 to 150 <sup>4</sup>	82	85	110IMY15-12-12-8G	i, Z, non-G
15	0.5	15	0.5	15.0	8.4 to 36 <sup>5</sup>	85	88	20IMX15-15-15-8G	i, Z, non-G
15	0.56	15	0.56	16.8	14 to 36	83	86	24IMS15-15-15-9	
15	0.56	15	0.56	16.8	16.8 to 75 <sup>3</sup>	83	86	40IMX15-15-15-8G	i, Z, non-G
15	0.56	15	0.56	16.8	50 to 150 <sup>4</sup>	82	85	110IMY15-15-15-8G	i, Z, non-G
24	0.32	24	0.32	15.4	8.4 to 36 <sup>5</sup>	83	86	20IMX15-24-24-8G	i, Z, non-G
24	0.35	24	0.35	16.8	14 to 36	84	87	24IMS15-24-24-9G	non-G
24	0.35	24	0.35	16.8	16.8 to 75 <sup>3</sup>	84	86	40IMX15-24-24-8G	i, Z, non-G
24	0.35	24	0.35	16.8	50 to 150 <sup>4</sup>	82	85	110IMY15-24-24-8G	i, Z, non-G

Flexible load distribution on dual and double outputs possible; up to 75% of the total output power  $P_{0 \text{ nom}}$  on one of the 2 outputs. IMX/IMY15-0503 models have reduced load distribution flexibility; 1.8 A max. on one of the 2 outputs. The total output power should not exceed Po nom.

Efficiency at  $T_A$  = 25 °C,  $V_{1 \text{ nom}}$ ,  $I_{0 \text{ nom}}$ . Models with synchronous rectifier. For the RoHS version of such models, add -G!



NFND Not for new designs. Replace 24IMS15 models by 20IMX15 and 48IMS15 by 40IMX15;

Replace 20IMX15-05, 40IMX15-05, 110IMY15-05 by models with synchronos rectifier SR; for new designs, chose always the RoHS version!



See Description of Options.

Short-time operation down to  $V_i = 14.4 \text{ V}$  possible ( $P_o$  reduced to approx. 85% of  $P_{o \text{ nom}}$ )

Short-time operation down to  $V_i = 43.2 \text{ V}$  possible ( $P_o$  reduced to approx. 85% of  $P_{o \text{ nom}}$ ) and up to 154 V

Initial start-up at 9 V, main output voltage regulation down to 8.4 V



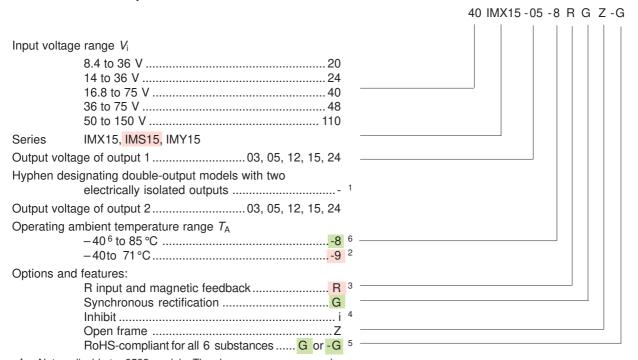
#### **RoHS-Compliant Models**

The type designation of RoHS-compliant models (compliant for the restriction of all six substances) for the IMX/IMY15 Series ends with "G".

However, in **single-output models** with 3.3 V or 5.1 V output an extra hyphen (-) is added after the existing "G", which already signifies a synchronous rectifier SR. This is an exception to our normal nomenclature to identify this type of product, since G was already used to designate products for higher output current fitted with a synchronous rectifier. Some examples

- 20IMX15-05-8R-G designates a standard version with RoHS compliance for all six substances.
- 20IMX15-05-8RG designates a synchronous rectifier<sup>SR</sup> version which is not RoHS-compliant.
- 20IMX15-05-8RG-G designates a synchronous rectifier<sup>SR</sup> version and RoHS compliant for all six substances.
- 20IMX15-05-05-8G designates a double-output model with RoHS compliance for all six substances
- 110IMY15-24-24-8RG designates a double-output model with RoHS compliance for all six substances.

### **Part Number Description**



- Not applicable to -0503 models. They have a common ground.
- <sup>2</sup> IMS15 models are not recommended for new designs.
- Standard for single-output and -0503 models
- Option i replaces the standard shutdown function; see *Description of Options*.
- <sup>5</sup> RoHS models with synchronous rectifier are ending with -G, as an exception.
- 6 Only -25 °C for older -8RGSR models (Rev. < BA); see table 7a.

Note: The sequence of options must follow the order above !

### Examples:

20IMX15-05-05-8G: DC-DC converter, input 9 to 36 V, 2 galvanically isolated outputs each providing 5 V, 1.3 A, RoHS-compliant.

110IMY15-0503-8RG: DC-DC converter, input 50 to 150 V, 2 outputs with common return providing +5.1 V, 1.5 A and +3.3 V, 1.5 A, RoHS-compliant. Converter fitted with magnetic feedback for tight output voltage regulation.

#### **Product Marking**

The converters without option Z are marked with basic type designation, input and output voltages and currents, applicable safety approval and recognition marks, company logo, date code, and serial number.





# **Functional Description**

The IMX15/IMS15/IMY15 Series of DC-DC converters are magnetic feedback-controlled flyback converters using current mode PWM (Pulse Width Modulation). The -05- and -0503-output voltage models exhibit an active magnetic feedback loop via a pulse transformer, resulting in very tight regulation of the output voltage (see the block diagrams). The output voltages of these models can be adjusted via the R-input. The R-input is referenced to the secondary side and allows for programming the output voltages in the range of approximately 80 to 105% of  $V_{0\ nom}$ , using either an external resistor or an external voltage source.

Several single-output models with 3.3 or 5.1 V output exhibit a synchronous rectifier to improve the efficiency.

The voltage regulation on the double-output models is achieved with an auxiliary winding of the main transformer. The output voltages can be adjusted via the Trim input, which is referenced to the primary side and allows for programming the output voltage in the range of 100 to 105% of  $V_{\rm 0\ nom}$  via an external resistor, or within 75 to 105% if using an external voltage source. The load regulation output characteristic allows for paralleling of one or more double-output models with equal output voltage.

Current limitation is provided by the primary circuit, thus limiting the total output power of double-output models. The shutdown input allows remote converter on/off.

Overtemperature protection will disable the converter under excessive overload conditions with automatic restart.

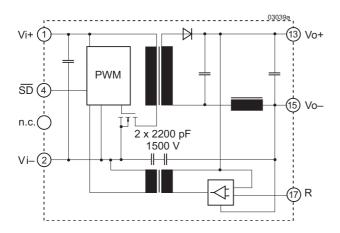


Fig. 1
Block diagram of single-output models

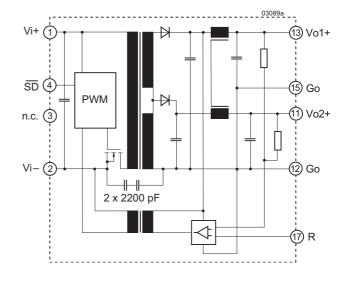


Fig. 2 Block diagram of -0503-models

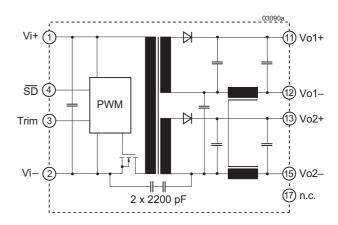


Fig. 3
Block diagram of double-output models



# **Electrical Input Data**

General conditions:

- $-T_A = 25$  °C, unless  $T_C$  is specified.
- Shut-down pin left open-circuit.
- Trim or R input left open-circuit.

Table 2a: Input data of IMX15 and IMY15 models

Input				20	IMX		4	OIMX			110IM	1	Unit
Charac	teristics		Conditions	min t	ур	max	min	typ	max	min	typ	max	
V <sub>i</sub>	Input voltage ra	ange <sup>1</sup>	$T_{A \min} - T_{A \max}$	9 5, 7		36	16.8 <sup>5, 6</sup>		75	50 <sup>5</sup>		150 <sup>8</sup>	V
V <sub>i nom</sub>	Nominal input voltage		$I_0 = 0 - I_{0 \text{ nom}}$	2	20			40			110		
V <sub>i sur</sub>	Repetitive surg	e voltage	Abs. max input (3 s)			40			100			168	
t <sub>startup</sub>	Converter	Switch on	Worst case condition at	0	.25	0.5		0.25	0.5		0.5	0.8	S
	start-up time <sup>2</sup>	SD high	$V_{\text{i min}}, I_{\text{o}} = I_{\text{o nom}}$			0.1			0.1			0.1	
t <sub>rise</sub>	Rise time <sup>2</sup>		V <sub>i nom</sub> , resistive load		5			5			5		ms
			Io nom, capac. load	-	10	20		10	20		10	20	
I <sub>i o</sub>	No-load input of	current	$I_0 = 0$ , $V_{i min} - V_{i max}$			65			45			15	mA
I <sub>irr</sub>	Reflected ripple	e current	$I_0 = 0 - I_{0 \text{ nom}}$			30			30			20	mA <sub>pp</sub>
I <sub>inr p</sub>	Inrush peak cu	rrent 3	$V_{\rm i} = V_{\rm i  nom}$			8			9			10	Α
Ci	Input capacitar	nce	for surge calculation	1	.5			0.75			0.35		μF
V <sub>SD</sub>	Shutdown volta	age	Converter disabled	-10	to 0.	.7	-1	0 to 0.	7	-	10 to 0	.7	V
			Converter operating	open o	r 2 -	- 20	open	or 2 –	- 20	ope	n or 2	- 20	
RSD	Shutdown inpu	t resistance		appr	ox. 1	0	арі	orox. 1	0	ap	prox.	10	kΩ
/ <sub>SD</sub>	Input current, v	vhen	V <sub>i min</sub> – V <sub>i max</sub> SD connected to Vi–			10			3			1	mA
f <sub>s</sub>	Switching frequ	uency	$V_{i \min} - V_{i \max},$ $I_0 = 0 - I_{0 \text{ nom}}$	appro	x. 3	00	арр	rox. 3	00	ар	prox. 3	00	kHz

- If  $V_0$  is set above  $V_{0 \text{ nom}}$  by use of the R or Trim input,  $V_{1 \text{ min}}$  will be proportionately increased.
- <sup>2</sup> Measured with resistive and max. admissible capacitive load. Valid for models with rev. Al or greater.
- Source impedance according to ETS 300132-2, version 4.3.
- Double-output models with both outputs in parallel. External filter as in fig. 6, table 6.
- Input undervoltage lockout at typ. 80% of  $V_{\rm i \, min}$ .
- Short time operation down to  $V_{i \min} > 14.4 \text{ V possible}$ .  $P_o$  reduced to approx. 85% of  $P_{o \text{ nom}}$ .
- Initial start-up at  $V_i = 9$  V, main output voltage regulation down to 8.4 V
- 8 Short time operation up to 154 V possible for 2 s.





### Table 2b: Input Data of IMS15 models; general conditions as in table 2a

Input					24IMS			48IMS		Unit
Charac	teristics		Conditions	min	typ	max	min	typ	max	
V <sub>i</sub>	Input voltage ra	ange <sup>1</sup>	$T_{\text{A min}} - T_{\text{A max}}$	14		36	36		75	V
$V_{\rm inom}$	Nominal input	voltage	$I_0 = 0 - I_{0 \text{ nom}}$		24			48		
V <sub>i sur</sub>	Repetitive surg	je voltage	Abs. max input (3 s)			50			100	
tstartup	Converter	Switch on	Worst case condition at		0.25	0.5		0.25	0.5	S
	start-up time 2	SD high	$V_{\text{i min}}, I_{\text{o}} = I_{\text{o nom}}$			0.1			0.1	
trise	Rise time <sup>2</sup>		V <sub>i nom</sub> , resistive load		5			5		ms
			Io nom, capac. load		10	20		10	20	
I <sub>i o</sub>	No-load input of	current	$I_0 = 0$ , $V_{i min} - V_{i max}$		20	40		10	20	mA
I <sub>irr</sub>	Reflected rippl	e current	$I_0 = 0 - I_{0 \text{ nom}}$		30			30		mA <sub>pp</sub>
I <sub>inr p</sub>	Inrush peak cu	rrent 3	$V_{\rm i} = V_{\rm i  nom}$			5			4.5	Α
Ci	Input capacitar	nce	for surge calculation		4			2		μF
V <sub>SD</sub>	Shutdown volta	age	Converter disabled		-10 to 0.7	,		-10 to 0.7	7	V
			Converter operating	or	oen or 2 –	20	C	pen or 2 –	20	
RSD	Shutdown inpu	t resistance			approx. 10	)		approx. 10	)	kΩ
/ <sub>SD</sub>	Input current, v	vhen	V <sub>i min</sub> - V <sub>i max</sub> SD connected to Vi-		1.2	3		1.2	3	mA
f <sub>S</sub>	Switching frequency	uency	$V_{i \min} - V_{i \max},$ $I_{o} = 0 - I_{o \text{ nom}}$		approx. 30	0		approx. 30	0	kHz

- <sup>1</sup> If  $V_0$  is set above  $V_{0 \text{ nom}}$  by use of the R or Trim input,  $V_{1 \text{ min}}$  will be proportionately increased.
- <sup>2</sup> Measured with resistive and max. admissible capacitive load.
- Source impedance according to ETS 300132-2, version 4.3.
- <sup>4</sup> Double-output models with both outputs in parallel. External filter as in fig. 6, table 6.
- Input undervoltage lockout at typ. 80% of  $\dot{V}_{\rm i \, min}$ .





#### **Inrush Current**

The inrush current has been made as low as possible by choosing a very small input capacitance. A series resistor may be installed in the input line to further reduce this current.

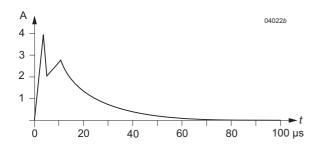


Fig. 4 Typical inrush current at  $V_{\rm i\; nom}$ ,  $P_{\rm o\; nom}$  versus time (40IMX15). Source impedance according to ETS 300132-2 at  $V_{\rm i\; nom}$ .

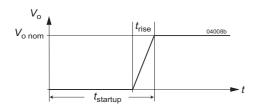


Fig. 5
Converter start-up and rise time

#### Input Undervoltage Lockout

A special feature of these converters is the accurate undervoltage lockout protection, which protects against large currents caused by operation at low voltages. This ensures easier start-up in distributed power systems.

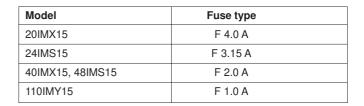
Tab. 3: Turn on and turn off voltage

Model	Turn	on	Turi	n off	Unit
	min	max	min	max	
20IMX15	7.5	8	7	7.5	V
24IMS15	12.5	13.5	12	13	
40IMX15	12.5	13.5	12	13	
48IMS15	31.5	32.5	31	32	
110IMY15	40	42.5	38	40.5	

# **Fuse and Reverse Polarity Protection**

The built-in suppressor diode also provides for reverse polarity protection at the input by conducting current in the reverse direction. An external fuse is required to limit this current.

Table 4: Recommended external fuses



#### **Input Transient Voltage Protection**

A built-in suppressor diode provides effective protection against input transients, which typically occur in many installations.

Table 5: Built-in transient voltage suppressor

Туре	Breakdown voltage V <sub>Br nom</sub> [V]	Peak power at 1 ms P <sub>p</sub> [W]	Peak pulse current I <sub>pp</sub> [A]
20IMX15	40	1500	22
24IMS15	53	600	7.7
40IMX15	100	1500	9.7
48IMS15	100	600	4.1
110IMY15	168	600	0.5

For very high energy transients as for example to achieve compliance to IEC/EN 61000-4-5 (see table *Electromagnetic Immunity*), an external inductor and a capacitor are required, see Fig. 6. The components are specified in table 6.

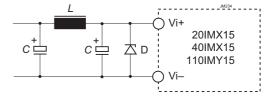


Fig. 6
Example for external circuitry to achieve better transient immunity. Values of components in the table below.

Table 6: Components for external circuitry for IEC/EN 61000-4-5, level 3 compliance.

Model	Inductor (L)	Capacitor (C)	Diode (D)
20IMX15	75 μH, 3.15 A	2 x 330 μF, 63 V	1.5k E47A
24IMS15	220 μH, 1.3 A	2 x 330 μF, 63 V	
40IMX15	220 μH, 1.3 A	2 x 330 μF, 100 V	
48IMS15	220 μH, 1 A	2 x 330 μF, 100 V	
110IMY15	220 μΗ, 0.4 Α	2 x 330 μF, 200 V	





# **Electrical Output Data**

We recommend connecting an external 1 µF ceramic capacitor across the output pins.

#### General conditions:

- $-T_A = 25$  °C, unless  $T_C$  is specified
- Shutdown pin and Trim or R pin left open-circuit (not connected)

Table 7a: Output data for single-output models -03-8RG1 and 05-8RG1

Outpu	t					3.3 V			5.1 V		Unit
Chara	cteristics		Conditions		min	typ	max	min	typ	max	
Vo	Output voltag	je	$V_{\text{i nom}}$ $I_{\text{o}} = 0.5 I_{\text{o nom}}$		3.25		3.35	5.05		5.15	V
I <sub>o nom</sub>	Output curre	nt 20IMX	V <sub>i min</sub> to V <sub>i max</sub>			4.5			3.5		Α
		40IMX/110IMY				4.5			3.5		
I₀ L	Current limit	2	$V_{\text{i nom}}, T_{\text{C}} = 25 \text{ °C}$ $V_{\text{o}} \le 93\% V_{\text{o nom}}$			6.0			4.6		
$\Delta V_{o}$	Line/load reg	ulation	$V_{\text{i min}}$ to $V_{\text{i max}}$ , (0.1 to 1) $I_{\text{o nom}}$				±0.5			±0.5	%
V <sub>o</sub>	Output voltag	ge noise	V <sub>i min</sub> to V <sub>i max</sub>	4			100			100	mV <sub>pp</sub>
			$I_0 = I_{0 \text{ nom}}$	5			60			60	
V <sub>o L</sub>	Output overv	oltage limit. 3			115		130	115		130	%
C <sub>o ext</sub>	Admissible c	apacitive load			0		4000 <sup>6</sup>	0		4000 <sup>6</sup>	μF
V <sub>o d</sub>	Dynamic	Voltage deviat.	V <sub>i nom</sub>			±250			±250		mV
t <sub>od</sub>	load regulation	Recovery time	$I_{0 \text{ nom}} \leftrightarrow {}^{1/2}I_{0 \text{ nom}}$ IEC/EN 61204				1			1	ms
$\alpha_{Vo}$	Temperature $\Delta V_{\rm o}/\Delta T_{\rm C}$	coefficient	$V_{\text{i min}}$ to $V_{\text{i max}}$ (0.1 to 1) $I_{\text{o nom}}$				±0.02			±0.02	%/K

- 1 SR Models -8RG (synchr. rectifier) have a minimum case and operating temperature of -25 °C. If the revision is BA (or later), it is -40 °C.
- <sup>2</sup> The current limit is primary side controlled. In the event of a sustained overload condition the thermal protection may cause the converter to shut down (automatic restart after cooling down).
- The overvoltage protection is via a primary side second regulation loop. It is not tracking with R control.
- $^4$  BW = 20 MHz
- <sup>5</sup> Measured with a probe according to EN 61204
- $^{6}$  For 110IMY15 models,  $\textit{C}_{\text{o ext}}$  is limited to 3000  $\mu\text{F}.$





Table 7b: Output data for -05-8R and -0503-8R models; general conditions as in table 7a

Output	t				5.1 V			5.1/3.3 V		Unit
Charac	eteristics		Conditions	min	typ	max	min	typ	max	
V <sub>o</sub> , V <sub>o1</sub> V <sub>o2</sub>	Output voltag	je	$V_{\text{i nom}}$ $I_0 = 0.5 I_{\text{o nom}}$	5.05		5.15	5.0 3.13		5.12 3.46	V
I <sub>o nom</sub>	Output currer	nt 1 20IMX	V <sub>i min</sub> - V <sub>i max</sub>		2.3			2 × 1.35	1.6 4	Α
		24IMS/48IMS			2.7			2 × 1.6	2.0 4	1
		40IMX/110IMY			2.5			2 × 1.5	1.8 4	1
I <sub>oL</sub> , I <sub>o1L</sub> I <sub>o2L</sub>	Current limit 2	20IMX	$V_{\text{i nom}}$ $V_{\text{o1}} \le 93\% \ V_{\text{o nom}}$		3.2			2.7 3.8		
I <sub>oL</sub> , I <sub>o1L</sub> I <sub>o2L</sub>		24IMS/48IMS			3.5			3.0 3.8		
I <sub>oL</sub> /I <sub>o1L</sub> I <sub>o2L</sub>		40IMX/110IMY			3.6			2.9 4.0		
$\Delta V_{o}$	Line/load reg	ulation 5.1 V	1 1111111			±0.5		_		%
		5.1 V	(0.1 – 1) I <sub>o nom</sub>			_			+3 / -5	
		3.3 V				_			±4.5	
V <sub>o1/2</sub>	Output voltag	je noise	$V_{\text{i min}} - V_{\text{i max}}$ 5			70			80	$mV_{pp}$
			$I_0 = I_{0 \text{ nom}}$			40			40	
V <sub>o L</sub>	Output overv	oltage limit <sup>7</sup>		115		130	115		130	%
C <sub>o ext</sub>	Admissible ca	apacitive load		0		4000	0		4000 <sup>3</sup>	μF
V <sub>o d</sub>	Dynamic	Voltage deviat.	V <sub>i nom</sub>			±250			±150	mV
t <sub>od</sub>	load regulation	Recovery time	$I_{0 \text{ nom}} \leftrightarrow {}^{1/2} I_{0 \text{ nom}}$ IEC/EN 61204			1			1	ms
$\alpha_{\text{Vo}}$	Temperature $\Delta V_{\rm o}/\Delta T_{\rm C}$	coefficient	$V_{i \text{ min}} - V_{i \text{ max}}$ (0.1 - 1) $I_{o \text{ nom}}$			±0.02			±0.02	%/K

<sup>&</sup>lt;sup>1</sup> Flexible load distribution:  $I_0$  max for one of the 2 outputs; however the total load should not exceed  $P_0$  nom specified in the table *Model Selection* 



<sup>&</sup>lt;sup>2</sup> The current limit is primary-side controlled.

<sup>&</sup>lt;sup>3</sup> For -0503-models: total capacitive load of both outputs.

<sup>&</sup>lt;sup>4</sup> For -0503-models: Conditions for specified output. Other output loaded with constant current  $I_0 = 0.5 I_{0 \text{ nom}}$ .

<sup>5</sup> BW = 20 MHz

<sup>&</sup>lt;sup>6</sup> Measured with a probe according to EN 61204

<sup>&</sup>lt;sup>7</sup> The overvoltage protection is via a primary side second regulation loop. It is not tracking with R control.



### able 7c: Output data for double-output models; general conditions as in table 7a

Output				:	2 × 5 V		2	× 12 V	'	2	× 15	٧	2 :	× 24	٧	Unit
Chara	cteristics		Conditions	min	typ n	nax	min	typ m	nax	min	typ	max	min	typ	max	
V <sub>o1</sub>	Output 1	24IMS/48IMS	V <sub>i nom</sub>	4.95	5	5.05	11.88	12	2.12	14.85		15.15	23.70		24.30	V
		other models	$I_0 = 0.5 I_{0 \text{ nom}}$	4.95	5	5.05	11.90	12	2.10	14.88		15.12	23.80		24.20	
$V_{o2}$	Output 2	24IMS/48IMS		4.94	5	5.06	11.84	12	2.16	14.80		15.20	23.64		24.36	
		other models		4.94	5	5.06	11.88	12	2.12	14.85		15.15	23.75		24.25	
I <sub>o nom</sub>	Output curren	t <sup>1</sup> 20IMX	$V_{\rm i  min} - V_{\rm i  max}$		2×1.3		2	<b>×</b> 0.65		2	<b>×</b> 0.5	50	2:	× 0.3	32	Α
		other models		:	2 <b>x</b> 1.4		2	<b>×</b> 0.70		2	<b>×</b> 0.5	56	2:	× 0.3	35	
I <sub>oL</sub>	Current limit 2,	. 4 20IMX	V <sub>i nom</sub>		3.0			1.6			1.3			0.85		
		24IMS/48IMS	$V_{\text{o1}} \leq 93\% \ V_{\text{o nom}}$		3.5			1.9			1.6			0.95		
		40IMX/110IMY			3.2			1.7			1.4			0.90		
$\Delta V_{o1}$	Line/load regu	ulation <sup>8</sup> output 1	$V_{\text{i min}} - V_{\text{i max}}, I_{\text{o nom}}$		±1			±1			±1			±1		%
$\Delta V_{o2}$		output 2	$V_{\text{i nom}}$ , (0.1 – 1) $I_{\text{o nom}}$			±3		=	±3			±3			±3	
$V_{o1/2}$	Output voltage	e noise	$V_{\rm imin} - V_{\rm imax}$ 5			80		1	20			150			240	$mV_{pp}$
			$I_0 = I_{0 \text{ nom}}$			40		6	60			70			120	
V <sub>o L</sub>	Output overvo	ltage limit <sup>7 8</sup>	Min. load 1%	115		130	115	1	30	115		130	115		130	%
C <sub>o ext</sub>	Admissible ca	pacitive load <sup>3</sup>		0	4	000	0	6	80	0		470	0		180	μF
V <sub>o d</sub>	Dynamic	Voltage deviat.	V <sub>i nom</sub>		±250			±300			±300		:	±600	)	mV
t <sub>o d</sub>	load regulation	Recovery time	$I_{\text{0 nom}} \leftrightarrow {}^{1}/_{2} I_{\text{0 nom}}$			1			1			1			1	ms
$\alpha_{\text{Vo}}$	Temperature $\Delta V_{\rm o}/\Delta T_{\rm C}$	coefficient	$V_{i \min} - V_{i \max}$ (0.1 – 1) $I_{0 \text{ nom}}$		±0	0.02		±0	0.02			±0.02			±0.02	%/K

Flexible load distribution: Each output of double-output models is capable of delivering 75% of the total output power; however the total load should not exceed P<sub>o nom</sub> specified in the table *Model Selection*.



<sup>&</sup>lt;sup>2</sup> The current limit is primary-side controlled.

Measured with both outputs connected in parallel.

<sup>&</sup>lt;sup>4</sup> Conditions for specified output. Other output loaded with constant current  $I_0 = 0.5 I_{0 \text{ nom}}$ .

<sup>5</sup> BW = 20 MHz

<sup>&</sup>lt;sup>6</sup> Measured with a probe according to EN 61204

<sup>&</sup>lt;sup>7</sup> The overvoltage protection is via a primary side second regulation loop, not tracking with Trim or R control.

<sup>8</sup> At start-up occurs a short overshoot at the output.



#### Thermal Considerations

If a converter, mounted on a PCB, is located in free, quasistationary air (convection cooling) at the indicated maximum ambient temperature  $T_{\rm A\,max}$  (see table Temperature specifications) and is operated at its nominal input voltage and output power, the case temperature measured at the measuring point of case temperature  $T_{\rm C}$  (see Mechanical Data) will approach the indicated value  $T_{\rm C\,max}$  after the warmup phase. However, the relationship between  $T_{\rm A}$  and  $T_{\rm C}$  depends heavily on the conditions of operation and integration into a system. The thermal conditions are influenced by input voltage, output current, airflow, temperature of surrounding components and surfaces, and the properties of the printed circuit board.  $T_{\rm A\,max}$  is therefore only an indicative value.

**Caution:** The case temperature  $T_{\rm C}$  measured at the measuring point of case temperature  $T_{\rm C}$  (see *Mechanical Data*) may under no circumstances exceed the specified maximum value. The installer must ensure that under all operating conditions  $T_{\rm C}$  remains within the limits stated in the table *Temperature specifications*.

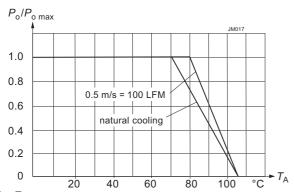


Fig. 7
Maximum output power versus ambient temperature

### **Overtemperature Protection**

The converters are protected from possible overheating by means of an internal temperature monitoring circuit. It shuts down the converter above the internal temperature limit, and attempts to automatically restart in short intervals. This feature helps protect against excessive internal temperatures, which could occur during heavy overload conditions.

#### **Output Overvoltage Protection**

The output of single-output models as well as -0503- and -05-05-models are protected against overvoltage by a second control loop. In the event of an overvoltage on one of the outputs, the converter will shut down and attempt to restart in short intervals.

Doubel-output models (except -0503- and -05-05-models) are protected against overvoltage by a Zener diode across the second output. Under worst case conditions the Zener diode will become a short circuit. Since with double-output models both outputs track each other, the protection diode is only provided in one of the outputs. The main purpose of this feature is to protect against possible overvoltage, which could occur due to a failure in the control logic. This protection circuit is not designed to withstand externally applied overvoltages.

#### **Short Circuit Behavior**

The current limit characteristic shuts down the converter, whenever a short circuit is applied to its output. It acts self-protecting, and automatically recovers after removal of the overload condition (hiccup mode).

#### **Parallel and Series Connection**

The outputs of one or several single- or double-output models

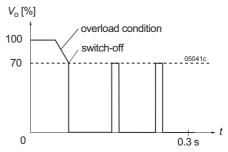


Fig. 8
Overload switch-off (hiccup mode); typical values

can be connected in series without any precautions, taking into consideration that the highest output voltage should remain below 42 V to ensure that the output remains SELV.

Both outputs of the same converter with equal voltage (e.g. 5 V / 5 V) can be connected in parallel and will share their output currents equally. Parallel operation of single or double outputs of two or more converters with the same output voltage may cause start-up problems at initial start-up. This is only advisable in applications, where one converter is able to deliver the full load current as, for example, required in true redundant systems.

**Note:** If the 2<sup>nd</sup> output of double-output models (except 0503) is not used, connect it in parallel to the 1<sup>st</sup> output.

### **Typical Performance Curves**

General conditions:

- $-T_A = 25$  °C, unless  $T_C$  is specified.
- Shutdown pin left open-circuit.
- Trim or R input not connected.

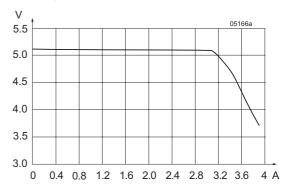


Fig. 9  $V_0$  versus  $I_0$  (typ) of converters with  $V_0 = 5.1 V$  (110IMY15-05-8R)





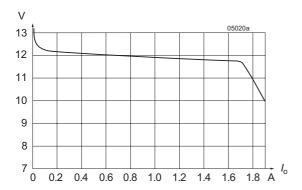


Fig. 10  $V_o$  versus  $I_o$  (typ.) of double-output models (2 x 12 V), with both outputs in parallel (110 IMY15-12-12-8)

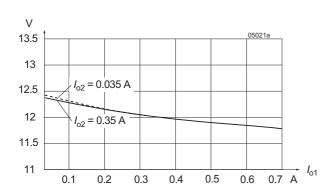


Fig. 11 Cross load regulation  $V_{o1}$  versus  $I_{o1}$  (typ.) for various  $I_{o2}$  (2 x 12 V).

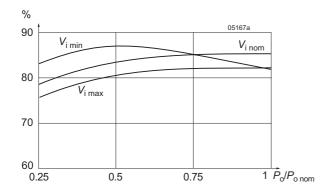


Fig. 12a
Efficiency versus input voltage and load. Typical values (40IMX15-12-12-8).

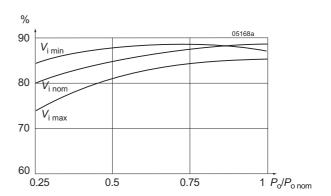


Fig. 12b Efficiency versus input voltage and load. Typical values (110IMY15-12-12-8)

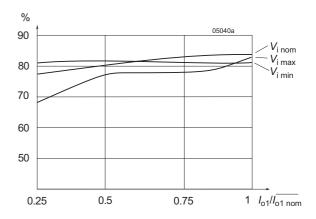


Fig. 12c Efficiency versus input voltage and load. Typical values (48IMS15-12-12-9)

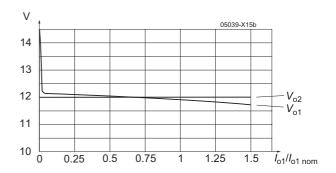


Fig. 13 Flexible load distribution on double-outputs models with option R (110IMY15-12-12-8R): Load variation from 0 to 150% of  $l_{\rm 01\ nom}$  on output 1; output 2 loaded with 50% of  $l_{\rm 02\ nom}$ .





# **Auxiliary Functions**

# **Shutdown Function**

The outputs of the converters may be enabled or disabled by means of a logic signal (TTL, CMOS, etc.) applied to the shutdown pin. If this function is not required, the shut-down pin should be left open-circuit.

Converter operating:
 Converter disabled:
 2.0 to 20 V
 -10 to +0.7 V

### **Adjustable Output Voltage**

- R input for single-output models and -0503-models
- · Trim input for double-output models

As a standard feature, the single- and double-output models offer adjustable output voltage(s) by using the control input R or Trim. If the control input is left open-circuit, the output voltage is set to  $V_{\text{o nom}}$ . For output voltages  $V_{\text{o}} > V_{\text{o nom}}$ , the minimum input voltage  $V_{\text{i min}}$  (see *Electrical Input Data*) increases proportionally to  $V_{\text{o}}/V_{\text{o nom}}$ .

#### Single-output models with synchronous rectifier (G):

The R input is referenced to the secondary side of the converter. Adjustment of the output voltage is possible by means of an external resistor connected between the R pin and either Vo+ or Vo-.

**Note:** For models with synchronous rectifier the logic for  $V_0$  adjustment differs from other models with R input.

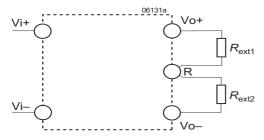


Fig. 14
Output voltage control for single-output models with synchronous rectifier.

Table 8: Vo versus Vext approximate values

V <sub>o nom</sub> [V]	Typ. values o $V_0$ [% of $V_{0 \text{ nom}}$ ]		Typ. values o $V_0$ [% of $V_{0 \text{ nom}}$ ]	
3.3	90	0.47	100	∞
	95	2.7	105	15
	100	∞	110	6.8
5.1	90	3.3	100	∞
	95	8.2	105	9.1
	100	∞	110	3.9

### All other models fitted with R-input:

The R input is referenced to the secondary side of the converter. Adjustment of the output voltage is possible by means of either an external resistor or a voltage source.

#### a) Adjustment by means of an external resistor $R_{\text{ext}}$ .

Depending upon the value of the required output voltage, the resistor shall be connected:

either: Between the R pin and Vo– to achieve an output voltage adjustment range of  $V_o \approx 80$  to 100 % of  $V_{o \text{ nom}}$ .

$$R_{\text{ext1}} \approx 4 \text{ k}\Omega \cdot \frac{V_{\text{o}}}{V_{\text{o nom}} - V_{\text{o}}}$$

or: Between the R pin and Vo+ to achieve an output voltage range of  $V_0 \approx 100$  to 105% of  $V_{0 \text{ nom}}$ .

$$R_{\text{ext2}} \approx 4 \text{ k}\Omega \cdot \frac{(V_{\text{o}} - 2.5\text{V})}{2.5 \text{ V} \cdot (V_{\text{o}}/V_{\text{o nom}} - 1)}$$

### b) Adjustment by means of an external voltage V<sub>ext</sub> between Vo– and R pin.

The control voltage range is 1.96 to 2.62 V and allows for adjustment in the range of  $V_0 \approx 80$  to 105% of  $V_{0 \text{ nom}}$ .

$$V_{\rm ext} \approx \frac{V_{\rm o} \cdot 2.5 \text{ V}}{V_{\rm o nom}}$$

Attempting to adjust the output below this range will cause the converter to shut down (hiccup mode).

**Note:** Applying an external control voltage >2.75 V may damage the converter.

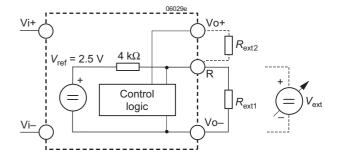


Fig. 15
Output voltage control for single-output models, -0503-models, and double-output models by means of the R input.

### **Double-output models with Trim input:**

The Trim input is referenced to the primary side. The figure below shows the topology. Adjustment is possible trough either an external resistor or an external voltage source  $V_{\rm ext}$ .

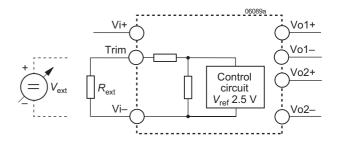


Fig. 16
Output voltage control for double-output models (with Trim input) by means of the Trim input.





### a) Adjustment by means of an external resistor $R_{\text{ext}}$ :

Programming of the output voltage by means of an external resistor  $R_{\rm ext}$  is possible within 100 to 105% of  $V_{\rm o\,nom}$ .  $R_{\rm ext}$  should be connected between the Trim pin and Vi–. Connection of  $R_{\rm ext}$  to Vi+ may damage the converter. The table below indicates suitable resistor values for typical output voltages under nominal conditions ( $V_{\rm i\,nom}$ ,  $I_{\rm o\,0}=0.5$   $I_{\rm o\,nom}$ ) with either parallel-connected outputs or equal-load conditions on both outputs.

Table 9:  $R_{\text{ext}}$  for  $V_0 > V_{\text{o nom}}$ ; approximate values ( $V_{\text{i nom}}$ ,  $I_{\text{o1, 2}} = 0.5 I_{\text{o1/2 nom}}$ )

V <sub>o</sub> [% V <sub>o nom</sub> ]	R <sub>ext</sub> [kΩ]
105 to 108 (107 typically)	0
105	1.5
104	5.6
103	12
102	27
101	68
100	∞

#### b) Adjustment by an external voltage source $V_{\text{ext}}$ :

For programming the output voltage in the range 75 to 105% of  $V_{\rm 0\ nom}$ , a source  $V_{\rm ext}$  (0 to 20 V) is required, connected between the Trim pin and Vi–. The table below indicates values  $V_{\rm 0}$  versus  $V_{\rm ext}$  (nominal conditions  $V_{\rm i\ nom}$ ,  $I_{\rm 0}$  = 0.5  $I_{\rm 0\ nom}$ ), with either parallel-connected outputs or equal load conditions on both outputs. Applying a control voltage >20 V will set the converter into a hiccup mode. Direct paralleling of the Trim pins of parallel connected converters is possible.

Table 10:  $V_o$  versus  $V_{\rm ext}$  for  $V_o$  = 75 to 105%  $V_{\rm o nom}$ ; typical values ( $V_{\rm i nom}$ ,  $I_{\rm o1/2}$  = 0.5  $I_{\rm o1/2 nom}$ )

V <sub>o</sub> [% V <sub>o nom</sub> ]	V <sub>ext</sub> [V]
≥105	0
102	1.6
95	4.5
85	9
75	13

# **Electromagnetic Compatibility (EMC)**

A suppressor diode together with an input filter forms an effective protection against high input transient voltages, which

typically occur in many installations, but especially in batterydriven mobile applications.

### **Electromagnetic Immunity**

Table 11: Immunity type tests

Phenomenon	Standard	Class Level	Coupling mode <sup>2</sup>	Value applied	Waveform	Source Imped.	Test procedure	In oper.	Perf. crit. <sup>3</sup>
Electrostatic discharge to case R-pin open	IEC/EN 61000-4-2	34	contact discharge	6000 V <sub>p</sub>	1/50 ns	330 Ω 150 pF	10 positive and 10 negative discharges	yes	A
Electromagnetic	IEC/EN	x 5	antenna	20 V/m	AM 80%, 1 kHz	n.a.	80 to 1000 MHz	yes	Α
field	61000-4-3	6	antenna	20 V/m	80% AM, 1 kHz	n.a.	800 – 1000 MHz	yes	Α
				10 Vm 5 V/m 3 V/m			1400 – 2000 MHz 2000 – 2700 MHz 5100 – 6000 MHz		
Electrical fast transients/burst	IEC/EN 61000-4-4	37	direct +i/-i <sup>2</sup>	2000 V <sub>p</sub>	bursts of 5/50 ns, 5 kHz over 15 ms, burst period: 300 ms	50 Ω	60 s positive 60 s negative transients per coupling mode	yes	A
Surges	IEC/EN 61000-4-5	31	+i/-i <sup>2</sup>	1000 V <sub>p</sub>	1.2/50 µs	2 Ω 18 μF	5 pos. and 5 neg. surges	yes	А
RF conducted immunity	IEC/EN 61000-4-6	3 8 2 9	+i/—i <sup>2</sup>	10 VAC <sup>8</sup> 3 VAC <sup>9</sup>	AM modulated 80%, 1 kHz	150 Ω	0.15 to 80 MHz	yes	А

- 1 External components required; see Fig. 6 and table 6. Exceeds the railway standard EN 50121-3-2:2016, table 3.3.
- <sup>2</sup> i = input
- 3 A = normal operation, no deviation from specs., B = temporary deviation from specs possible.
- Corresponds to the railway standard EN 50121-3-2:2016, table 5.3
- <sup>5</sup> Corresponds to the railway standard EN 50121-3-2:2016, table 5.1.
- Corresponds to the railway standard EN 50121-3-2:2016, table 5.2.
- Corresponds to the railway standard EN 50121-3-2:2016, table 3.2.
- Corresponds to the railway standard EN 50121-3-2:2016, table 3.1.
- 9 Valid for 24IMS15 and 48IMS15





#### **Conducted Emissions**

Compliance with EN 55011, group 1, class A, was tested with the filter fig. 6 (values table 6).

The resuts are shown in fig. 17a, b, c.

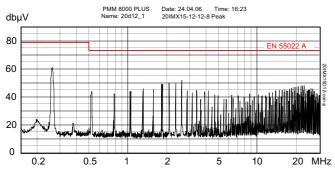


Fig. 17a 20IMX15-12-8: Typ. disturbance voltage (peak) at pos. input according to EN 55011, measured at  $V_{i \text{ nom}}$  and  $I_{o \text{ nom}}$ .

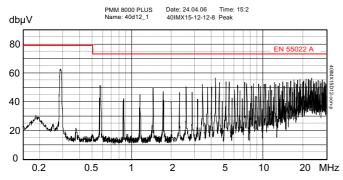


Fig. 17b 40IMX15-12-12-8: Typ. disturbance voltage (peak) at pos. input according to EN 55011, measured at  $V_{i \text{ nom}}$  and  $I_{o \text{ nom}}$ .

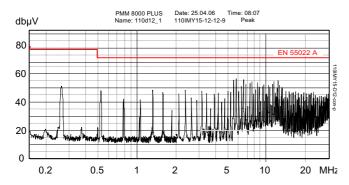


Fig. 17c 110IMY15-12-12-8: Typ. disturbance voltage (peak) at pos. input according to EN 55011, measured at V<sub>i nom</sub> and I<sub>o nom</sub>.

Compliance with EN 55011, group 1, class B can be performed with the filter in fig. 18. The result is shown in fig. 19.

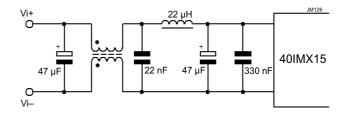


Fig. 18
Example for external circuitry to comply with EN 55011,
Group 1, Class B, conducted.
This filter was designed for a 40IMX15-12-12-8. All
capacitors are rated to 100 V, the chokes to 1.5 A.

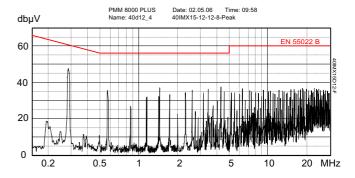


Fig. 19
Typ. disturbance voltage (peak) at input according to EN 55011, group 1, class A, measured at V<sub>i nom</sub> and I<sub>o nom</sub>.
Output leads 0.1 m, twisted, input filter as in fig. 18
(40IMX15-12-12-8)





# **Immunity to Environmental Conditions**

Table 12: Mechanical and climatic stress

Test I	Method	Standard	Test Conditions		Status
Cab	Damp heat steady state	IEC/EN 60068-2-78 MIL-STD-810D sect. 507.2	Temperature: Relative humidity: Duration:	40 ±2 ⊕C 93 +2/-3 % 56 days	Converter not operating
Ea	Shock (half-sinusoidal)	IEC/EN 60068-2-27 MIL-STD-810D sect. 516.3	Acceleration amplitude: Bump duration: Number of bumps:	50 g <sub>n</sub> = 490 m/s <sup>2</sup> 11 ms 18 (3 each direction)	Converter operating
Fc	Vibration (sinusoidal)	IEC/EN 60068-2-6	Acceleration amplitude: Frequency (1 Oct/min): Test duration:	0.35 mm (10 to 60 Hz) 5 $g_n$ = 49 m/s² (60 to 2000 Hz) 10 to 2000 Hz 7.5 h (2.5 h each axis)	Converter operating
Fda	Random vibration wide band reproducibility high	IEC/EN 60068-2-35	Acceleration spectral density: Frequency band: Acceleration magnitude: Test duration:	0.05 g <sub>n</sub> <sup>2</sup> /Hz 20 to 500 Hz 4.9 g <sub>n rms</sub> 3 h (1 h each axis)	Converter operating
Ка	Salt mist test sodium chloride (NaCl) solution	EN 50155:2007, clause 12.2.10, class ST2	Temperature: Duration:	35 <sup>±2</sup> °C 16 h	Converter not operating

# **Temperatures**

Table 14: Temperature specifications

Valid for air pressure of 800 to 1200 hPa (800 to 1200 mbar)

Temp	Temperature		-9		-8		Unit
Char	acteristics	Conditions	min	max	min	max	
$T_{A}$	Ambient temperature	Operational <sup>1</sup>	-40	71	-40 <sup>2, 3</sup>	85	°C
$T_{C}$	Case temperature		-40	95	-40 <sup>2, 3</sup>	105	
Ts	Storage temperature	Non operational	-55	85	-55	85	

<sup>&</sup>lt;sup>1</sup> See Thermal Considerations

# Reliability

Table 13: MTBF

Model	Standard	Ground Benign	Ground Fixed		Ground Mobile	Unit
		T <sub>C</sub> = 40 °C	T <sub>C</sub> = 40 °C	<i>T</i> <sub>C</sub> = 70 °C	<i>T</i> <sub>C</sub> = 50 °C	
20IMX15-12-12-8	MIL-HDBK-217F	697 000	366 000	229 000	312 000	h
20IMX15-15-15-8	Bellcore	2 345 000	1 172 000	632 000	317 000	
48 IMS15-05-05-9	MIL-HDBK-217F	535 000	283 000	179 000	245 000	
110IMY15-05-8R	MIL-HDBK-217F	485 000	255 000	167 000	223 000	
	Bellcore	1 547 000	774 000	394 000	206 000	



<sup>&</sup>lt;sup>2</sup> Start-up at -55 °C, except models with synchronous rectifier <sup>SR</sup>

<sup>3 –25 °</sup>C for all models with synchronous rectifier <sup>SR</sup> with Revision lower than BA.



# **Mechanical Data**

Dimensions in mm (inches). Tolerances ±0.3 mm, unless otherwise noted.



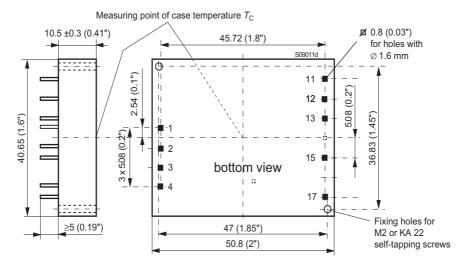


Fig. 20 Case IMX15, IMS15, IMY15 Material: Fortron 1140L6

Weight: 35 g

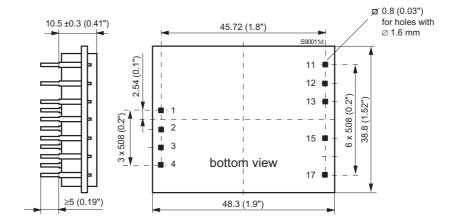


Fig. 21 Open frame (option Z) Weight: 26 g





# Safety and Installation Instructions

#### Pin allocation

Table 15: Pin allocation

Pin	Standard and Option Z					
	single	double	-0503-			
1	Vi+	Vi+	Vi+			
2	Vi–	Vi–	Vi–			
3	-	Trim	n.c.			
4	SD	SD	SD			
5	-	-	-			
6	-	-	-			
11	-	Vo1+	Vo2+			
12	-	Vo1-	Go			
13	Vo+	Vo2+	Vo1+			
15	Vo-	Vo2-	Go			
17	R	n.c.	R			

		10005
0		11 🏻
		12 🛮
   <sub>0</sub> 1		13 🗖
" '	Bottom view	
<b>-</b> 2		
<b>-</b> 3		15 🏻
<b>-</b> 4		::
	7	17 🛮

Fig. 22 Footprint

#### **Installation Instructions**

Installation must strictly follow the national safety regulations in compliance with the enclosure, mounting, creepage, clearance, casualty, markings and segregation requirements of the end-use application.

The converter must be connected to a secondary circuit.

Do not open the converter.

Ensure that a converter failure (e.g., by an internal short-circuit) does not result in a hazardous condition.

#### Input Fuse

To prevent excessive current flowing through the input supply lines in case of a malfunction an external fuse should be installed in a non-earthed input supply line; see *table 4*.

#### **Standards and Approvals**

The converters are approved according to the latest edition of EN/IEC 60950-1 and UL/CSA 60950-1. 110IMY models are fitted with a CE mark.

The converters have been evaluated for:

- Building in
- Supplementary insulation input to output, based on their maximum input voltage (IMX15, IMS15)
- Reinforced insulation input to output, based on their maximum input voltage (IMY15 models)
- Pollution degree 2 environment (not option Z)
- Connecting the input to a secondary circuit subject to a maximum transient rating of 1500 V (IMX15, IMS15)
- Connecting the input to a secondary circuit subject to a maximum transient rating of 2500 V (IMY15)

The converters are subject to manufacturing surveillance in accordance with the above mentioned UL standards and with ISO9001:2008.

### **Railway Applications**

To comply with railway standards, all components are coated with a protective lacquer (except option Z).

#### **Protection Degree and Cleaning Liquids**

The protection degree of the converters is IP 40, except openframe models (option Z).

In order to avoid possible damage, any penetration of cleaning fluids should be prevented, since the power supplies are not hermetical sealed.

However, open-frame models (option Z) leave the factory unlacquered; they may be lacquered by the customer, for instance together with the mother board. Cleaning liquids are not permitted — except washing at room temperature with isopropyl alcohol. If necessary, the mother board must be cleaned, before fitting the open-frame converter.

**Note:** Cleaning liquids may damage the adhesive joints of the ferrite cores.

#### Isolation

The electric strength test is performed in the factory as routine test in accordance with EN 50514 and IEC/EN 60950. The Company will not honor any warranty claims resulting from incorrectly executed electric strength field tests.

Table 16: Electric strength test voltages

Characteristic	Input to output IMS15   IMX15   IMY15			Output to output	Unit
Factory test ≥1 s	1.2	1.5 <sup>1</sup>	3.0	0.1	kVAC
Equivalent DC voltage	(1.7)	(2.1)	(4.2)	0.15	kVDC
Insulation resistance at 500 VDC	>100	>100	>100	-	МΩ
Partial discharge extinction voltage	Consult the Company			-	kV

- <sup>1</sup> 1.5 kVAC according to IEC 60950, sect. 6.2, Telecom equipment; type test with 1.5 kVAC / 60 s (IEE 802.3). IMX15 units produced before 2013 were tested only with 1.2 kVAC.
- <sup>2</sup> The test voltage between outputs is not applied as routine test.





# **Description of Options**

Table 17: Survey of options

Option	Function of option	Characteristic
-9	Temperature range, NFND	See table Temperatures specifications
i	Inhibit	Replaces the shutdown function with inverted logic
Z	Open frame	See Mechanical Data

#### Option -9 versus -8

IMX15 and IMY15 models with -9 (not for new designs) have a limited temperature range. Standard models with suffix -8 are rated up to  $T_A$  = 85 °C; see table *Temperature specifications*.

### Option i: Inhibit

Replaces the shut-down function with inverted logic.

The output(s) of the converter may be enabled or disabled by

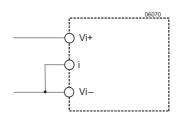


Fig. 23
If the inhibit function is not used, the inhibit pin should be connected to Vi—.

means of a logic signal (TTL, CMOS, etc.) applied to the inhibit pin. No output voltage overshoot will occur, when the converter is turned on. If the inhibit function is not required, the inhibit pin should be connected to Vi— to enable the output (active low logic, fail safe).

Voltage on pin i:

Converter operating: -10 V to 0.8 V

Converter inhibited: 2.4 V to  $V_{i \text{ max}}$  (<75 V) or pin i left open-circuit.

### Option Z

Open frame and not lacquered. This option can be chosen for mounting onto a mother board, which is subject to be lacquered. See *Protection Degree and Cleaning Liquids* (page 18).

### Option G or -G

Products RoHS-compliant for all 6 substances. See *RoHS-Compliant Models* (page 3) for the correct denotation.

### **Accessories**

Supports are available for chassis mounting CMBIMX15 (HZZ00626) and for DIN-rail mounting DMBIMX15 (HZZ00625). They exhibit the connectors and allow for placing different external components. For details see our Mounting Supports Data Sheet BCD20007 on our website.

Fig. 24 DIN-rail mounting support DMBIMX15 (HZZ00625)



NUCLEAR AND MEDICAL APPLICATIONS - These products are not designed or intended for use as critical components in life support systems, equipment used in hazardous environments, or nuclear control systems.

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