



Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts,Customers Priority,Honest Operation,and Considerate Service",our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

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Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China



International
IOR Rectifier

12CWQ04FN

SCHOTTKY RECTIFIER

12 Amp

$$I_{F(AV)} = 12\text{Amp}$$
$$V_R = 40\text{V}$$

Major Ratings and Characteristics

Characteristics	Values	Units
$I_{F(AV)}$ Rectangular waveform	12	A
V_{RRM}	40	V
I_{FSM} @ tp = 5 μ s sine	550	A
V_F @ 6 Apk, $T_J = 125^\circ\text{C}$ (per leg)	0.48	V
T_J range	-55 to 150	$^\circ\text{C}$

Description/ Features

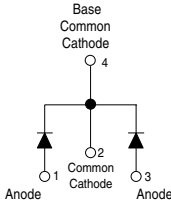
The 12CWQ04FN surface mount, center tap, Schottky rectifier series has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, free-wheeling diodes, battery charging, and reverse battery protection.

- Popular D-PAK outline
- Center tap configuration
- Small foot print, surface mountable
- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability

Case Styles



D-PAK (TO-252AA)



12CWQ04FN

Bulletin PD-20546 rev. I 05/06

International
IOR Rectifier

Voltage Ratings

Part number	12CWQ04FN
V_R Max. DC Reverse Voltage (V)	40
V_{RWM} Max. Working Peak Reverse Voltage (V)	

Absolute Maximum Ratings

Parameters	12CWQ...	Units	Conditions
$I_{F(AV)}$ Max. Average Forward (Per Leg) Current* See Fig. 5 (Per Device)	6 12	A	50% duty cycle @ $T_C = 134^\circ\text{C}$, rectangular wave form
I_{FSM} Max. Peak One Cycle Non-Repetitive Surge Current * See Fig. 7	550 90	A	5 μs Sine or 3 μs Rect. pulse 10ms Sine or 6ms Rect. pulse Following any rated load condition and with rated V_{RRM} applied
E_{AS} Non-Repet. Avalan. Energy (Per Leg)	9	mJ	$T_J = 25^\circ\text{C}$, $I_{AS} = 1.5$ Amps, $L = 8$ mH
I_{AR} Repetitive Avalanche Current (Per Leg)	1.2	A	Current decaying linearly to zero in 1 μsec Frequency limited by T_J max. $V_A = 1.5 \times V_R$ typical

Electrical Specifications

Parameters	12CWQ...	Units	Conditions
V_{FM} Max. Forward Voltage Drop (Per Leg) * See Fig. 1 (1)	0.53	V	@ 6A $T_J = 25^\circ\text{C}$
	0.68	V	@ 12A $T_J = 25^\circ\text{C}$
	0.48	V	@ 6A $T_J = 125^\circ\text{C}$
	0.64	V	@ 12A $T_J = 125^\circ\text{C}$
I_{RM} Max. Reverse Leakage Current (Per Leg) * See Fig. 2 (1)	3	mA	$T_J = 25^\circ\text{C}$ $V_R = \text{rated } V_R$
	40	mA	$T_J = 125^\circ\text{C}$
$V_{F(TO)}$ Threshold Voltage	0.28	V	$T_J = T_J \text{ max.}$
r_t Forward Slope Resistance	25.58	m Ω	
C_T Typ. Junction Capacitance (Per Leg)	405	pF	$V_R = 5V_{DC}$ (test signal range 100Khz to 1Mhz) 25°C
L_S Typical Series Inductance (Per Leg)	5.0	nH	Measured lead to lead 5mm from package body

(1) Pulse Width < 300 μs , Duty Cycle <2%

Thermal-Mechanical Specifications

Parameters	12CWQ...	Units	Conditions
T_J Max. Junction Temperature Range (*)	-55 to 150	$^\circ\text{C}$	
T_{stg} Max. Storage Temperature Range	-55 to 150	$^\circ\text{C}$	
R_{thJC} Max. Thermal Resistance (Per Leg) Junction to Case (Per Device)	3.0	$^\circ\text{C/W}$	DC operation * See Fig. 4
	1.5		
wt Approximate Weight	0.3 (0.01)	g (oz.)	
Case Style	D-Pak		Similar to TO-252AA
Device Marking	12CWQ04FN		

(*) $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{th(j-a)}}$ thermal runaway condition for a diode on its own heatsink

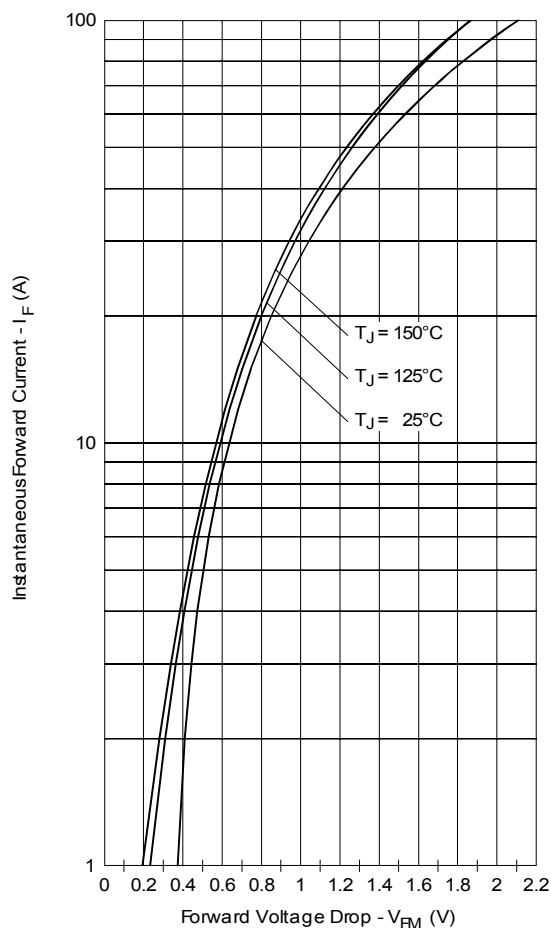


Fig. 1 - Max. Forward Voltage Drop Characteristics (Per Leg)

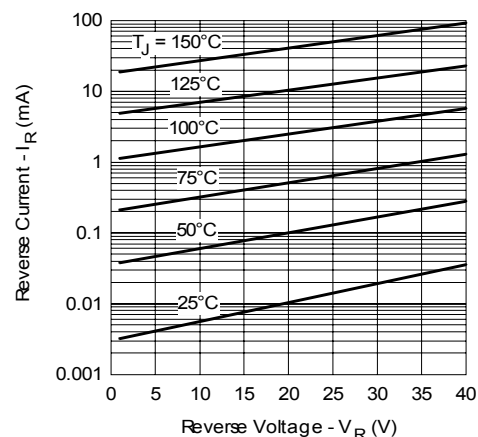


Fig. 2 - Typical Values Of Reverse Current Vs. Reverse Voltage (Per Leg)

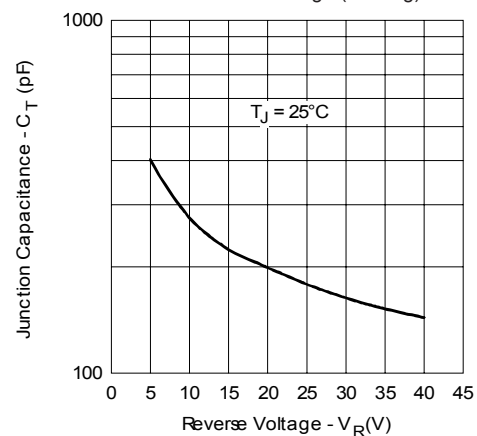


Fig. 3 - Typical Junction Capacitance Vs. Reverse Voltage (Per Leg)

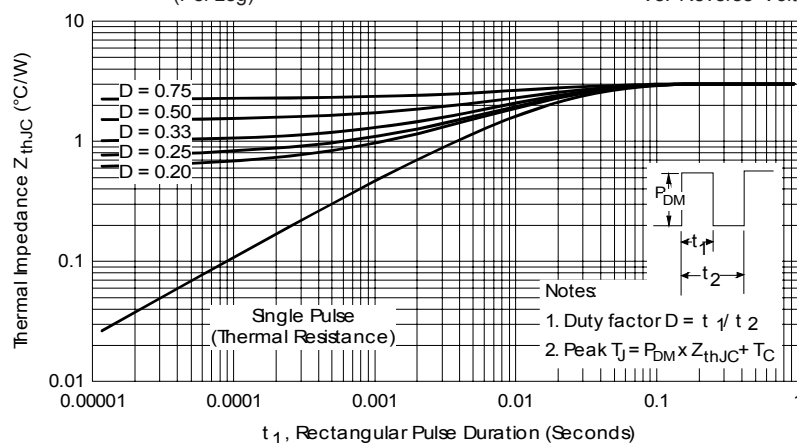


Fig. 4 - Max. Thermal Impedance Z_{thJC} Characteristics (Per Leg)

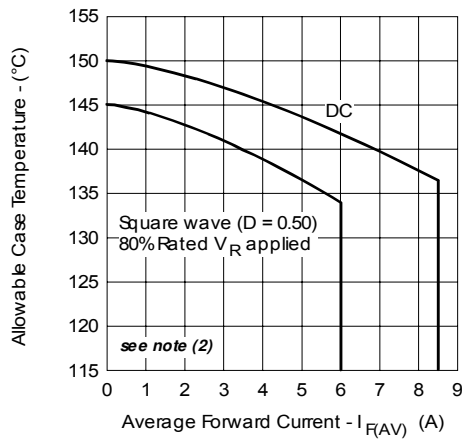


Fig. 5 - Max. Allowable Case Temperature Vs. Average Forward Current (Per Leg)

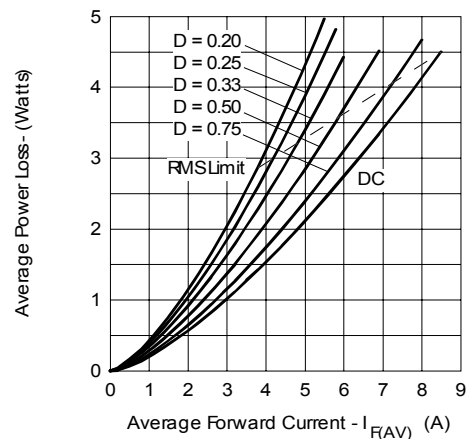


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

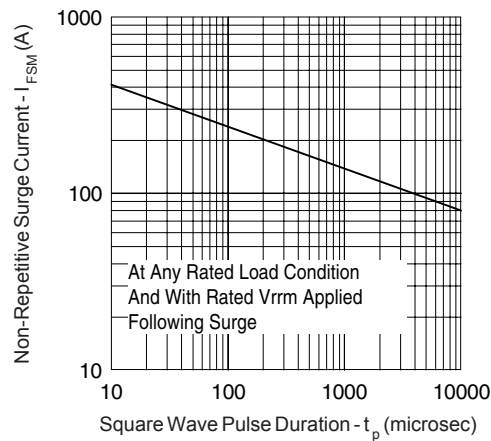


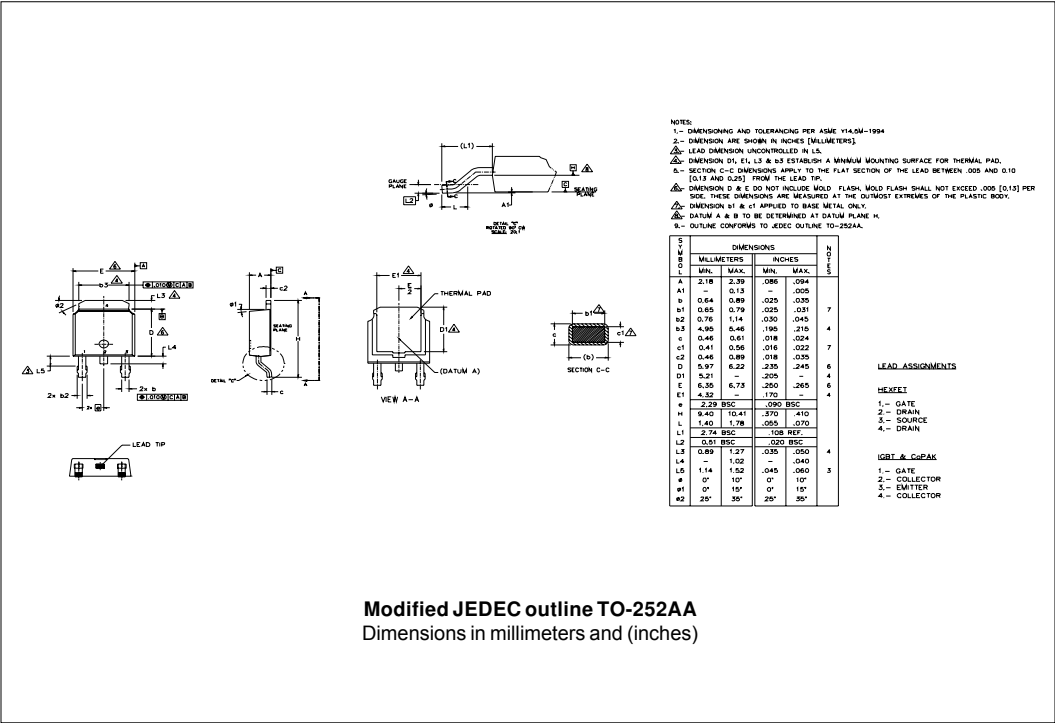
Fig. 7 - Max. Non-Repetitive Surge Current (Per Leg)

(2) Formula used: $T_C = T_J - (P_d + P_{d_{REV}}) \times R_{thJC}$;

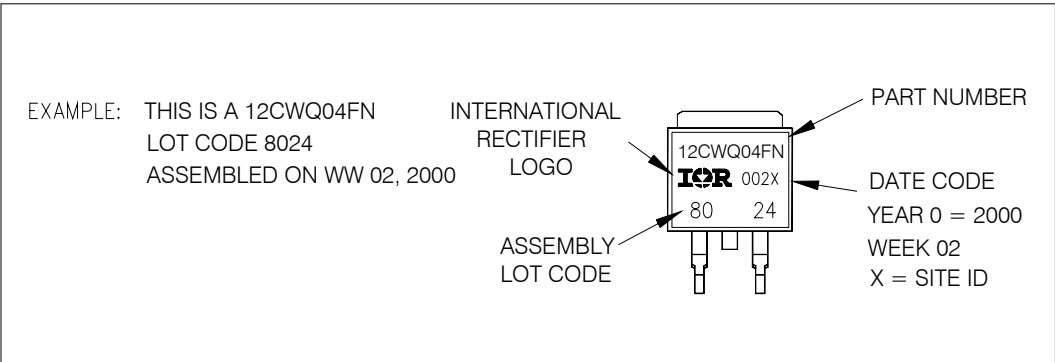
P_d = Forward Power Loss = $I_{F(AV)} \times V_{FM} @ (I_{F(AV)} / D)$ (see Fig. 6);

$P_{d_{REV}}$ = Inverse Power Loss = $V_{R1} \times I_R (1 - D)$; $I_R @ V_{R1} = 80\%$ rated V_R

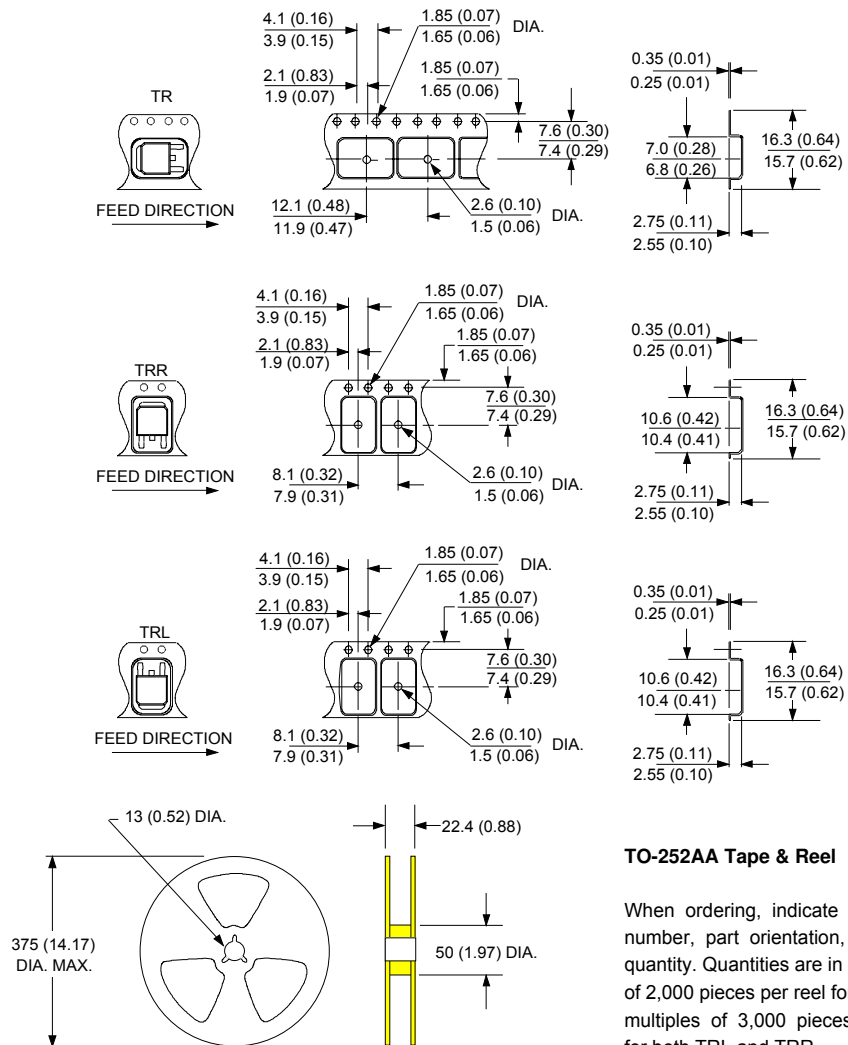
Outline Table



Marking Information



Tape & Reel Information



Ordering Information Table

Device Code							
	12	C	W	Q	04	FN	TRL -
	①	②	③	④	⑤	⑥	⑦ ⑧
1	-	Current Rating (12A)					
2	-	Center Tap Configuration					
3	-	Package Identifier					
		W = D-Pak					
4	-	Schottky "Q" Series					
5	-	Voltage Rating (04 = 40V)					
6	-	FN = TO-252AA					
7	-	• none = Tube (50 pieces) • TR = Tape & Reel • TRL = Tape & Reel (Left Oriented) • TRR = Tape & Reel (Right Oriented)					
8	-	• none = Standard Production • PbF = Lead-Free					

Data and specifications subject to change without notice.
This product has been designed and qualified for AEC Q101 Level.
Qualification Standards can be found on IR's Web site.



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