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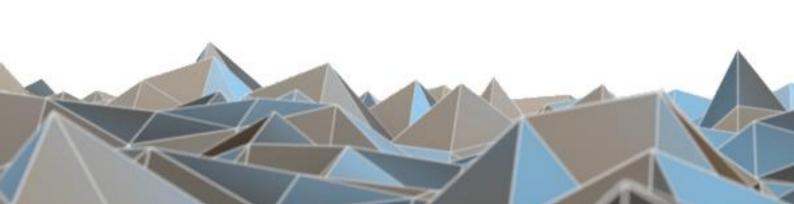
BLUETECHNIX Embedding Ideas

Sentis-ToF-M100

Hardware User Manual

Version 0.10







Bluetechnix

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Sentis-ToF-M100 – Hardware User Manual

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Warning

Due to technical requirements components may contain dangerous substances.



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1 General Information

This guide applies to the Sentis-ToF-M100 camera platform from Bluetechnix GmbH. Follow this guide chapter by chapter to set up and understand your product. If a section of this document only applies to certain camera parts, this is indicated at the beginning of the respective section.

The document applies to X-Grade product from V0.2.0.

1.1 Symbols Used

This guide makes use of a few symbols and conventions:



Warning

Indicates a situation which, if not avoided, could result in minor or moderate injury and/or property damage or damage to the device.



Caution

Indicates a situation which, if not avoided, may result in minor damage to the device, in malfunction of the device or in data loss.



Note

Notes provide information on special issues related to the device or provide information that will make operation of the device easier.

Procedures

A procedure always starts with a headline

1. The number indicates the step number of a certain procedure you are expected to follow. Steps are numbered sequentially.

This sign > indicates an expected result of your action.

References

This symbol indicates a cross reference to a different chapter of this manual or to an external document.



Certification

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X-Grade Version

X-Grade version of the products are not intended for sale and have therefore no certifications. The user is responsible for a correct usage in order with federal laws.

1.3 Safety instructions



1.2

Important

This manual is part of the device and contains information and illustrations about the correct handling of the device and must be read before installation or use. Observe the operating instructions. Non-observance of the instructions, operation which is not in accordance with use as prescribed below, wrong installation or handling can affect the safety of people and machinery.

The installation and connection must comply with the applicable national and international standards. Responsibility lies with the person installing the unit.

1.4 Electrical connection



Note

The unit must be connected by a qualified electrician.

Device of protection class III (PC III).

The electric supply must only be made via PELV circuits.

The device must only be powered by a limited energy source ($\leq 30V$; $\leq 8A$; $\leq 100VA$). Disconnect power before connecting the unit.





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Overview

1.5 Components

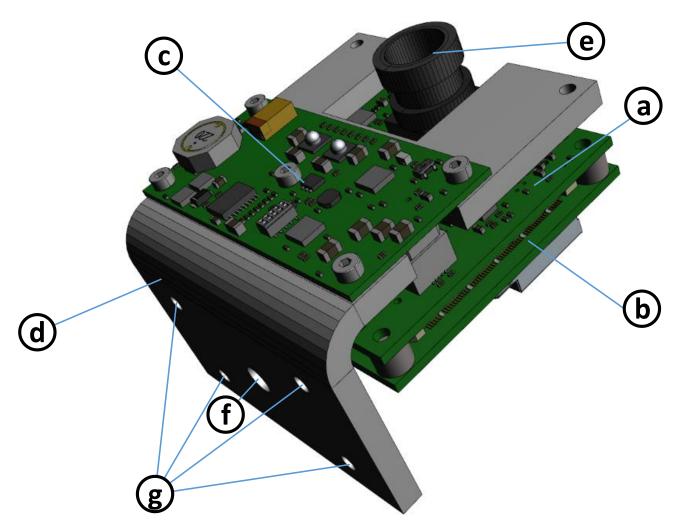


Figure 0-1: Sentis-ToF-M100 components

- a. Main-Board
- b. Interface-Board
- c. LED-Board
- d. Cooling Plate
- e. Sensor Lens
- f. Tripod Socket
- g. Mounting Holes (Use M3 screws for mounting the device to an additional cooling plate)



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1.6 Interfaces and Connectors

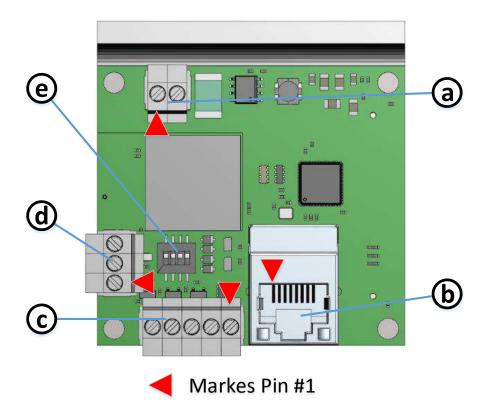


Figure 0-2: Sentis-ToF-M100 connectors and interfaces

- a. Power Supply
- b. Ethernet (RJ45) 10/100Base-T
- c. GPIOs
- d. RS232/485
- e. Configuration DIP-Switch



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2 Hardware Installation

2.1 Mounting



Caution

Cooling plate may become hot!

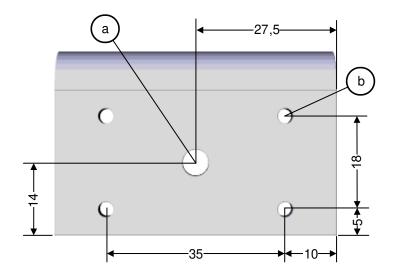


Figure 2-1: Mounting Holes for the Cooling Plate

2.1.1 Tripod Socket (a)

This is a 1/4"-20 screw thread, which allows mounting the Sentis-ToF-M100 to any standard camera tripod.

2.1.2 Mounting Holes (b)

The cooling plate has four M3 screw threads that allows mounting the Sentis-ToF-M100 to a heat spreader.

2.1.3 Mount Spacing

If the Sentis^{ToF} - M100 is used without any additional heat sink attached to the cooling plate, the recommended minimum spacing between hardware and surrounding is 10mm in each direction.

Note

By mounting the camera onto a heat sink, it's allowed to decrease the recommended minimum spacing. In this case the customer is responsible for an adequate cooling.



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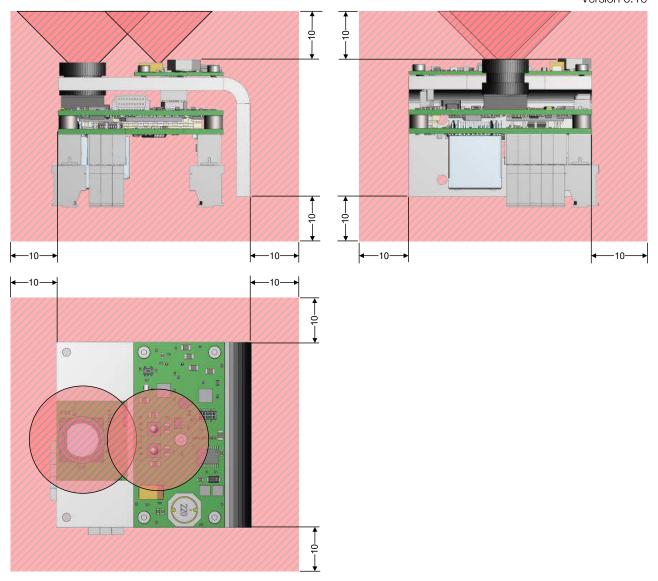


Figure 2-2: Recommended minimum spacing for air circulation

2.2 Processor cooling

In harsh environment or when the Sentis^{ToF} - M100 is used within a case without appropriate cooling it may be necessary to provide a head sink for the processor. Therefore the Interface-Board has a cut-out for gluing an appropriate heat sink onto the processor as shown in the Figure 2-3.





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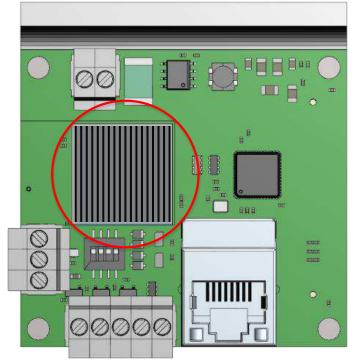


Figure 2-3: Processor with glued heat sink

2.3 Optical Isolation (a)

To prevent direct irradiation from the LED into the camera lens, an optical barrier has to be applied. The following pictures shows the maximum height of two types of such an isolation, dependent of the mounting position.

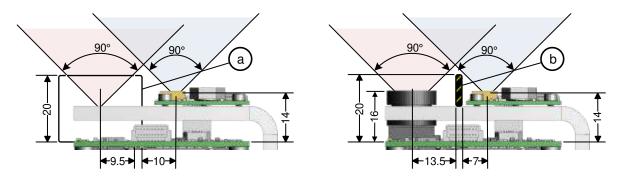


Figure 2-4: Optical Barrier

The isolation can be implemented as a straight barrier (Figure 2-4 b), or a surrounding rectangle barrier (Figure 2-4 a). Figure 2-5 shows a 3D model of both possibilities.



Caution

The optical barrier has to be completely non-transparent for infrared light. Be aware, that many black materials may be transparent for infrared light!



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Figure 2-5: Optical Barriers (3D Model)

Figure 2-6 shows the recommended dimension of a surrounding optical isolator. On the PCB in the rectangle shown, there are no components placed, therefore the barrier can go down to the PCB.

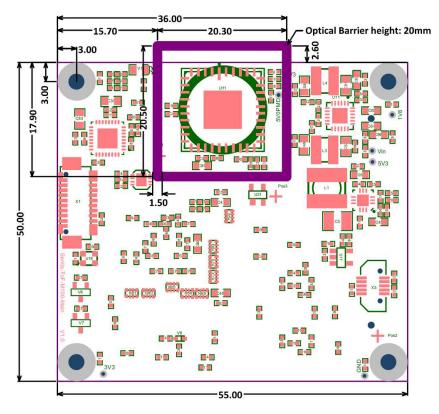


Figure 2-6: Mechanical dimensions for a sensor surrounding optical barrier

2.4 Lens and focus

The M12 lens is not glued onto the lens holder so you can use other lenses or change the focus. If you use other lenses they should be of type "fast lens". Be aware that in that case you may have to recalibrate the sensor.

The sensor array is 7.2mm x 5.4mm.



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2.5 External ToF-Flash

In case you want to use the external ToF-Flash or another external light source you can connect them to the Sentis-ToF-M100 by mounting an appropriate adapter board. Please refer to 3.4.1 to see how to mount the adapter board.



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3 Board Description

3.1 Signal naming

Signal names are usually written in capital letters. They are noted in positive logic (positive asserted). If the signal is negative asserted an "n" will be added as prefix to the signal name.

Type:

The type describes the electrical characteristics of the signal. The following types are available:

- I Input
- O Output
- DN Negative Differential Output
- DP Positive Differential Output
- P Power supply
- 3.3V TTL TTL compatible signal with 3.3V high level and 0V low level
- 5V tolerant Accepts 5V input level

3.2 Connector Numbering

All pins no. 1 of each connector are marked in the figures with a red arrow. The connector numbering always starts at this pin, continuing in this row, and going backwards at the opposite side.



3.3 Main-Board

The Main-Board can be used in connection with a customized daughter-board. In that case you have the possibility to develop a customized board with the interfaces of your need. The Sentis^{ToF} - M100 Main-Board in that case must be connected with the daughter-board using the 100pol board to board connector.

3.3.1 Interface-Board Connector

Mating connector: FX10A-100P/10-SV from Hirose



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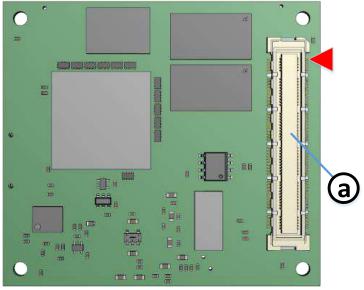


Figure 3-1: Main Board Interface Connector Location

Note

This mezzanine connectors are not reverse polarity protected.

No	Signal	Туре	Description
1	GND	Р	Power Ground
2	GND	Р	Power Ground
3	GND	Р	Power Ground
4	GND	Р	Power Ground
5	GND	Р	Power Ground
6	A1	0	Asynchronous Memory Address 1
7	A2	0	Asynchronous Memory Address 2
8	A3	0	Asynchronous Memory Address 3
9	A4	0	Asynchronous Memory Address 4
10	A5	0	Asynchronous Memory Address 5
11	A6	0	Asynchronous Memory Address 6
12	A7	0	Asynchronous Memory Address 7
13	A8	0	Asynchronous Memory Address 8
14	A9	0	Asynchronous Memory Address 9
15	A10	0	Asynchronous Memory Address 10
16	GND	Р	Power Ground
17	nAMS1	0	Asynchronous Memory Select 1
18	nAMS2	0	Asynchronous Memory Select 2
19	GND	Р	Power Ground
20	SPI.MOSI	0	Serial Peripheral Interface Data Output
21	SPI.MISO	I	Serial Peripheral Interface Data Input
22	SPI.SCLK	0	Serial Peripheral Interface Serial Clock
23	SPI.SS	0	Serial Peripheral Interface Slave Select
24	GND	Р	Power Ground
25	I2C.A0	I	I ² C Slave Address Selection 0
26	I2C.A1	<u> </u>	I ² C Slave Address Selection 1



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No	Signal	Туре	Description
27	PEN	I	Power Enable
28	I2C.SDA	Ю	I ² C Data IO
29	I2C.SCL	I	I ² C Clock Input
30	BM1		Boot-Mode Selection
31	GND	Р	Power Ground
32	PPI.CLK		Parallel Peripheral Interface Clock Input
33	PPI.SYNC1	Ю	Parallel Peripheral Interface Frame Sync 1
34	PPI.SYNC2	10	Parallel Peripheral Interface Frame Sync 2
35	PPI.D7	Ю	Parallel Peripheral Interface Data 7
36	PPI.D6	Ю	Parallel Peripheral Interface Data 6
37	PPI.D5	Ю	Parallel Peripheral Interface Data 5
38	PPI.D4	10	Parallel Peripheral Interface Data 4
39	PPI.D3	Ю	Parallel Peripheral Interface Data 3
40	PPI.D2	Ю	Parallel Peripheral Interface Data 2
41	PPI.D1	Ю	Parallel Peripheral Interface Data 1
42	PPI.D0	10	Parallel Peripheral Interface Data 0
43	IV2	I	Interface Board Version Control 2
44	IV1	I	Interface Board Version Control 1
45	IV0	I	Interface Board Version Control 0
46	3V3	Р	3.3V Power output (max. 200mA)
47	3V3	Р	3.3V Power output (max. 200mA)
48	GND	Р	Power Ground
49	GND	Р	Power Ground
50	GND	Р	Power Ground
51	VIN	Р	Main Supply Voltage (12V to 30V)
52	VIN	Р	Main Supply Voltage (12V to 30V)
53	VIN	Р	Main Supply Voltage (12V to 30V)
54	GND	Р	Power Ground
55	GND	Р	Power Ground
56	SSDI.MnS		Synchronous Serial Data Interface Master/Slave selection
57	SSDI.nSS	IO	Synchronous Serial Data Interface Slave Select
58	SSDI.DIN		Synchronous Serial Data Interface Data Input
59	SSDI.CLK	10	Synchronous Serial Data Interface Clock
60	SSDI.DOUT0	0	Synchronous Serial Data Interface Data Output 0
61	SSDI.DOUT1	0	Synchronous Serial Data Interface data Output 1
62	nRST_OUT	0	Reset Output
63	nRST_IN		Reset Input
64 05	EXT.MOD_N	DN	Differential Modulation Signal for External Flash
65	EXT.MOD_P	DP	Differential Modulation Signal for External Flash
66	OWI	10	One Wire Interface
67 67	PF40	10	GPIO
68 60	PF39	10 10	GPIO
69 70	PF38	10	GPIO
70 71	PF37	10	GPIO GPIO
71 72	PF36 PF11	10 10	GPIO GPIO
72 73	UART.TX	0	UART transmit data
73 74	UART.RX	1	UART receive data
74 75	GND	P	Power Ground
75 76	nARE	P 0	Asynchronous Memory Read Enable
76 77	nARE	0	Asynchronous Memory Write Enable
11	HAWE	0	Asynchionous Memory While Endule



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No	Signal	Туре	Description	
78	nAOE	0	Asynchronous Memory Output Enable	
79	GND	Р	Power Ground	
80	D15	IO	Asynchronous Memory Data 15	
81	D14	IO	Asynchronous Memory Data 14	
82	D13	IO	Asynchronous Memory Data 13	
83	D12	IO	Asynchronous Memory Data 12	
84	D11	Ю	Asynchronous Memory Data 11	
85	D10	Ю	Asynchronous Memory Data 10	
86	D9	IO	Asynchronous Memory Data 9	
87	D8	IO	Asynchronous Memory Data 8	
88	D7	IO	Asynchronous Memory Data 7	
89	D6	Ю	Asynchronous Memory Data 6	
90	D5	Ю	Asynchronous Memory Data 5	
91	D4	Ю	Asynchronous Memory Data 4	
92	D3	Ю	Asynchronous Memory Data 3	
93	D2	Ю	Asynchronous Memory Data 2	
94	D1	Ю	Asynchronous Memory Data 1	
95	D0	Ю	Asynchronous Memory Data 0	
96	VLED	Р	LED Supply Voltage (12V to 30V)	
97	VLED	Р	LED Supply Voltage (12V to 30V)	
98	VLED	Р	LED Supply Voltage (12V to 30V)	
99	VLED	Р	LED Supply Voltage (12V to 30V)	
100	VLED	Р	LED Supply Voltage (12V to 30V)	

Table 3-1: 100-pole Main Board Connector Description

3.3.2 Debug Interface

The debug interface is only needed if you want to develop your own application running on the Sentis^{ToF} - M100. Please refer to 4.4.

The mating connector types are: IL-WX-20PB-VF-BE (straight)

IL-WX-20PB-VF-BE (straight) IL-WX-20PB-HF-HD-S-BE (right angle)

Note

ā

This connector is not reverse polarity protected.



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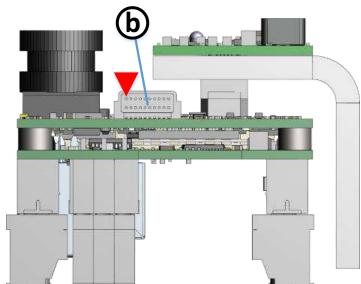


Figure 3-2: Debug Connector Location

No	Signal	Туре	Description
1	SSDI.TCLK	Ю	Synchronous Serial Data Interface Clock
2	SSDI.DTPRI	0	Synchronous Serial Data Interface Clock
3	SSDI.DRPRI	I	Synchronous Serial Data Interface Clock
4	SSDI.TFS	Ю	Synchronous Serial Data Interface Frame Synchronization
5	JTAG.EMU	0	Blackfin JTAG Interface
6	JTAG.TMS	I	Blackfin JTAG Interface
7	JTAG.TCK	I	Blackfin JTAG Interface
8	JTAG.TRST	I	Blackfin JTAG Interface
9	JTAG.TDI	I	Blackfin JTAG Interface
10	JTAG.TDO	0	Blackfin JTAG Interface
11	3V3	Р	3.3V Power Output (max. 50mA)
12	GND	Р	Power Ground
13	Reserved		Leave this pin unconnected
14	Reserved		Leave this pin unconnected
15	GND	Р	Power Ground
16	Reserved		Leave this pin unconnected
17	Reserved		Leave this pin unconnected
18	PF38	10	Blackfin GPIO
19	PF39	Ю	Blackfin GPIO
20	GND	Р	Power Ground

Table 3-2: Debug Connector Interface Description



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3.3.3 LED-Board Interface

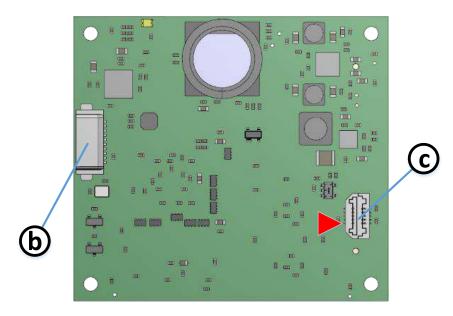


Figure 3-3: LED-Board Interface Connector Location

No	Signal	Туре	Description
1	I2C.SDA	IO	I ² C Data IO
2	I2C.SCL	0	I ² C Clock Output
3	LED.EN0	0	LED Enable
4	LED.MOD_N	DN O	Negative LVDS Modulation Signal
5	LED.MOD_P	DP O	Positive LVDS Modulation Signal
6	VLED	Р	LED Voltage Supply
7	VLED	Р	LED Voltage Supply
8	GND	Р	Power Ground
9	GND	Р	Power Ground
10	3V3	Р	3.3V Supply (max. 50mA)

Table 3-3: LED-Board Interface Description

3.4 ToF-Flash Adapter

3.4.1 Adapter Assembling

In case it would be necessary to use an external illumination to extend the range of the Sentis^{ToF} - M100 you can use the modulation signal interface to synchronize this light source with the Sentis^{ToF} - M100. There ate two Adapters, one with the connector facing inside the module, the other facing outside. Perform the following steps to modify the Sentis^{ToF} - M100 for external illuminations.

1. Remove the two mounting screws at the board edge as shown in the picture below



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2. Remove the LED-Board while leaving the Cooling Plate.



3. Attach the modulation light interface adapter



4. Screw the adapter with two of the LED-Board screws



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3.4.2 Modulation Signal Interface

Once the adapter is mounted you can connect an external illumination module using the "Modulation Signal Interface" connector (a) on the Adapter.

Mating Connector Type: MQ172X-4SA-CV

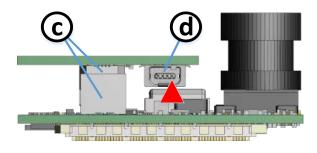


Figure 3-4: Modulation Light Connector Location

The Modulation Light Interface provides the modulation signal for an external illumination module (differential LVDS).



Caution

Overvoltage on the Modulation Light Interface will destroy the Sentis-ToF-M100.

Pin No.	Signal Name	Туре	Description
1	DATA_EX ¹⁾	IO (3.3V TTL)	Data exchange signal
2	EXT.MOD_N	DN	Modulation signal output-
3	EXT.MOD_P	DP	Modulation signal output+
4	GND	Р	Reference ground

Table 3-4: Modulation Signal Interface



1) Note

The usage of this pin may depend on the firmware version.



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3.5 Interface-Board

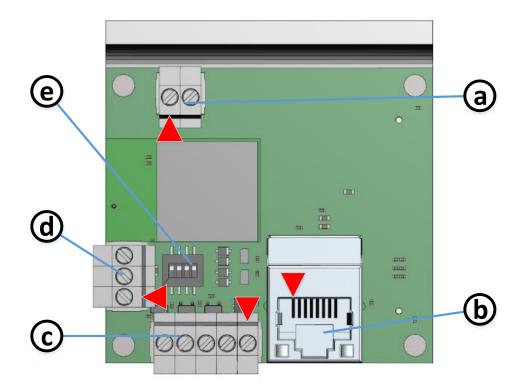


Figure 3-5: Interface Board Connector Location

3.5.1 Main-Board Connector

The Main-Board Connector is described in chapter 3.3.1.

3.5.2 Power Connector (a)

This 3.5mm terminal connector allows plugging a cable entry plug like: **691361100002** from Würth Elektronik. Compatible connectors from other manufacturers may be found as well.

Pin #1 is the positive supply voltage, pin #2 is power ground. This pins are protected against wrong polarity.

Voltage range: 12V to 30V.



Note

Use inherently limited power sources only!

3.5.3 Ethernet (b)

This is a standard straight RJ45 10/100 Base-T compatible Ethernet connector.



3.5.4 GPIO (c)

This 5 pole 3.5mm terminal connector allows plugging a cable entry plug like: **691361100005** from Würth Elektronik.

No.	BF561 Signal	Туре	Description
1	GND	Р	Signal Ground
2	PF38	I	General Purpose Input
3	PF37 PF47	l O (open drain)	General Purpose Input and Output
4	PF36 PF40	l O (open drain)	General Purpose Input and Output
5	PF11 PF39	l O (open drain)	General Purpose Input and Output

Table 3-5: GPIO Connector Description

For the software usage of this GPIOs please refer to the Software User Manual.

This GPIO pins have 12V tolerant inputs. If they are used as outputs, they must be externally pulled up to the IO-voltage.



Caution

Overvoltage on the GPIO input pins (12V max) may permanently damage the device.

3.5.5 RS232/RS485 (d)

This 3 pole 3.5mm terminal connector allows plugging a cable entry plug like: **691361100003** from Würth Elektronik.

No.	Signal	Туре	Description
1	GND	Р	Signal Ground
2	RS232 RxD ¹⁾	IO	RS232 Receive Data
	RS485 A/Y	DN	RS485 Negative Differential Data
3	RS232 TxD ¹⁾	IO	RS232 Transmit Data
	RS485 B/Z	DP	RS485 Positive Differential Data

Table 3-6: GPIO Connector Description



1) Note

The Interface-Mode can be selected with the DIP-Switch 1 (see table below).

Both interfaces are running in half duplex mode only.



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3.5.6 DIP-Switch (e)

The DIP-Switch allows configuring the RS232/RS485 transceiver. The following table shows the functionality of each switch.

No.	Name	Description
1	Boot Mode	ON: Boot from 8-bit/16-bit flash OFF: Boot from SPI serial EEPROM
2	RS485 Enable	ON: Transceiver works in RS485 mode OFF: Transceiver works in RS232 mode
3	NU	Not Used
4	RS485 Termination	ON: Enables the 120Ω RS485 termination resistor OFF: No termination resistor is active

Table 3-7: DIP-Switch Description



Note

Make sure that the termination resistor is always disabled, if the driver runs in RS232 mode.