



Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of “Quality Parts,Customers Priority,Honest Operation,and Considerate Service”,our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



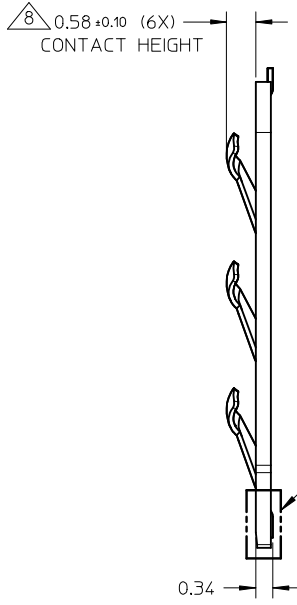
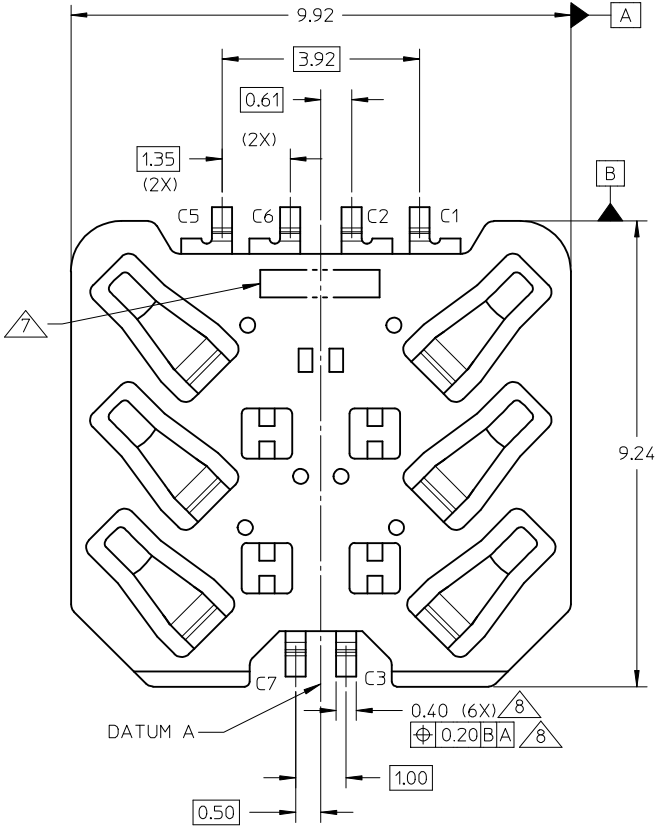
Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

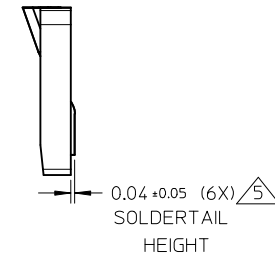
Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China





DETAIL 1

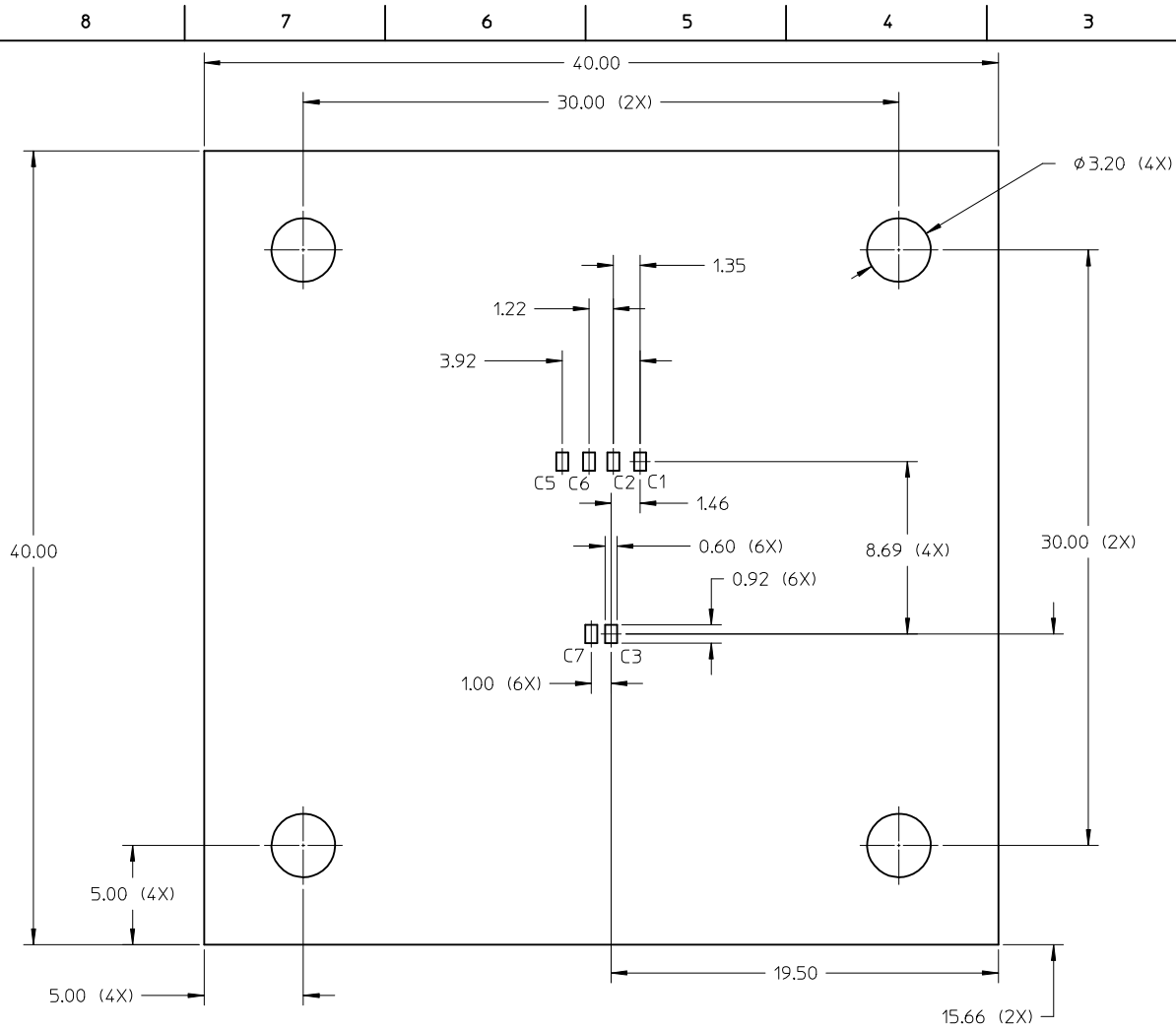


DETAIL 1

NOTES:

1. MATERIALS:
 - 1.1 CONNECTOR:-
 - INSERT MOLD HOUSING: LCP, GLASS FILLED, UL94V-0, COLOUR BLACK.
 - TERMINAL: COPPER ALLOY.
2. PLATING FINISHES:
 - 2.1 TERMINAL:-
 - CONTACT: 0.50um MIN. GOLD ON CONTACT AREA OVER 1.27um MIN. NICKEL UNDERPLATE.
 - SOLDERTAIL: 2.00um MIN. MATT TIN OVER 1.27um MIN. NICKEL UNDERPLATE.
3. PRODUCT SPECIFICATION: PS-151059-0001
4. PACKAGING SPECIFICATION: PK-151059-0001
5. OVERALL SOLDERTAIL COPLANARITY 0.08MM MAX. BEFORE REFLOW.
6. COMPLIANT TO RoHS DIRECTIVE 2011/65/EU & ELV DIRECTIVE 2000/53/EC.
7. DATE CODE LOCATION (YYWWD)

PAD NO. CORRECTION EC NO: S2015-0928 DRWN: JTAN02 CHKD: SCHEONG APPR: KHL IM	DESCRIPTION 2015/02/17 2015/02/24 2015/02/26	QUALITY SYMBOLS	GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE MM ONLY		SCALE NTS	DESIGN UNITS METRIC	THIRD ANGLE PROJECTION	
		$F_A = 0$ $F_G = 0$ $F_P = 0$	mm INCH 4 PLACES ± --- ± --- 3 PLACES ± --- ± --- 2 PLACES ± 0.13 ± --- 1 PLACE ± --- ± --- 0 PLACE ± --- ± ---	DRAWN BY JTAN02	DATE 2014/12/02	TITLE NANO BLOCK SIM, 0.34MM HEIGHT				
		ANGULAR ± 1 ° DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS	MATERIAL NO. 1510590001	APPROVED BY KHL IM	DATE 2014/12/19	DOCUMENT NO. SD-151059-0001 				
		SIZE A3	THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION		SHEET NO. 1 OF 2					



RECOMMENDED PWB LAYOUT
 (NON SOLDER MASK DEFINE PWB)
 TOLERANCE: +0.05MM
 PWB THICKNESS: 0.80MM

PAD NO. CORRECTION EC NO: S2015-0928 DRWN: JTAN02 CHKD: SCHEONG APPR: KHLIM	2015/02/17 2015/02/24 2015/02/26	QUALITY SYMBOLS $F_A=0$ $F_G=0$ $F_P=0$	GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE MM ONLY		SCALE NTS	DESIGN UNITS METRIC	THIRD ANGLE PROJECTION			
						DRAWN BY JTAN02	DATE 2014/12/02	TITLE NANO BLOCK SIM, 0.34MM HEIGHT				
							CHECKED BY SCHEONG	DATE 2014/12/02				
							APPROVED BY KHLIM	DATE 2014/12/19				
A1	REV	DESCRIPTION PAD NO. CORRECTION	ANGULAR $\pm 1^\circ$ DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS		MATERIAL NO. 1510590001	DOCUMENT NO. SD-151059-0001	SHEET NO. 2 OF 2	THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION				