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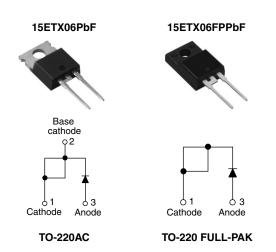
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Vishay High Power Products

Hyperfast Rectifier, 15 A FRED Pt[™]



PRODUCT SUMMARY				
t _{rr}	18 ns			
I _{F(AV)}	15 A			
V _R	600 V			

FEATURES

- Benchmark ultralow forward voltage drop
- Hyperfast recovery time
- Low leakage current
- 175 °C operating junction temperature
- Fully isolated package (V_{INS} = 2500 V_{RMS})
- Lead (Pb)-free ("PbF" suffix)
- Designed and qualified for industrial level

DESCRIPTION/APPLICATIONS

State of the art hyperfast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recovery time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC boost stage in the AC-DC section of SMPS, inverters or as freewheeling diodes.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Peak repetitive reverse voltage	V _{RRM}		600	V	
Average rectified forward current		T _C = 133 °C	- 15		
Average rectined forward current	I _{F(AV)}	T _C = 62 °C (FULL-PAK)	15	А	
Non-repetitive peak surge current	I _{FSM}	T _J = 25 °C	170	A	
Peak repetitive forward current	I _{FM}		30		
Operating junction and storage temperatures	T _J , T _{Stg}		- 65 to 175	۵°C	

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	600	-	-	
Forward voltage V _F	V	I _F = 15 A	-	2.3	3.2	V
	۷F	I _F = 15 A, T _J = 150 °C	-	1.5	1.8	
		$V_{R} = V_{R}$ rated	-	0.1	50	
Reverse leakage current	I _R	$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	40	300	μΑ
Junction capacitance	CT	V _R = 600 V	-	20	-	pF
Series inductance	L _S	Measured lead to lead 5 mm from package body		8.0	-	nH

* Pb containing terminations are not RoHS compliant, exemptions may apply



RoHS

COMPLIANT

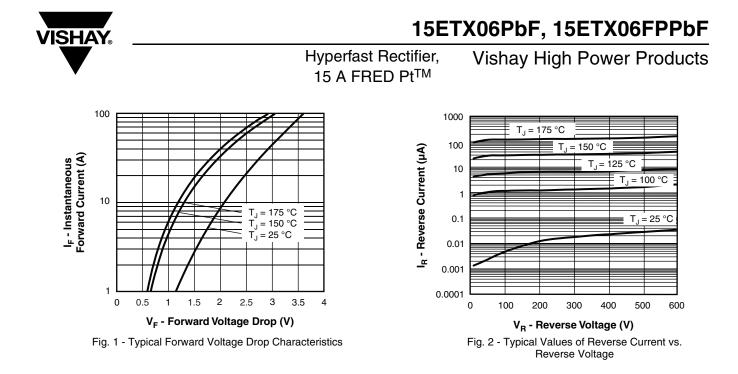
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DYNAMIC RECOVERY CHARACTERISTICS ($T_c = 25$ °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Reverse recovery time		$I_F = 1 \text{ A}, \text{ d}I_F/\text{d}t = 100 \text{ A}/\mu\text{s}, \text{ V}_R = 30 \text{ V}$		-	18	22	
	.	$I_F = 15 \text{ A}, \text{ d}I_F/\text{d}t = 100 \text{ A}/\mu\text{s}, \text{ V}_R = 30 \text{ V}$		-	20	32	
	t _{rr}	T _J = 25 °C	$I_F = 15 A$	-	22	-	ns
		T _J = 125 °C		-	52	-	
Peak recovery current I _{RRM}		T _J = 25 °C		-	2.4	-	•
	T _J = 125 °C	dI _F /dt = 200 A/µs V _B = 390 V	-	5.1	-	A	
Reverse recovery charge	Q _{rr}	T _J = 25 °C	n	-	25	-	μC
		T _J = 125 °C		-	150	-	
Reverse recovery time	t _{rr}	T _J = 125 °C	I _F = 15 A	-	37	-	ns
Peak recovery current	I _{RRM}		dI _F /dt = 800 A/µs	-	16	-	А
Reverse recovery charge	Q _{rr}		V _R = 390 V	-	350	-	nC

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		- 65	-	175	°C
Thermal resistance,	Р		-	1.0	1.3	
junction to case (FULL-PAK)	R _{thJC}		-	3.0	3.5	
Thermal resistance, junction to ambient per leg	R _{thJA}	Typical socket mount	-	-	70	°C/W
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.5	-	
MART - La			-	2.0	-	g
Weight			-	0.07	-	oz.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf ⋅ cm (lbf ⋅ in)
Marking davias		Case style TO-220AC	15ETX06			
Marking device		Case style TO-220 FULL-PAK	15ETX06FP			



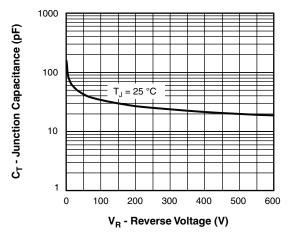


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

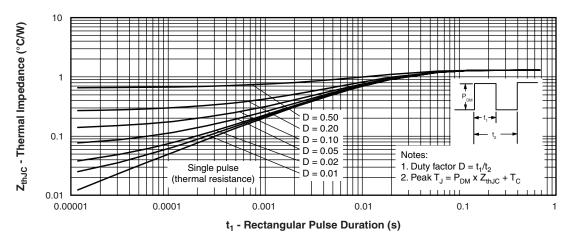
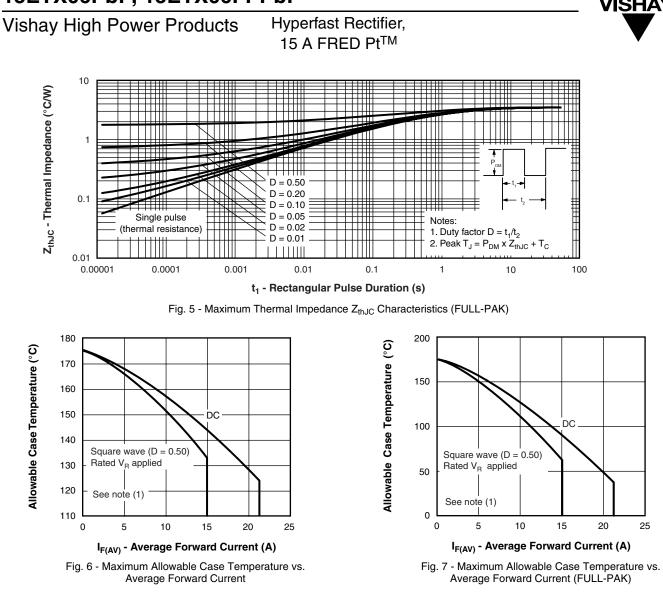


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics



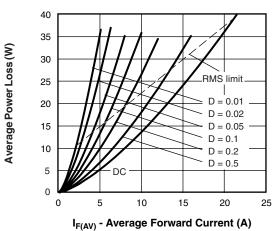


Fig. 8 - Forward Power Loss Characteristics

Note

(1)

 $\begin{array}{l} \mbox{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \ x \ R_{th,JC}; \\ Pd = \mbox{Forward power loss} = I_{F(AV)} \ x \ V_{FM} \ at \ (I_{F(AV)}/D) \ (see \ fig. \ 8); \\ Pd_{REV} = \mbox{Inverse power loss} = V_{R1} \ x \ I_R \ (1 - D); \ I_R \ at \ V_{R1} = \ Rated \ V_R \end{array}$

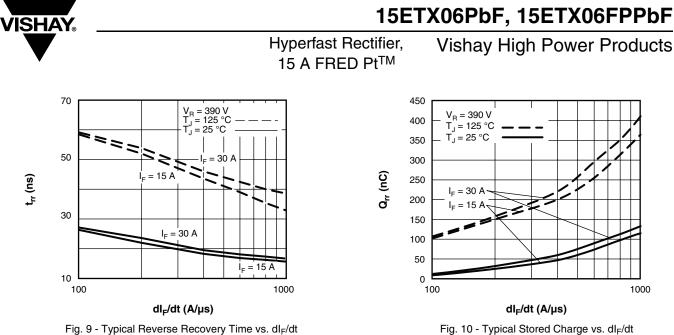


Fig. 10 - Typical Stored Charge vs. dI_F/dt

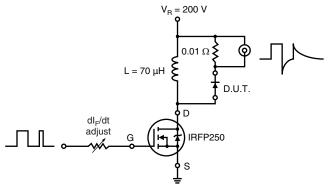


Fig. 11 - Reverse Recovery Parameter Test Circuit

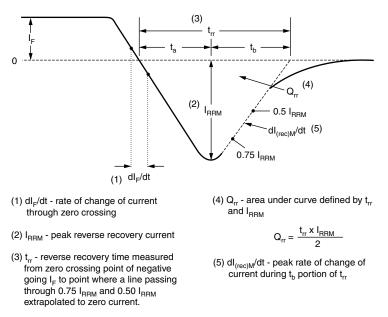


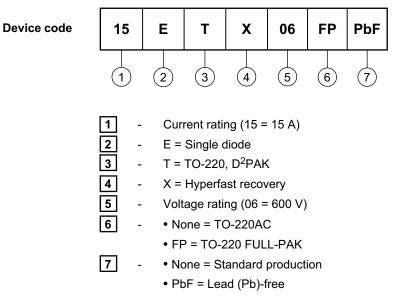
Fig. 12 - Reverse Recovery Waveform and Definitions

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ORDERING INFORMATION TABLE



Tube standard pack quantity: 50 pieces

LINKS TO RELATED DOCUMENTS				
Dimensions http://www.vishay.com/doc?95039				
Part marking information	http://www.vishay.com/doc?95045			



Vishay

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