



Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of “Quality Parts,Customers Priority,Honest Operation,and Considerate Service”,our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



## Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: [info@chipsmall.com](mailto:info@chipsmall.com) Web: [www.chipsmall.com](http://www.chipsmall.com)

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China



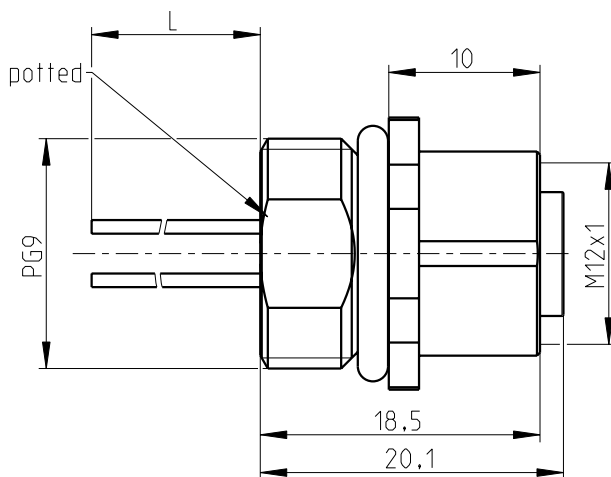
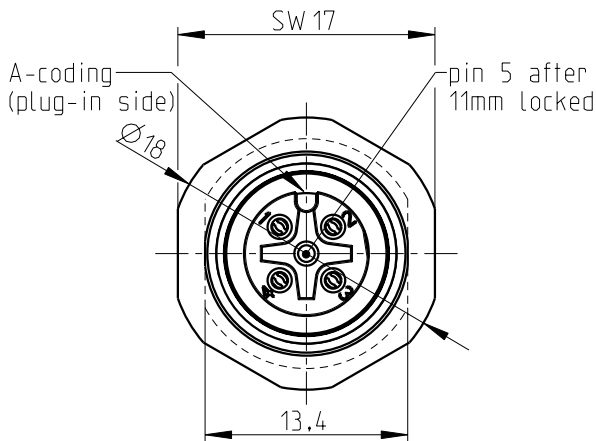
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CONINVERS GmbH Heisenbergstrasse 1 D-71083 Herrenberg

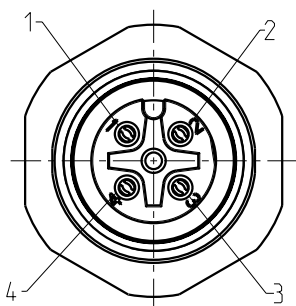
| Mechanical data<br>(according to IEC 61076-2-101) |                 | Material           |                     |
|---|-----------------|--------------------|---------------------|
| no. of poles / coding                             | 4 / A           | housing / surface  | GD-Zn / Ni          |
| contact termination                               | crimp / socket  | contact carrier    | PA 66 / UL 94 V0    |
| contact-Ø   | 1mm             | colour             | black               |
| temperature range                                 | -40 °C / +85 °C | wire               | TPE                 |
| protection class                                  | IP 67           | cross section      | 0,34mm <sup>2</sup> |
| mating cycles                                     | >100            | contacts / surface | CuZn / Au           |
| locking system                                    | M12 STANDARD    | sealing gasket     | FKM                 |
|   |                 | potting            | PU 2k / UL 94 V0    |

further information:  
- see technical data M12-1  
- see installation recommendation PG9 design E



All specified data refers to the mounted M12 installation plugs ≥ IP54 and requires that the connector pair be correctly plugged and locked. Influences from wires, labels or PCB mounting are also to be taken into consideration.


View of pin assignment  
Plug-in side



Pin layout

| pin | colour |
|-----|--------|
| 1   | brown  |
| 2   | white  |
| 3   | blue   |
| 4   | black  |

| order-no. | L (m) |
|-----------|-------|
| 1693788   | 0,5   |
| 1503276   | 1,0   |
| 1406413   | 2,0   |
| 1567694   | 4,0   |

|   |            |       |   |                                     |                      |  |               |                  |  |
|---|------------|-------|---|-------------------------------------|----------------------|--|---------------|------------------|--|
| general tolerance<br>acc. to DIN ISO 2768-c   |            |       | technical changes reserved  |                                     | Index 2 / 2012-06-05 |  | scale:<br>2:1 |                  |  |
|   | date       | name  | description:<br><br>Housing with crimp contacts<br>Front mounting (PG9) |                                     |                      |  |               | page: 1<br>of: 1 |  |
| design  | 2010-11-23 | Thumm |   |                                     |                      |  |               |                  |  |
| chk.  |            |       |   |                                     |                      |  |               |                  |  |
|   |            |       |   |                                     |                      |  |               |                  |  |
|  |            |       | drawing-no.:  | BA-L15RAC0BX,XX (X,XX=L)            |                      |  | doc.-no.:     | 00665103         |  |
|   |            |       | order-no.:  | see chart                           |                      |  |               |                  |  |
|   |            |       | article designation:  | SACC-E-M12FS-4CON-PG9/L (see chart) |                      |  |               |                  |  |
|   |            |       |   |                                     |                      |  |               |                  |  |