

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China

- 1N6702US AVAILABLE IN JAN, JANTX AND JANTXV
- 5 AMP SCHOTTKY BARRIER RECTIFIERS
- HERMETICALLY SEALED
- LEADLESS PACKAGE FOR SURFACE MOUNT
- METALLURGICALLY BONDED, DOUBLE PLUG CONSTRUCTION

1N6700US thru 1N6702US
and
CDLL6700 thru CDLL6702US
and
CDLL5A20 thru CDLL5A40

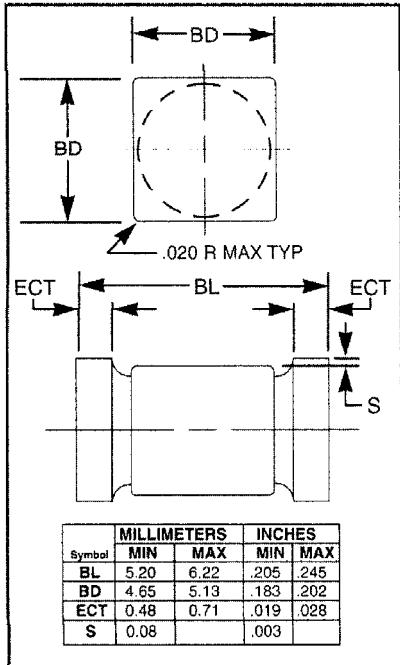
MAXIMUM RATINGS

Operating Temperature: -65°C to +125°C

Storage Temperature: -65°C to +150°C

Average Rectified Forward Current: 5.0 AMP, $T_{EC} = +55^\circ\text{C}$

Derating: 71 mA / °C above $T_{EC} = +55^\circ\text{C}$



ELECTRICAL CHARACTERISTICS @ 25°C, unless otherwise specified.

CDI TYPE NUMBER	WORKING PEAK REVERSE VOLTAGE	MAXIMUM FORWARD VOLTAGE			MAXIMUM REVERSE LEAKAGE CURRENT AT RATED VOLTAGE	
		V_{RWM}	$V_F @ 3.0\text{A}$	$V_F @ 5.0\text{A}$	$V_F @ 15.7\text{A}$	$I_R @ 25^\circ\text{C}$
		VOLTS	VOLTS	VOLTS	VOLTS	mA
1N6700US	20	0.44	0.47	0.60	0.70	0.20
1N6701US	30	0.44	0.47	0.60	0.70	0.20
1N6702US	40	0.44	0.47	0.60	0.70	0.20
CDLL6700	20	0.50	0.55	0.70	0.70	0.20
CDLL6701	30	0.50	0.55	0.70	0.70	0.20
CDLL6702	40	0.50	0.55	0.70	0.70	0.20
CDLL5A20	20	0.50	0.55	0.70	0.70	0.20
CDLL5A30	30	0.50	0.55	0.70	0.70	0.20
CDLL5A40	40	0.50	0.55	0.70	0.70	0.20

FIGURE 1

DESIGN DATA

CASE: D-5C, Hermetically sealed glass case.

LEAD FINISH: Tin / Lead

THERMAL RESISTANCE: Junction to End Cap = 10 °C/W maximum

POLARITY: Cathode end is identified

MOUNTING POSITION: ANY.

MOUNTING SURFACE SELECTION:
The Axial Coefficient of Expansion (COE) of this device is approximately + 4PPM / °C. The COE of the Mounting Surface System should be selected to provide a suitable match with this device.

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and
CDLL5A20 thru CDLL5A40**

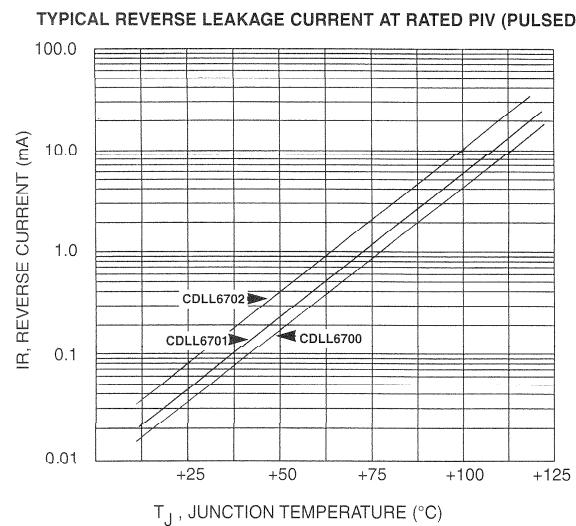


FIGURE 1

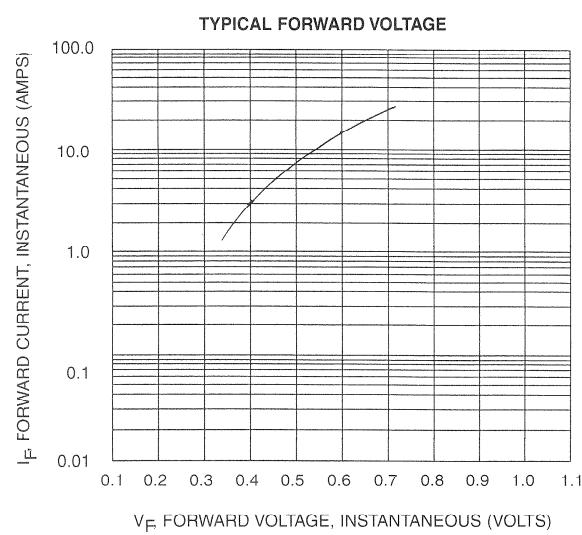


FIGURE 2