



Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of “Quality Parts,Customers Priority,Honest Operation,and Considerate Service”,our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

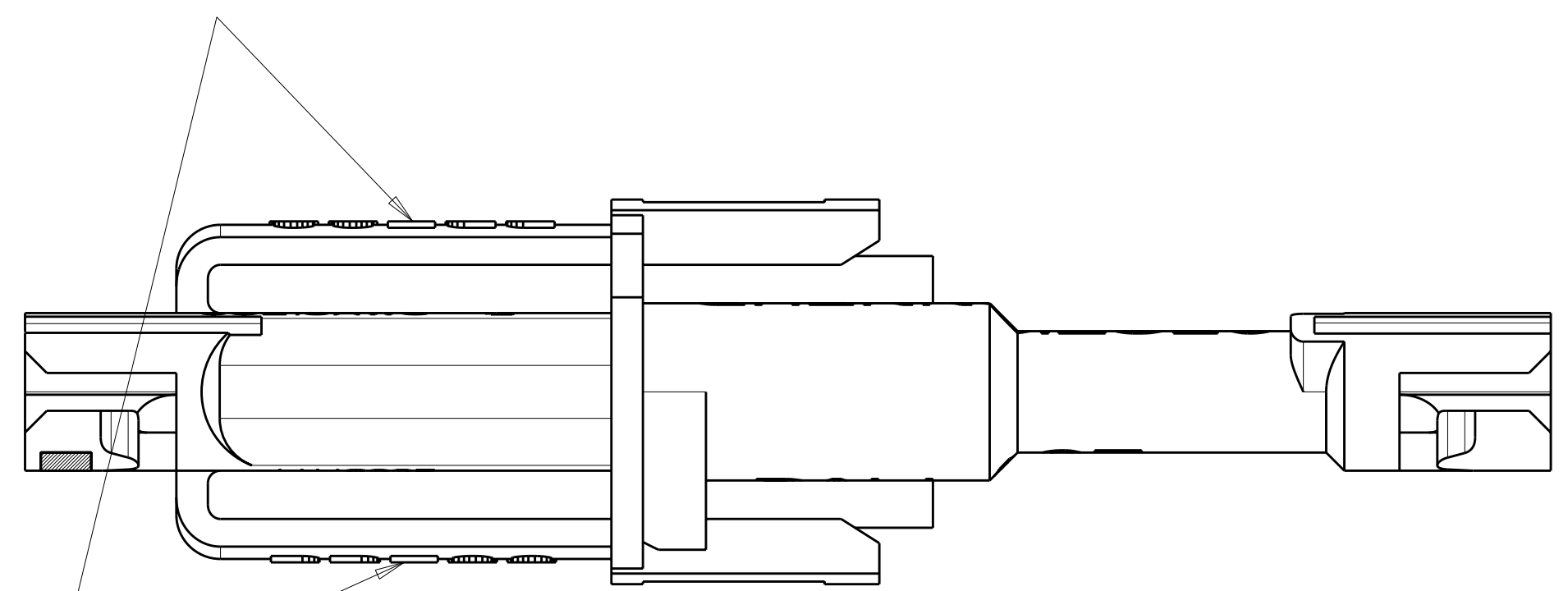
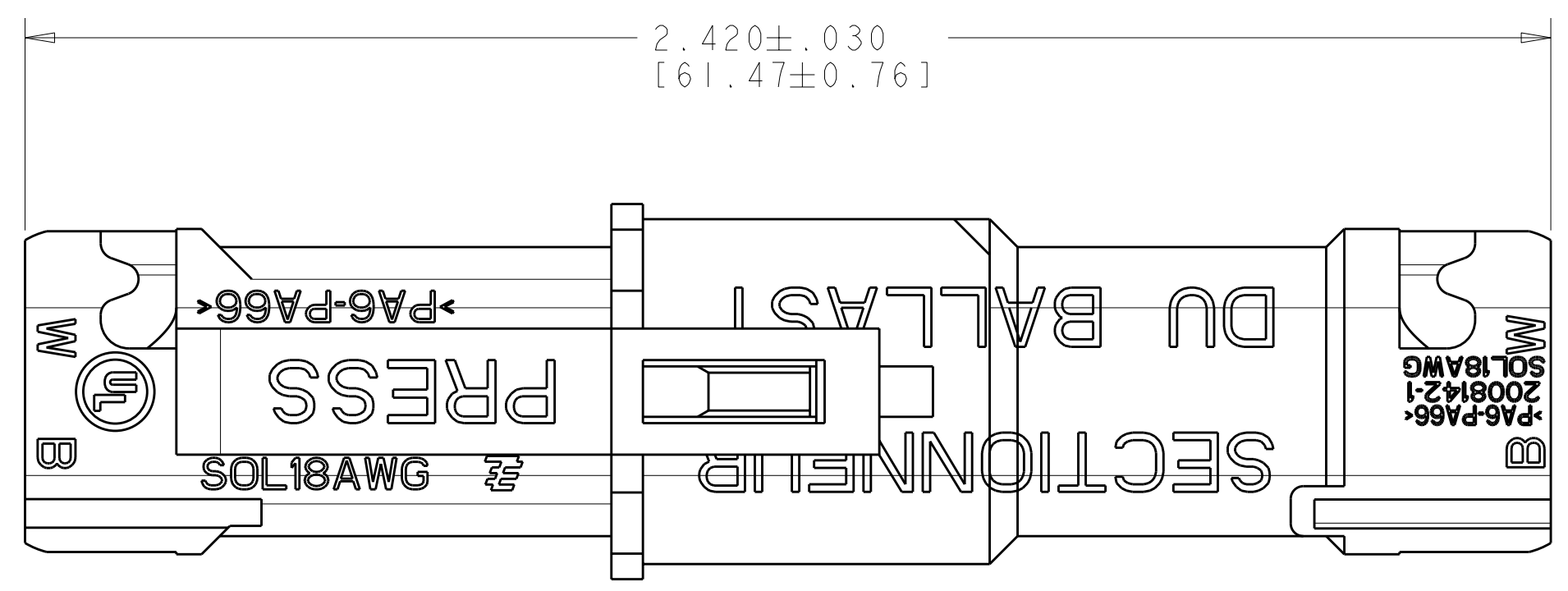
Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

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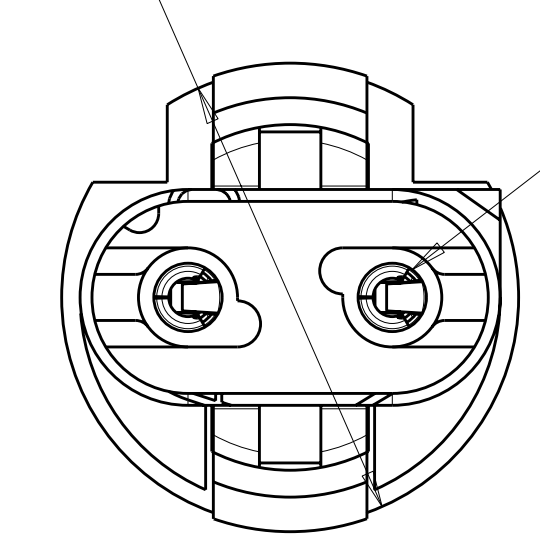
Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China



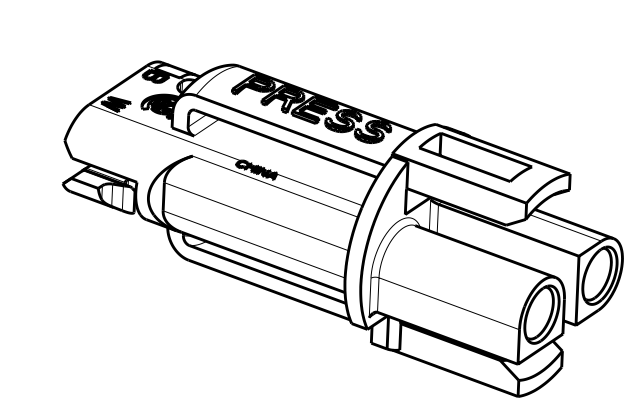
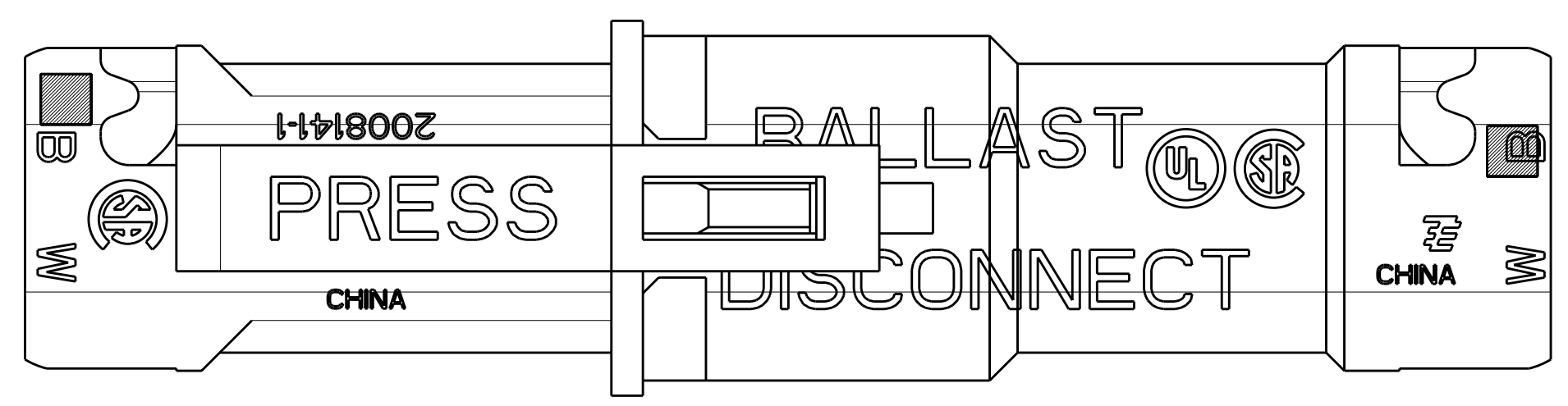
LOC	DIST	REVISIONS			
REV	DATE	DESCRIPTION	DATE	DWN	APVD
A1		REVISED PER ECO-11-005027	14MAR2011	RK	HMR



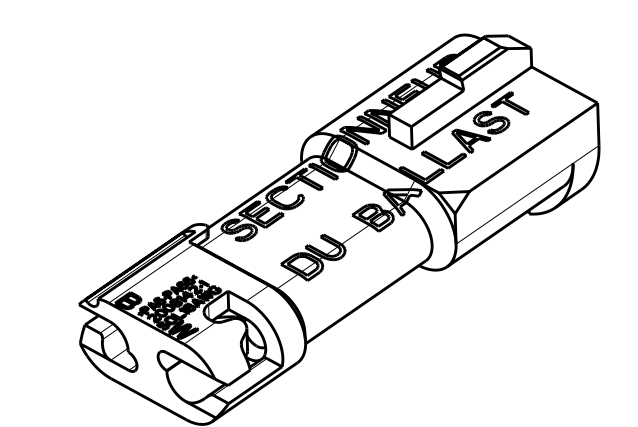
Ø .595 ± .010
[15.11 ± 0.25]



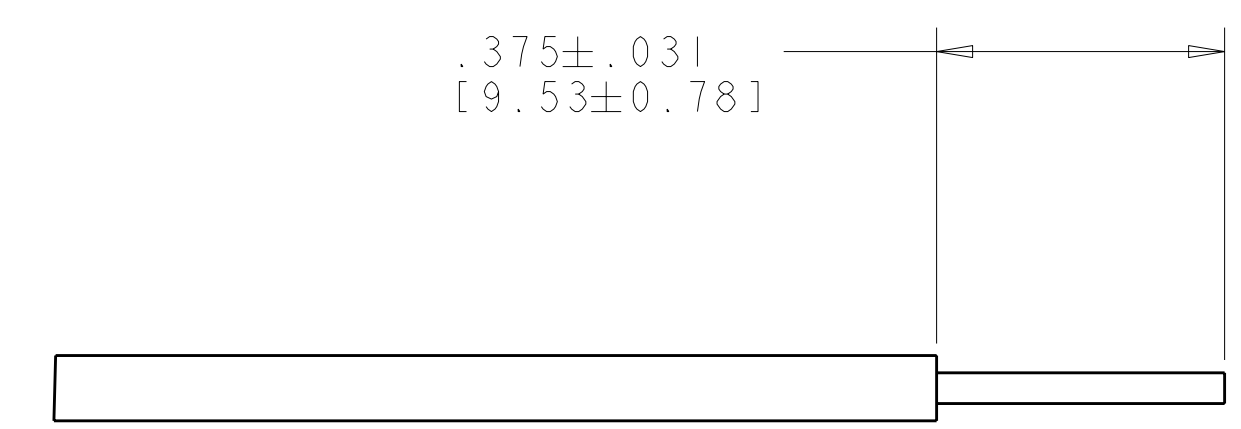
ACCEPTS 18 AWG SOLID WIRE
WITH .086 [2.18] MAX INSULATION DIAMETER
BOTH CONNECTORS



PLUG CONNECTOR ASSEMBLY



RECEPTACLE CONNECTOR ASSEMBLY



RECOMMENDED WIRE STRIP LENGTH

2008144-1
PART NUMBER

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN C. DAILY 23APR2007	TE Connectivity	
DIMENSIONS: IN [mm]		CHK J. LANDIS 23APR2007	NAME ASSEMBLY, MATED PAIR, 2 POSITION PLUG AND RECEPTACLE PT, LIGHT-N-LOK	
TOLERANCES UNLESS OTHERWISE SPECIFIED:		APVD	PRODUCT SPEC 108-2275	RESTRICTED TO
9 PLC ±		APVD	APPLICATION SPEC 114-13204	SCALE 2:1 SHEET 1 OF 1 REV A1
3 PLC ±		APVD	WEIGHT	
5 PLC ±		APVD	CUSTOMER DRAWING	
4 PLC ±		APVD		
ANGLES ±		APVD		
FINISH ±		APVD		