



Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of “Quality Parts,Customers Priority,Honest Operation,and Considerate Service”,our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

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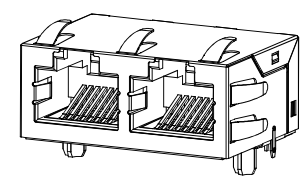
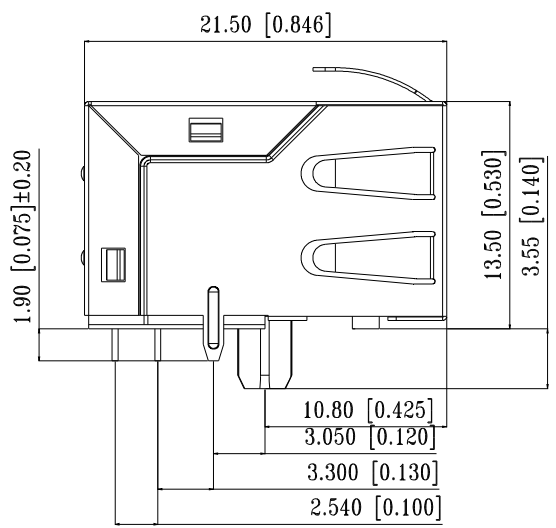
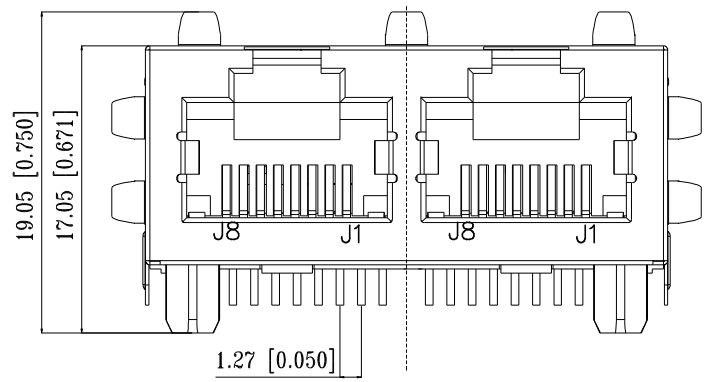
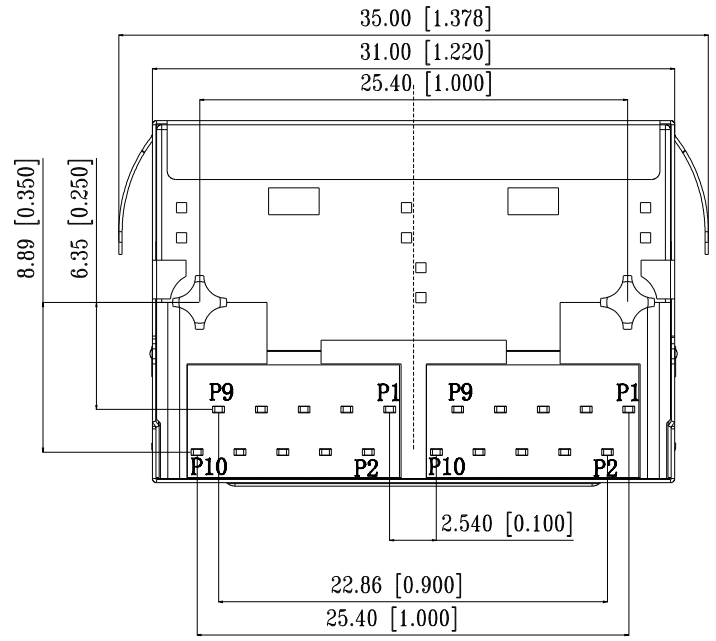
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REVISIONS

P	LTR	DESCRIPTION	DATE	DWN	APVD
	A1	NEW RELEASE	03MAR2017	GCM	MSZ



- 1 CONNECTOR MATERIAL:
 HOUSING: LCP BLACK UL94 V-0
 INSERT: LCP BLACK UL94 V-0
 SHIELD: BRASS
 SHIELD PLATING: NICKEL
 CONTACT: COPPER ALLOY
 CONTACT PLATING: SELECTIVE GOLD, MIN. 0.76µm (30µinch) IN CONTACT AREA
 OVER MIN. 1.27µm (50µinch) NICKEL
 SOLDER PIN PLATING: 3.05µm (120µinch) TIN OVER 1.02µm (40µinch) NICKEL
 OVER ALL
 SHIELDING PIN PLATING: NICKEL
- 2 PIN NOT ELECTRICALLY CONNECTED MAYBE OMITTED SEE ELECTRICAL CIRCUIT DIAGRAM FOR OMITTED PINS
- 3 RJ45 CAVITIES CONFORM TO FCC RULES AND REGULATION PART 68
- 4 THE PART IS RECOMMENDED FOR REFLOW SOLDERING PROCESS PEAK SOLDERING: TEMPERATURE MAX. +260° C, MAX. 10s
- 5 OPERATING TEMPERATURE: T= -40° C TO +85° C
- 6 STORAGE TEMPERATURE: T= -40° C TO +85° C
- 7 UNLESS OTHERWISE SPECIFIED, SEE TABLE FOR ALL DIMENSIONS TOLERANCES
- 8 JACK CONFIGURATION: 1 x 2
 TAB DIRECTION: UP
- 9 PACKAGING: REEL (T+R) ACCORDING TO PACKAGING SPECIFICATION 107-18116

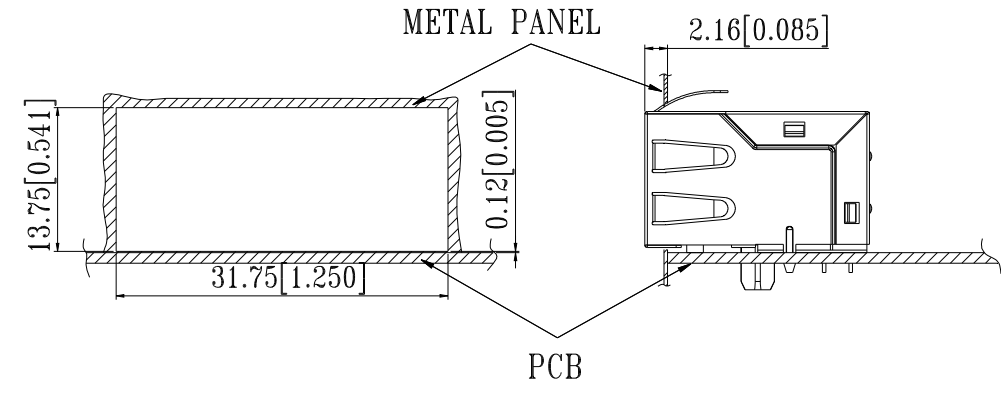
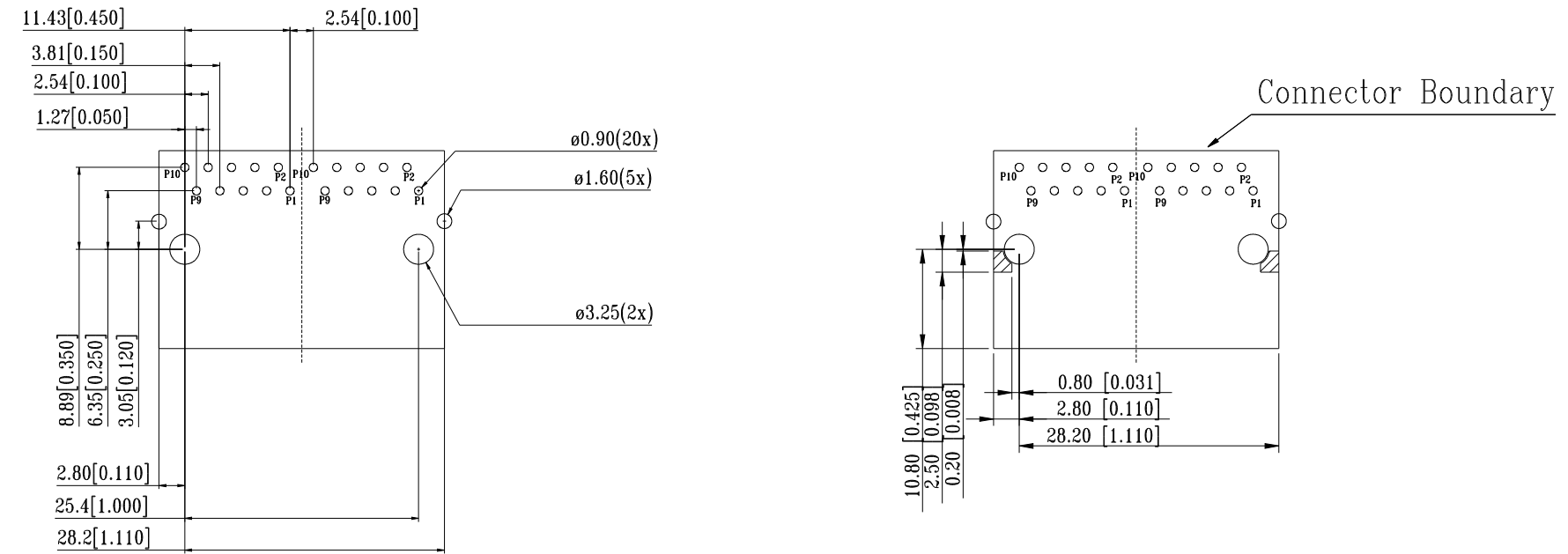
TABLE OF TOLERANCE (mm)	
RANGE	TOLERANCE
0-10	±0.15
>10-40	±0.25
>40-70	±0.30
>70	±0.40

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN 27AUG2016 GANESH C M	TE Connectivity																	
		CHK 27AUG2016 FRANZ MUELLER																		
		APVD 27AUG2016 MARTIN SZELAG	NAME RJ45 JACK INT.MAG. 1Gb 1x2 INV.																	
DIMENSIONS: mm [INCHES]		PRODUCT SPEC 108-94552																		
TOLERANCES UNLESS OTHERWISE SPECIFIED:		APPLICATION SPEC 114-94447																		
<table border="0"> <tr> <td>0 PLC</td><td>± -</td><td rowspan="5" style="text-align: center; vertical-align: middle;">7</td> </tr> <tr> <td>1 PLC</td><td>± -</td> </tr> <tr> <td>2 PLC</td><td>± -</td> </tr> <tr> <td>3 PLC</td><td>± -</td> </tr> <tr> <td>4 PLC</td><td>± -</td> </tr> <tr> <td>ANGLES</td><td>± -</td><td></td> </tr> </table>		0 PLC	± -	7	1 PLC	± -	2 PLC	± -	3 PLC	± -	4 PLC	± -	ANGLES	± -		WEIGHT -	SIZE A3	CAGE CODE 00779	DRAWING NO C=2301996-3	RESTRICTED TO -
0 PLC	± -	7																		
1 PLC	± -																			
2 PLC	± -																			
3 PLC	± -																			
4 PLC	± -																			
ANGLES	± -																			
MATERIAL 1		FINISH 1	CUSTOMER DRAWING		SCALE 3:1	SHEET 1 of 3	REV A1													


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REVISIONS					
P	LTR	DESCRIPTION	DATE	DWN	APVD
-	-	SEE SHEET 1	-	-	-

SUGGESTED PCB LAYOUT



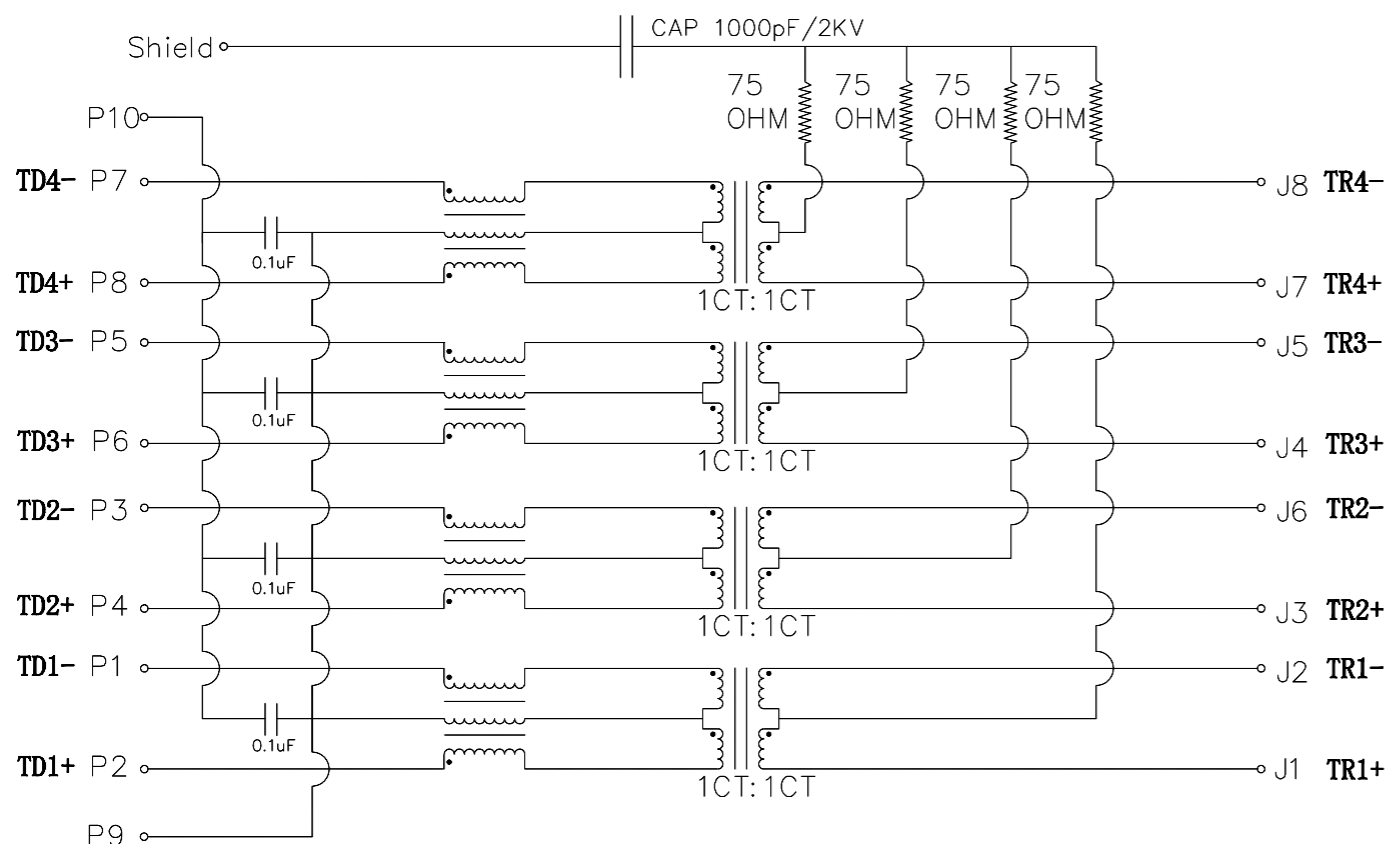
UNIT: mm / inch
 Tolerances: ±0.10 / 0.004

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN GANESH C M 27AUG2016	 TE Connectivity	
DIMENSIONS: mm [INCHES]		CHK FRANZ MUELLER 27AUG2016		
TOLERANCES UNLESS OTHERWISE SPECIFIED:		APVD MARTIN SZELAG 27AUG2016	NAME RJ45 JACK INT.MAG. 1Gb 1x2 INV.	
0 PLC ± - 1 PLC ± - 2 PLC ± - 3 PLC ± - 4 PLC ± - ANGLES ± - MATERIAL 1		PRODUCT SPEC 108-94552	RESTRICTED TO	
FINISH 1		APPLICATION SPEC 114-94447	SIZE A3	CAGE CODE 00779
		WEIGHT 0	DRAWING NO C=2301996-3	SCALE 3:1
		CUSTOMER DRAWING	SHEET 2 of 3	REV A1

REVISIONS

P	LTR	DESCRIPTION	DATE	DWN	APVD
-	-	SEE SHEET 1	-	-	-

ELECTRICAL CIRCUIT DIAGRAM



- 1.0 Turn Ratio @100KHz: (P2~P1):(J1~J2) = 1:1±2%
- (P4~P3):(J3~J6) = 1:1±2%
- (P6~P5):(J4~J5) = 1:1±2%
- (P8~P7):(J7~J8) = 1:1±2%

2.0 Primary Inductance: 350uH MIN @100KHZ,0.1V 8mA DC BIAS

3.0 DC Resistance: 1.2 OHMS MAX

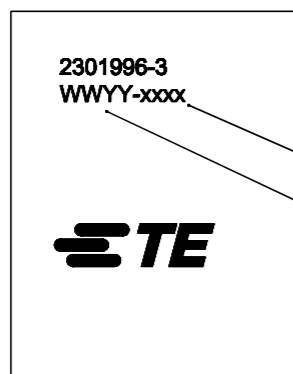
4.0 Insertion Loss: 1-100MHz -1.0dB MAX
 100-125MHz -1.2dB MAX

5.0 Return Loss: 1-40MHz -16dB MIN
 40-60MHz -12dB MIN
 60-80MHz -10dB MIN
 80-100MHz -8dB MIN

6.0 Cross Talk: 1-100MHz -30dB MIN

7.0 COMMON TO COMMON MODE ATTENUATION: 1-100MHz -30dB MIN

8.0 Isolation: PHY Side to Line Side: 2250VDC



CONTROL CODE

DATE CODE

"0213"

YY-YEAR

WW-WEEK

FCC

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN	27AUG2016		 TE Connectivity
		CHK	27AUG2016		
		APVD	27AUG2016		
		NAME	MARTIN SZELAG		
DIMENSIONS:	TOLERANCES UNLESS OTHERWISE SPECIFIED:	PRODUCT SPEC		RJ45 JACK INT.MAG. 1Gb 1x2 INV.	
mm [INCHES]	0 PLC ± -	108-94552			
	1 PLC ± -	APPLICATION SPEC			
	2 PLC ± -	114-94447		SIZE	CAGE CODE
	3 PLC ± -	WEIGHT		DRAWING NO	
	4 PLC ± -	0		A3 00779 C=2301996-3	
	ANGLES ± -	CUSTOMER DRAWING		RESTRICTED TO	
MATERIAL	FINISH	SCALE		SHEET	REV
1	1	3:1		3 OF 3	A1