

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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Illustration shows a fully mounted version of the electronic housing

Intermediate element for modular extension of the volume of the housing in the pitch. The open intermediate element is 17.5 mm wide.



Key commercial data

Packing unit	11
Minimum order quantity	10 1
Weight per Piece (excluding packing)	16.21 GRM
Custom tariff number	85369010
Country of origin	Germany

Technical data

Product type	Component housing center part
Approval	UL Recognized
Width	17.5 mm
Color	green

Classifications

eCl@ss

eCl@ss 4.0	27180401
eCl@ss 4.1	27180401
eCl@ss 5.0	27180506
eCl@ss 5.1	27180506
eCl@ss 6.0	27180802
eCl@ss 7.0	27182702



Classifications

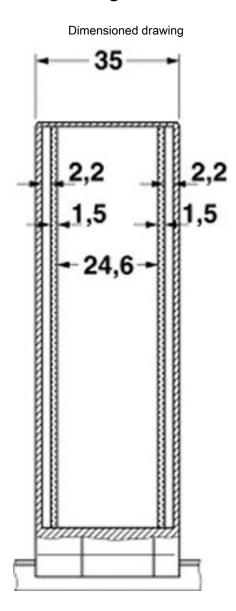
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eCl(ass

eCl@ss	
eCl@ss 8.0	27182702
ETIM	
ETIM 2.0	EC001031
ETIM 3.0	EC001031
ETIM 4.0	EC001031
ETIM 5.0	EC000886
UNSPSC	
UNSPSC 6.01	31261501
UNSPSC 7.0901	31261501
UNSPSC 11	31261501
UNSPSC 12.01	31261501
UNSPSC 13.2	31261501
Approvals	
Approvals	
UL Recognized	
Ex Approvals	
Approvals submitted	
Approval details	

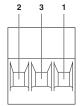
Drawings

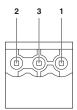
UL Recognized **51**





Schematic diagram



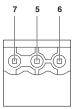


Pin assignment left



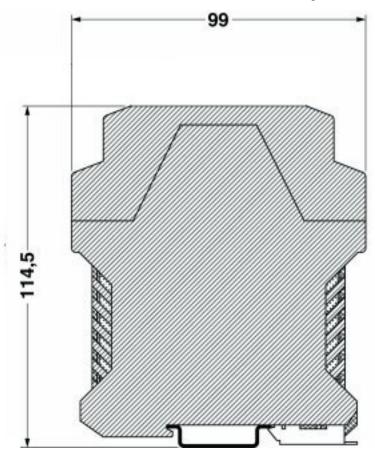
Schematic diagram

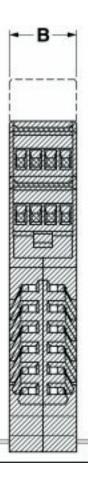




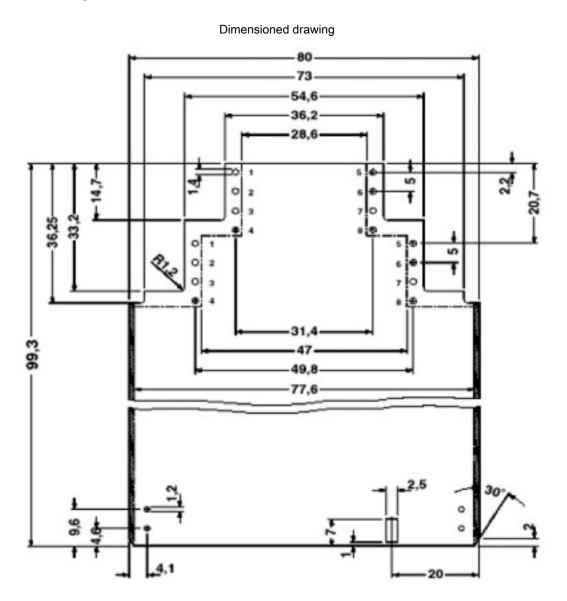
Pin assignment right

Dimensioned drawing









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