imall

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2N3903 is a Preferred Device

General Purpose Transistors

NPN Silicon

Features

• Pb–Free Package May be Available. The G–Suffix Denotes a Pb–Free Lead Finish

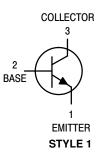
MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector – Emitter Voltage	V _{CEO}	40	Vdc
Collector-Base Voltage	V _{CBO}	60	Vdc
Emitter-Base Voltage	V _{EBO}	6.0	Vdc
Collector Current – Continuous	Ι _C	200	mAdc
Total Device Dissipation @ T _A = 25°C Derate above 25°C	P _D	625 5.0	mW mW/°C
Total Device Dissipation @ T _C = 25°C Derate above 25°C	P _D	1.5 12	W mW/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	−55 to +150	°C

THERMAL CHARACTERISTICS (Note 1)

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Ambient	$R_{ hetaJA}$	200	°C/W
Thermal Resistance, Junction-to-Case	$R_{ extsf{ heta}JC}$	83.3	°C/W

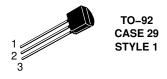
1. Indicates Data in addition to JEDEC Requirements.



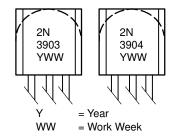


ON Semiconductor®

http://onsemi.com



MARKING DIAGRAMS



ORDERING INFORMATION

Device	Package	Shipping [†]
2N3903	TO-92	5000 Units/Box
2N3903RLRM	TO-92	2000/Ammo Pack
2N3904	TO-92	5000 Units/Box
2N3904RLRA	TO-92	2000/Tape & Reel
2N3904RLRE	TO-92	2000/Tape & Reel
2N3904RLRM	TO-92	2000/Ammo Pack
2N3904RLRMG	TO-92	2000/Ammo Pack
2N3904RLRP	TO-92	2000/Ammo Pack
2N3904RL1	TO-92	2000/Tape & Reel
2N3904ZL1	TO-92	2000/Ammo Pack

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

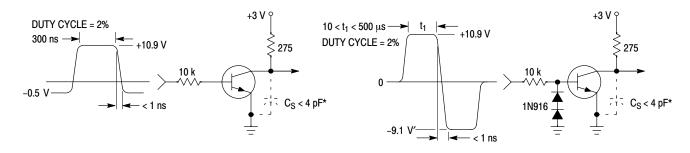
 $\ensuremath{\textbf{Preferred}}$ devices are recommended choices for future use and best overall value.

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic			Min	Max	Unit
OFF CHARACTERISTICS					
Collector – Emitter Breakdown Voltage (Note 2) ($I_c = 1.0 \text{ mAdc}, I_B = 0$)			40	-	Vdc
Collector – Base Breakdown Voltage ($I_C = 10 \ \mu Adc, I_E = 0$)		V _{(BR)CBO}	60	-	Vdc
Emitter – Base Breakdown Voltage ($I_E = 10 \ \mu Adc$, $I_C = 0$)		V _{(BR)EBO}	6.0	-	Vdc
Base Cutoff Current (V _{CE} = 30 Vdc, V _{EB} = 3.0 Vdc)		I _{BL}	-	50	nAdc
Collector Cutoff Current (V _{CE} = 30 Vdc, V _{EB} = 3.0 Vdc)		I _{CEX}	-	50	nAdc
ON CHARACTERISTICS				1	
DC Current Gain (Note 2) (I _C = 0.1 mAdc, V _{CE} = 1.0 Vdc)	2N3903	h _{FE}	20	_	-
$(I_{C} = 1.0 \text{ mAdc}, V_{CE} = 1.0 \text{ Vdc})$	2N3904 2N3903 2N3904		40 35 70		
$(I_{C} = 10 \text{ mAdc}, V_{CE} = 1.0 \text{ Vdc})$	2N3904 2N3903 2N3904		50 100	150 300	
$(I_{C} = 50 \text{ mAdc}, V_{CE} = 1.0 \text{ Vdc})$	2N3904 2N3903 2N3904		30 60		
$(I_{C} = 100 \text{ mAdc}, V_{CE} = 1.0 \text{ Vdc})$	2N3903 2N3904		15 30		
Collector – Emitter Saturation Voltage (Note 2) ($I_C = 10 \text{ mAdc}, I_B = 1.0 \text{ mAdc}$) ($I_C = 50 \text{ mAdc}, I_B = 5.0 \text{ mAdc}$		V _{CE(sat)}	-	0.2 0.3	Vdc
Base – Emitter Saturation Voltage (Note 2) ($I_C = 10 \text{ mAdc}, I_B = 1.0 \text{ mAdc}$) ($I_C = 50 \text{ mAdc}, I_B = 5.0 \text{ mAdc}$)		V _{BE(sat)}	0.65 -	0.85 0.95	Vdc
SMALL-SIGNAL CHARACTERISTICS					÷
Current-Gain – Bandwidth Product ($I_C = 10 \text{ mAdc}, V_{CE} = 20 \text{ Vdc}, f = 100 \text{ MHz}$)	2N3903 2N3904	fT	250 300		MHz
Output Capacitance (V _{CB} = 5.0 Vdc, I _E = 0, f = 1.0 MHz)		C _{obo}	-	4.0	pF
Input Capacitance (V _{EB} = 0.5 Vdc, I _C = 0, f = 1.0 MHz)		C _{ibo}	-	8.0	pF
Input Impedance $(I_C = 1.0 \text{ mAdc}, V_{CE} = 10 \text{ Vdc}, f = 1.0 \text{ kHz})$	2N3903 2N3904	h _{ie}	1.0 1.0	8.0 10	kΩ
Voltage Feedback Ratio (I _C = 1.0 mAdc, V _{CE} = 10 Vdc, f = 1.0 kHz)	2N3903 2N3904	h _{re}	0.1 0.5	5.0 8.0	X 10
Small–Signal Current Gain 2N3903 (I _C = 1.0 mAdc, V _{CE} = 10 Vdc, f = 1.0 kHz) 2N3903 2N3904 2N3904		h _{fe}	50 100	200 400	-
Output Admittance ($I_C = 1.0 \text{ mAdc}$, $V_{CE} = 10 \text{ Vdc}$, $f = 1.0 \text{ kHz}$)		h _{oe}	1.0	40	μmhos
Noise Figure (I _C = 100 μ Adc, V _{CE} = 5.0 Vdc, R _S = 1.0 k Ω , f = 1.0 kHz)	2N3903 2N3904	NF	-	6.0 5.0	dB
SWITCHING CHARACTERISTICS					
Delay Time $(V_{CC} = 3.0 \text{ Vdc}, V_{BE} = 0.5 \text{ Vdc}.)$		t _d	-	35	ns

Delay Time	$(V_{CC} = 3.0 \text{ Vdc}, V_{BE} = 0.5 \text{ Vdc},$		t _d	-	35	ns
Rise Time	$I_{\rm C} = 10 \text{ mAdc}, I_{\rm B1} = 1.0 \text{ mAdc})$		t _r	-	35	ns
Storage Time	$(V_{CC} = 3.0 \text{ Vdc}, I_C = 10 \text{ mAdc}, I_{B1} = I_{B2} = 1.0 \text{ mAdc})$	2N3903 2N3904	t _s	-	175 200	ns
Fall Time			t _f	-	50	ns

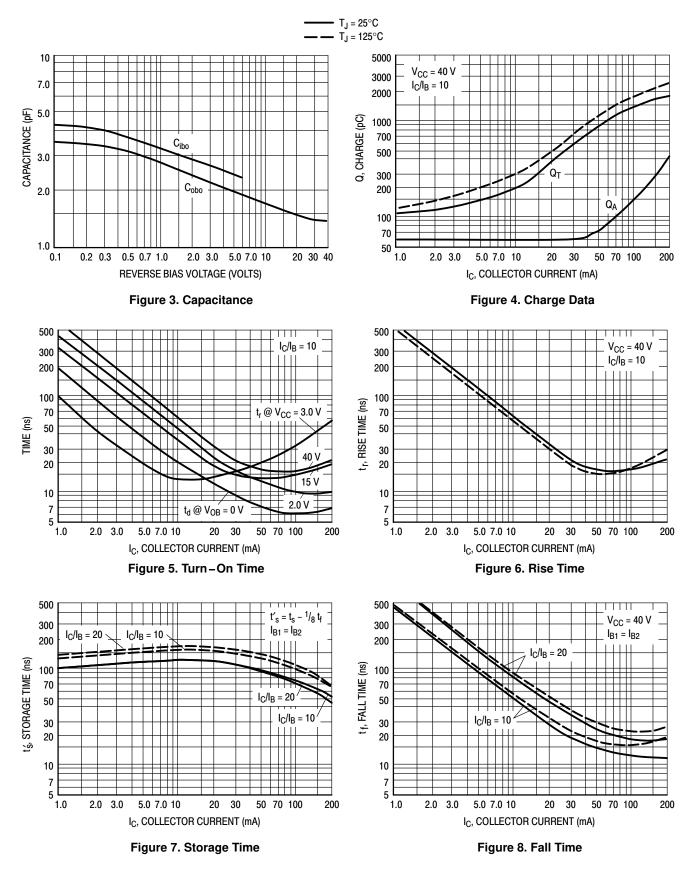
2. Pulse Test: Pulse Width \leq 300 $\mu s;$ Duty Cycle \leq 2%.

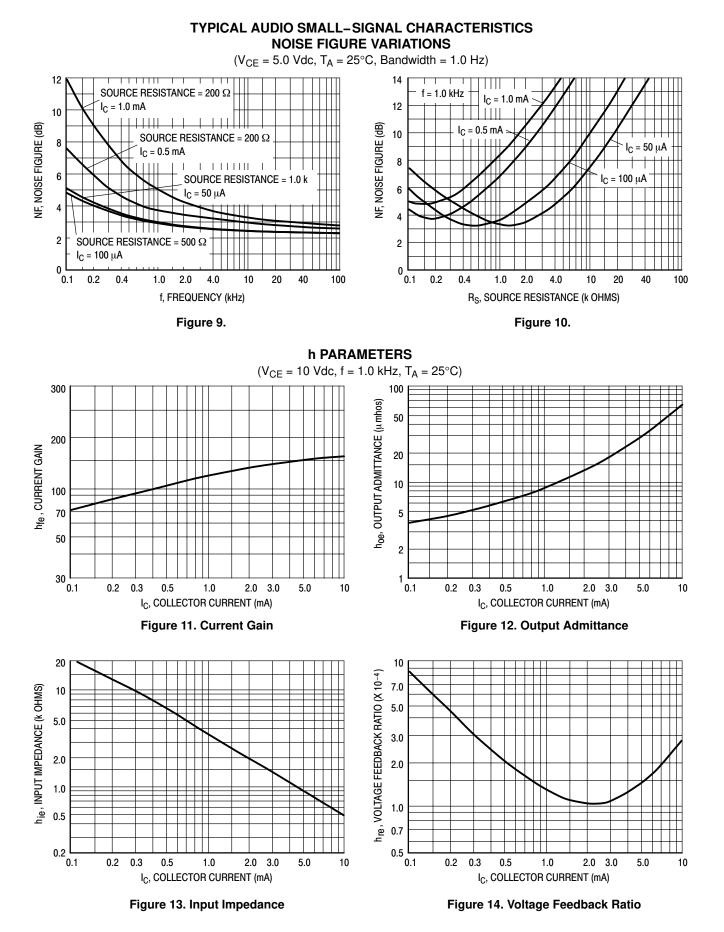


* Total shunt capacitance of test jig and connectors

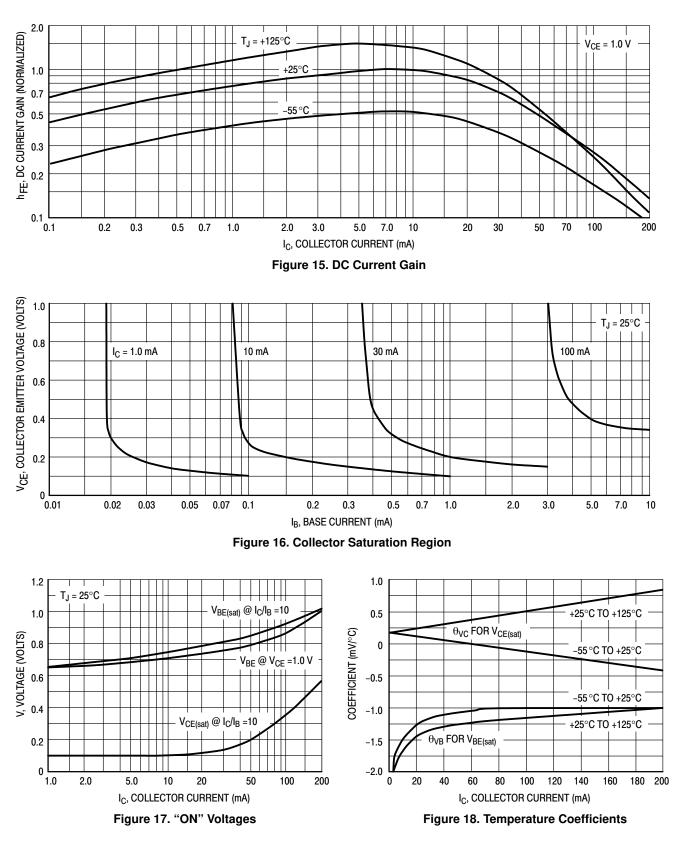
Figure 1. Delay and Rise Time Equivalent Test Circuit Figure 2. Storage and Fall Time Equivalent Test Circuit

TYPICAL TRANSIENT CHARACTERISTICS



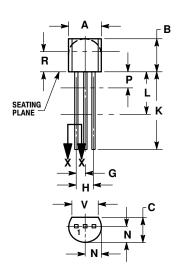


TYPICAL STATIC CHARACTERISTICS



PACKAGE DIMENSIONS

TO-92 **TO-226AA** CASE 29-11 **ISSUE AL**





NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH. 3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED. 4. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.175	0.205	4.45	5.20
В	0.170	0.210	4.32	5.33
C	0.125	0.165	3.18	4.19
D	0.016	0.021	0.407	0.533
G	0.045	0.055	1.15	1.39
Н	0.095	0.105	2.42	2.66
J	0.015	0.020	0.39	0.50
K	0.500		12.70	
L	0.250		6.35	
Ν	0.080	0.105	2.04	2.66
Ρ		0.100		2.54
R	0.115		2.93	
۷	0.135		3.43	

STYLE 1:		STYLE 14:	
PIN 1.	EMITTER	PIN 1.	EMITTER
2.	BASE	2.	COLLECTOR
3.	COLLECTOR	3.	BASE

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