imall

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

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DESCRIPTION:

The CENTRAL SEMICONDUCTOR 2N4391 series types are N-Channel silicon JFETs designed for analog switching and chopper applications.

MARKING: FULL PART NUMBER

MAXIMUM RATINGS: (T _A =25°C)	SYMBOL		UNITS
Gate-Drain Voltage	V _{GD}	40	V
Gate-Source Voltage	V _{GS}	40	V
Gate Current	IG	50	mA
Power Dissipation (T _C =25°C)	PD	1.8	W
Operating and Storage Junction Temperature	т _Ј , т _{stg}	-65 to +175	°C

ELECTRICAL CHARACTERISTICS: (T_A=25°C unless otherwise noted)

		<u>2N</u> 4	<u>4391</u>	2N4	392	<u>2N</u>	<u>4393</u>	
SYMBOL	TEST CONDITIONS	MIN	MAX	MIN	MAX	MIN	MAX	UNITS
IGSS	V _{GS} =20V	-	0.1	-	0.1	-	0.1	nA
IGSS	V _{GS} =20V, T _A =125°C	-	0.2	-	0.2	-	0.2	μA
IDSS	V _{DS} =20V	50	150	25	75	5.0	30	mA
^I D(OFF)	V _{DS} =20V, V _{GS} =12V	-	0.1	-	-	-	-	nA
^I D(OFF)	V _{DS} =20V, V _{GS} =7.0V	-	-	-	0.1	-	-	nA
^I D(OFF)	V _{DS} =20V, V _{GS} =5.0V	-	-	-	-	-	0.1	nA
ID(OFF)	V _{DS} =20V, V _{GS} =12V, T _A =150°C	-	0.2	-	-	-	-	μA
^I D(OFF)	V _{DS} =20V, V _{GS} =7.0V, T _A =150°C	-	-	-	0.2	-	-	μA
^I D(OFF)	V _{DS} =20V, V _{GS} =5.0V, T _A =150°C	-	-	-	-	-	0.2	μA
BVGSS	I _G =1.0μA	40	-	40		40	-	V
V _{GS(OFF)}	V _{DS} =20V, I _D =1.0nA	4.0	10	2.0	5.0	0.5	3.0	V
V _{GS(f)}	V _{DS} =0, I _G =1.0mA	-	1.0	-	1.0	-	1.0	V
V _{DS(ON)}	I _D =12mA	-	0.4	-	-	-	-	V
V _{DS(ON)}	I _D =6.0mA	-	-	-	0.4	-	-	V
V _{DS(ON)}	I _D =3.0mA	-	-	-	-	-	0.4	V
^r DS(ON)	I _D =1.0mA, V _{GS} =0	-	30	-	60	-	100	Ω
^r ds(on)	V _{GS} =0, I _D =0, f=1.0kHz	-	30	-	60	-	100	Ω
C _{rss}	V _{GS} =12V, V _{DS} =0, f=1.0MHz	-	3.5	-	-	-	-	pF
C _{rss}	V _{GS} =7.0V, V _{DS} =0, f=1.0MHz	-	-	-	3.5	-	-	pF
C _{rss}	V _{GS} =5.0V, V _{DS} =0, f=1.0MHz	-	-	-	-	-	3.5	pF
C _{iss}	V_{DS} =20V, V_{GS} =0, f=1.0MHz	-	14	-	14	-	14	pF

R2 (26-September 2016)

TO-18 CASE

N-CHANNEL JFET



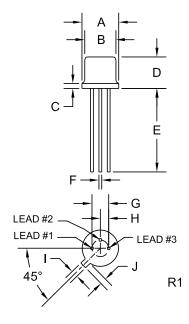
2N4391 2N4392 2N4393

SILICON N-CHANNEL JFET

ELECTRICAL CHARACTERISTICS - Continued: (T_A=25°C unless otherwise noted)

		<u>2N</u> 4	<u>4391</u>	<u>2N4</u>	392	<u>2N</u>	<u>4393</u>	
SYMBOL	TEST CONDITIONS	MIN	MAX	MIN	MAX	MIN	MAX	UNITS
t _r	I _{D(ON)} =12mA	-	5.0	-	-	-	-	ns
t _r	ID(ON)=6.0mA	-	-	-	5.0	-	-	ns
t _r	ID(ON)=3.0mA	-	-	-	-	-	5.0	ns
t _f	VGS(OFF)=12V	-	15	-	-	-	-	ns
t _f	V _{GS(OFF)} =7.0V	-	-	-	20	-	-	ns
t _f	V _{GS(OFF)} =5.0V	-	-	-	-	-	30	ns
t _{on}	ID(ON)=12mA	-	15	-	-	-	-	ns
t _{on}	I _{D(ON)} =6.0mA	-	-	-	15	-	-	ns
t _{on}	ID(ON)=3.0mA	-	-	-	-	-	15	ns
t _{off}	V _{GS(OFF)} =12V	-	20	-	-	-	-	ns
t _{off}	V _{GS(OFF)} =7.0V	-	-	-	35	-	-	ns
toff	VGS(OFF)=5.0V	-	-	-	-	-	50	ns

TO-18 CASE - MECHANICAL OUTLINE



DIMENSIONS						
	INC	HES	MILLIMETERS			
SYMBOL	MIN	MAX	MIN	MAX		
A (DIA)	0.209	0.230	5.31	5.84		
B (DIA)	0.178	0.195	4.52	4.95		
С	-	0.030	-	0.76		
D	0.170	0.210	4.32	5.33		
E	0.500	-	12.70	-		
F (DIA)	0.016	0.019	0.41	0.48		
G (DIA)	0.100		2.54			
Н	0.050		1.27			
I	0.036	0.046	0.91	1.17		
J	0.028	0.048	0.71	1.22		
TO-18 (REV: R1)						

LEAD CODE:

1) Source 2) Drain 3) Gate

MARKING: FULL PART NUMBER

R2 (26-September 2016)

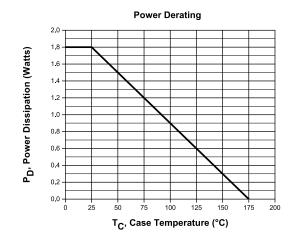
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SILICON N-CHANNEL JFET

TYPICAL ELECTRICAL CHARACTERISTICS



R2 (26-September 2016)

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OUTSTANDING SUPPORT AND SUPERIOR SERVICES

PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- Inventory bonding
- Consolidated shipping options

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free guick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- Custom electrical curves
- · Environmental regulation compliance
- Customer specific screening
- · Up-screening capabilities

· Custom product packing

Custom bar coding for shipments

- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- · Application and design sample kits
- · Custom product and package development

REQUESTING PRODUCT PLATING

- If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when 1. ordering (example: 2N2222A TIN/LEAD).
- 2. If requesting Lead (Pb) Free plated devices, add the suffix " PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

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