

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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Preferred Device

General Purpose Transistors

PNP Silicon

Features

• Pb-Free Packages are Available*

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector – Emitter Voltage	V _{CEO}	40	Vdc
Collector - Base Voltage	V _{CBO}	40	Vdc
Emitter – Base Voltage	V _{EBO}	5.0	Vdc
Collector Current – Continuous	I _C	600	mAdc
Total Device Dissipation @ T _A = 25°C Derate above 25°C	P _D	625 5.0	mW mW/°C
Total Device Dissipation @ T _C = 25°C Derate above 25°C	P _D	1.5 12	W mW/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-55 to +150	°C

THERMAL CHARACTERISTICS

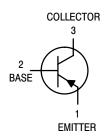
Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	200	°C/W
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	83.3	°C/W

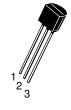
Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.



ON Semiconductor®

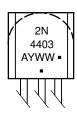
http://onsemi.com





TO-92 **CASE 29** STYLE 1

MARKING DIAGRAM



2N4403 = Device Code = Assembly Location Α

= Year WW = Work Week = Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 3 of this data sheet.

Preferred devices are recommended choices for future use and best overall value.

^{*}For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ELECTRICAL CHARACTERISTICS ($T_A = 25$ °C unless otherwise noted)

	Characteristic		Min	Max	Unit
OFF CHARACTERISTICS		.		1	
Collector–Emitter Breakdov (I _C = 1.0 mAdc, I _B = 0)	V _{(BR)CEO}	40	-	Vdc	
Collector-Base Breakdown (I _C = 0.1 mAdc, I _E = 0)	V _{(BR)CBO}	40	-	Vdc	
Emitter-Base Breakdown V $(I_E = 0.1 \text{ mAdc}, I_C = 0)$	V _{(BR)EBO}	5.0	-	Vdc	
Base Cutoff Current (V _{CE} = 35 Vdc, V _{EB} = 0.4	I _{BEV}	-	0.1	μAdc	
Collector Cutoff Current (V _{CE} = 35 Vdc, V _{EB} = 0.4	I _{CEX}	-	0.1	μAdc	
ON CHARACTERISTICS			-	·	•
DC Current Gain $ \begin{aligned} &(I_C = 0.1 \text{ mAdc, } V_{CE} = 1.6 \\ &(I_C = 1.0 \text{ mAdc, } V_{CE} = 1.6 \\ &(I_C = 10 \text{ mAdc, } V_{CE} = 1.0 \\ &(I_C = 150 \text{ mAdc, } V_{CE} = 2.6 \\ &(I_C = 500 \text{ mAdc, } V_{CE} = 2.6 \\ \end{aligned} $	0 Vdc) Vdc) 0 Vdc) (Note 1)	h _{FE}	30 60 100 100 20	- - - 300 -	-
Collector–Emitter Saturation Voltage (Note 1) ($I_C = 150$ mAdc, $I_B = 15$ mAdc) ($I_C = 500$ mAdc, $I_B = 50$ mAdc)		V _{CE(sat)}	_ _	0.4 0.75	Vdc
Base – Emitter Saturation Voltage (Note 1) (I _C = 150 mAdc, I _B = 15 mAdc) (I _C = 500 mAdc, I _B = 50 mAdc)		V _{BE(sat)}	0.75 -	0.95 1.3	Vdc
SMALL-SIGNAL CHARAC	TERISTICS	<u>'</u>			
Current-Gain - Bandwidth Product (I _C = 20 mAdc, V _{CE} = 10 Vdc, f = 100 MHz)		f _T	200	_	MHz
Collector–Base Capacitance (V _{CB} = 10 Vdc, I _E = 0, f = 1.0 MHz)		C _{cb}	-	8.5	pF
Emitter–Base Capacitance (V _{EB} = 0.5 Vdc, I _C = 0, f = 1.0 MHz)		C _{eb}	-	30	pF
Input Impedance (I _C = 1.0 mAdc, V _{CE} = 10 Vdc, f = 1.0 kHz)		h _{ie}	1.5 k	15 k	Ω
Voltage Feedback Ratio (I _C = 1.0 mAdc, V _{CE} = 10 Vdc, f = 1.0 kHz)		h _{re}	0.1	8.0	X 10 ⁻⁴
Small-Signal Current Gain (I _C = 1.0 mAdc, V _{CE} = 10 Vdc, f = 1.0 kHz)		h _{fe}	60	500	-
Output Admittance (I _C = 1.0 mAdc, V _{CE} = 10 Vdc, f = 1.0 kHz)		h _{oe}	1.0	100	μmhos
SWITCHING CHARACTERI	STICS				
Delay Time	(V _{CC} = 30 Vdc, V _{BE} = +2.0 Vdc,	t _d	-	15	ns
Rise Time	$I_C = 150 \text{ mAdc}, I_{B1} = 15 \text{ mAdc})$	t _r	_	20	ns
Storage Time	(V _{CC} = 30 Vdc, I _C = 150 mAdc,	t _s	-	225	ns
Fall Time	$I_{B1} = 15 \text{ mA}, I_{B2} = 15 \text{ mA})$	t _f	_	30	ns

Fall Time $I_{B1} = 15 \text{ mA}, I_{B2} = 15 \text{ mA}$)

1. Pulse Test: Pulse Width $\leq 300 \mu \text{s}$, Duty Cycle $\leq 2.0\%$.

ORDERING INFORMATION

Device	Package	Shipping [†]
2N4403	TO-92	5,000 Units / Box
2N4403G	TO-92 (Pb-Free)	5,000 Units / Box
2N4403RL	TO-92	2,000 / Tape & Reel
2N4403RLG	TO-92 (Pb-Free)	2,000 / Tape & Reel
2N4403RLRA	TO-92	2,000 / Tape & Reel
2N4403RLRAG	TO-92 (Pb-Free)	2,000 / Tape & Reel
2N4403RLRM	TO-92	2,000 / Ammo Pack
2N4403RLRMG	TO-92 (Pb-Free)	2,000 / Ammo Pack
2N4403RLRP	TO-92	2,000 / Ammo Pack
2N4403RLRPG	TO-92 (Pb-Free)	2,000 / Ammo Pack

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

SWITCHING TIME EQUIVALENT TEST CIRCUIT

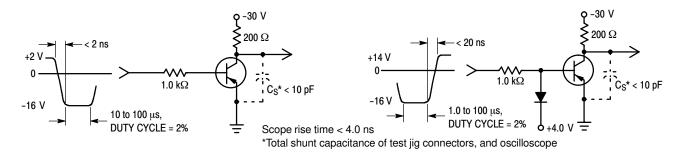


Figure 1. Turn-On Time

Figure 2. Turn-Off Time

TRANSIENT CHARACTERISTICS

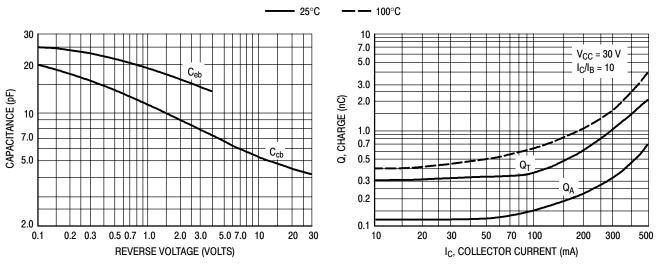
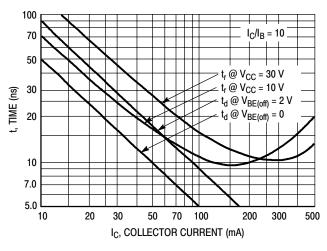


Figure 3. Capacitances

Figure 4. Charge Data



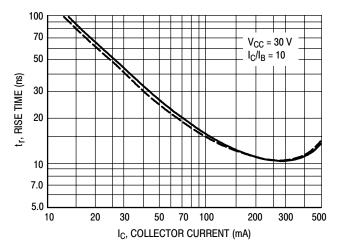


Figure 5. Turn-On Time

Figure 6. Rise Time

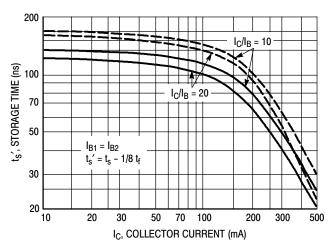


Figure 7. Storage Time

SMALL-SIGNAL CHARACTERISTICS NOISE FIGURE

 $V_{CE} = -10 \text{ Vdc}, T_A = 25^{\circ}\text{C}; Bandwidth} = 1.0 \text{ Hz}$

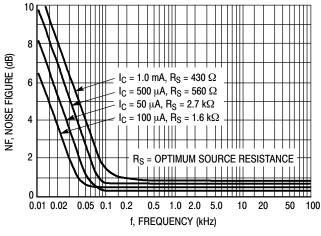


Figure 8. Frequency Effects

Figure 9. Source Resistance Effects

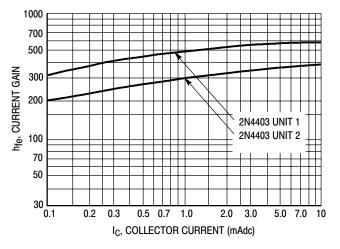
h PARAMETERS

 $V_{CE} = -10 \text{ Vdc}, f = 1.0 \text{ kHz}, T_A = 25^{\circ}\text{C}$

100 k

This group of graphs illustrates the relationship between h_{fe} and other "h" parameters for this series of transistors. To obtain these curves, a high–gain and a low–gain unit were

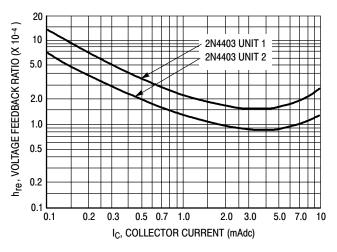
selected from the 2N4403 lines, and the same units were used to develop the correspondingly–numbered curves on each graph.



2N4403 UNIT 1 2N4403 UNIT 2 hie, INPUT IMPEDANCE (OHMS) 20 k 10 k 5 k 2 k 1 k 500 200 100 0.2 3.0 0.1 0.3 0.5 0.7 1.0 2.0 5.0 7.0 10 IC, COLLECTOR CURRENT (mAdc)

Figure 10. Current Gain

Figure 11. Input Impedance



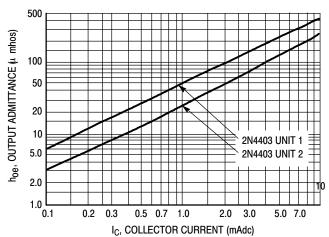


Figure 12. Voltage Feedback Ratio

Figure 13. Output Admittance

STATIC CHARACTERISTICS

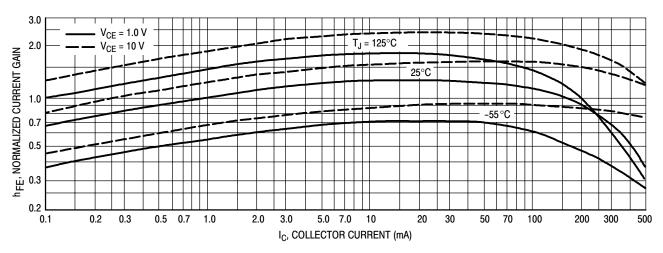


Figure 14. DC Current Gain

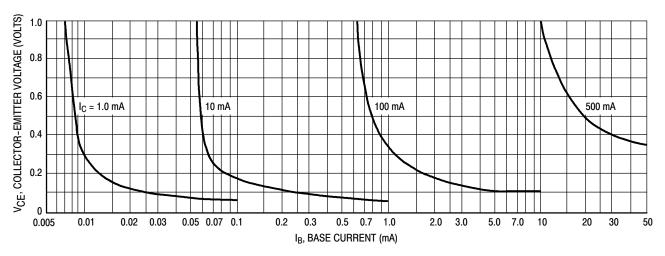


Figure 15. Collector Saturation Region

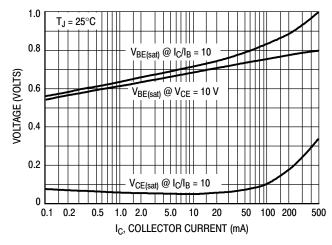


Figure 16. "On" Voltages

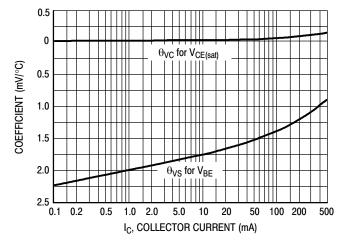
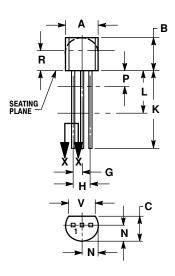


Figure 17. Temperature Coefficients

PACKAGE DIMENSIONS

TO-92 TO-226AA CASE 29-11 **ISSUE AL**





- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- 114-30M, 1902.

 CONTROLLING DIMENSION: INCH.

 CONTOUR OF PACKAGE BEYOND DIMENSION R
 IS UNCONTROLLED.

 LEAD DIMENSION IS UNCONTROLLED IN P AND
- BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.175	0.205	4.45	5.20
В	0.170	0.210	4.32	5.33
С	0.125	0.165	3.18	4.19
D	0.016	0.021	0.407	0.533
G	0.045	0.055	1.15	1.39
Н	0.095	0.105	2.42	2.66
J	0.015	0.020	0.39	0.50
K	0.500		12.70	
٦	0.250		6.35	
N	0.080	0.105	2.04	2.66
Р		0.100		2.54
R	0.115		2.93	
٧	0.135		3.43	

STYLE 1:

PIN 1. EMITTER

COLLECTOR

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