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With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

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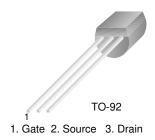




September 2007

2N5953 N-Channel RF Amplifier

- · This device is designed primarily for electronic switching applications such as low on resistance analog switching.
- · Sourced from process 50.



Absolute Maximum Ratings* T_a=25°C unless otherwise noted

Symbol	Parameter	Value	Units
V _{DG}	Drain-Gate Voltage	30	V
V _{GS}	Gate-Source Voltage	-30	V
I _{GF}	Forward Gate Current	10	mA
T _J , T _{STG}	Operating and Storage Junction Temperature Range	-55 ~ 150	°C

^{*} This ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

NOTES

Thermal Characteristics $T_a=25^{\circ}\text{C}$ unless otherwise noted

Symbol	Parameter	Max.	Units
P _D	Total Device Dissipation	350	mW
	Derate above 25°C	2.8	mW/°C
$R_{\theta JC}$	Thermal Resistance, Junction to Case	125	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	357	°C/W

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These rating are based on a maximum junction temperature of 150 degrees C.

²⁾ These are steady limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

$\textbf{Electrical Characteristics*} \ \, \mathbf{T_{a}\text{=}25^{\circ}C} \ \, \mathbf{unless \ otherwise \ noted}$

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Off Characteristics

V _{(BR)GSS}	Gate-Source Breakdown Voltage	$I_{G} = 1.0 \mu A, V_{DS} = 0$	-30		V
1	Gate Reverse Current	$V_{GS} = 15V, V_{DS} = 0, T = 25^{\circ}C$		-1.0	nA
IGSS		T = 100°C		-200	IIA
V _{GS(off)}	Gate-Source Cut-off Voltage	$V_{DS} = 15V, I_{D} = 100nA$	-0.8	-3.0	V
V _{GS}	Gate-Source Forward Voltage	$V_{DS} = 15V, I_D = 250\mu A$	-0.5	-2.5	V

On Characteristics

I_{DSS}	Zero-Gate Voltage Drain Current *	$V_{DS} = 15V, V_{GS} = 0$	2.5	5	mA
V _D s(on)	Drain-Source On Voltage	$I_D = 267 \mu A$		0.1	V

Small Signal Characteristics

gfs	Forward Transferconductance	$V_{DS} = 15V, V_{GS} = 0V, f = 100MHz$	1000	6500	μ/Ω
goss	Common- Source Output Conductance	$V_{DS} = 15V, V_{GS} = 0V, f = 1.0kHz$		50	μ/Ω
gos	Output Conductance	$V_{DS} = 15V, V_{GS} = 0V, f = 100MHz$		50	μ/Ω
gis	Input Conductance	$V_{DS} = 15V, V_{GS} = 0V, f = 100MHz$		250	μ/Ω
Ciss	Input Capacitance	$V_{DS} = 15V, V_{GS} = 0V, f = 1.0MHz$		6	pF
Crss	Reverse Transfer Capacitance	$V_{DS} = 15V, V_{GS} = 0V, f = 1.0MHz$		2	pF
e n	Equivalent Short-Circuit Input Noise Voltage	$V_{DS} = 15V, V_{GS} = 0V, f = 1.0kHz$		100	nV
NF	Noise Figure	$\begin{split} V_{DS} = 15 V, & V_{GS} = 0 V, \\ R_G = 1.0 m \Omega, & f = 1.0 k Hz \\ R_G = 1.0 k \Omega, & f = 100 M Hz \end{split}$		2 5	dB

^{*} Pulse Test: Pulse Width ≤ 300µs, Duty Cycle = 2%





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Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
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