

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China







Small Signal MOSFET

60 V, 310 mA, Single, N-Channel, SOT-23

Features

- Low R_{DS(on)}
- Small Footprint Surface Mount Package
- Trench Technology
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

Applications

- Low Side Load Switch
- Level Shift Circuits
- DC-DC Converter
- Portable Applications i.e. DSC, PDA, Cell Phone, etc.

MAXIMUM RATINGS (T_J = 25°C unless otherwise stated)

Rating		Symbol	Value	Unit
Drain-to-Source Voltage		V _{DSS}	60	V
Gate-to-Source Voltage		V _{GS}	±20	V
Drain Current (Note 1) Steady State t < 5 s	$T_A = 25^{\circ}C$ $T_A = 85^{\circ}C$ $T_A = 25^{\circ}C$	Ι _D	260 190 310	mA
	$T_A = 85^{\circ}C$		220	
Power Dissipation (Note 1) Steady State t < 5 s		P _D	300 420	mW
Pulsed Drain Current (t _p = 10 μs)		I_{DM}	1.2	Α
Operating Junction and Storage Temperature Range		T _J , T _{STG}	–55 to +150	°C
Source Current (Body Diode)		I _S	300	mA
Lead Temperature for Soldering Purposes (1/8" from case for 10 s)		TL	260	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Junction-to-Ambient - Steady State (Note 1)	$R_{ heta JA}$	417	°C/W
Junction-to-Ambient - t ≤ 5 s (Note 1)	$R_{\theta JA}$	300	

^{1.} Surface-mounted on FR4 board using 1 in sq pad size (Cu area = 1.127 in sq [1 oz] including traces)



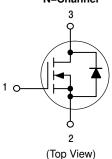
ON Semiconductor®

www.onsemi.com

V _{(BR)DSS}	R _{DS(on)} MAX	I _D MAX (Note 1)
60 V	3.0 Ω @ 4.5 V	310 mA
	2.5 Ω @ 10 V	

Simplified Schematic

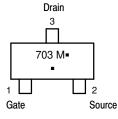
N-Channel



MARKING DIAGRAM & PIN ASSIGNMENT



SOT-23 **CASE 318** STYLE 21



703 = Device Code M = Date Code

= Pb-Free Package (Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping [†]
2N7002ET1G	SOT-23 (Pb-Free)	3000/Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

ELECTRICAL CHARACTERISTICS (T_{.1} = 25°C unless otherwise specified)

Parameter	Symbol	Test Co	ondition	Min	Тур	Max	Units
OFF CHARACTERISTICS				•			
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0 \text{ V}, I_{D} = 250 \mu\text{A}$		60			٧
Drain-to-Source Breakdown Voltage Temperature Coefficient	V _{(BR)DSS} /T _J				75		mV/°C
Zero Gate Voltage Drain Current	I _{DSS}	V _{GS} = 0 V, V _{DS} = 60 V	T _J = 25°C			1	μΑ
			T _J = 125°C			500	1
Gate-to-Source Leakage Current	I _{GSS}	V _{DS} = 0 V, V	V _{GS} = ±20 V			±100	nA
ON CHARACTERISTICS (Note 2)							
Gate Threshold Voltage	V _{GS(TH)}	$V_{GS} = V_{DS}$, I _D = 250 μA	1.0		2.5	V
Negative Threshold Temperature Coefficient	V _{GS(TH)} /T _J				4.4		mV/°C
Drain-to-Source On Resistance	R _{DS(on)}	V _{GS} = 10 V, I _D = 240 mA			0.86	2.5	Ω
		V _{GS} = 4.5 V, I _D = 50 mA			1.1	3.0	1
Forward Transconductance	9FS	V _{DS} = 5 V, I _D = 200 mA			530		mS
CHARGES AND CAPACITANCES				•	•		
Input Capacitance	C _{ISS}	$V_{GS} = 0 \text{ V, f} = 1 \text{ MHz,}$ $V_{DS} = 25 \text{ V}$			26.7	40	pF
Output Capacitance	C _{OSS}				4.6		
Reverse Transfer Capacitance	C _{RSS}				2.9		
Total Gate Charge	$Q_{G(TOT)}$				0.81		nC
Threshold Gate Charge	Q _{G(TH)}	$V_{GS} = 5 \text{ V}.$	V _{DS} = 10 V;		0.31		1
Gate-to-Source Charge	Q _{GS}		240 mA		0.48		1
Gate-to-Drain Charge	Q_{GD}	1			0.08		1
SWITCHING CHARACTERISTICS, V _{GS}	= V (Note 3)						
Turn-On Delay Time	t _{d(ON)}				1.7		ns
Rise Time	t _r	V_{GS} = 10 V, V_{DD} = 30 V, I_D = 200 mA, R_G = 10 Ω			1.2		1
Turn-Off Delay Time	t _{d(OFF)}				4.8		1
Fall Time	t _f				3.6		
DRAIN-SOURCE DIODE CHARACTER	ISTICS						
Forward Diode Voltage	V _{SD}	V _{GS} = 0 V,	$T_J = 25^{\circ}C$		0.79	1.2	V
		$I_S = 200 \text{ mA}$ $T_J = 85^{\circ}\text{C}$			0.7		

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

2. Pulse Test: pulse width $\leq 300~\mu s$, duty cycle $\leq 2\%$ 3. Switching characteristics are independent of operating junction temperatures

TYPICAL CHARACTERISTICS

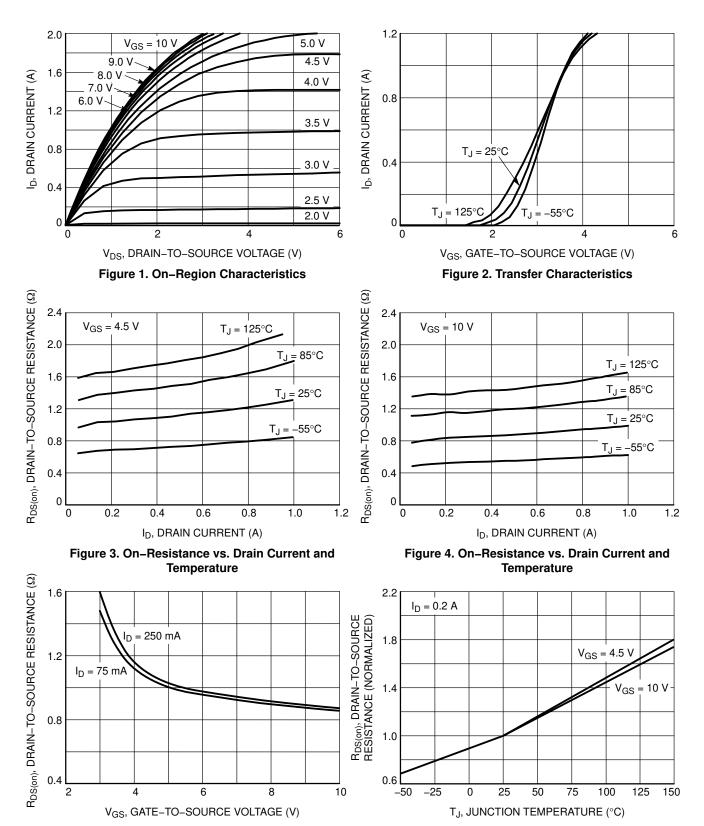


Figure 5. On-Resistance vs. Gate-to-Source Voltage

Figure 6. On–Resistance Variation with Temperature

TYPICAL CHARACTERISTICS

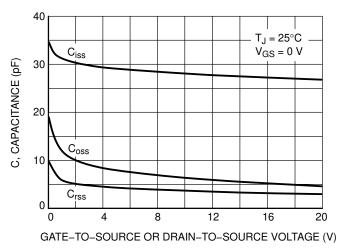


Figure 7. Capacitance Variation

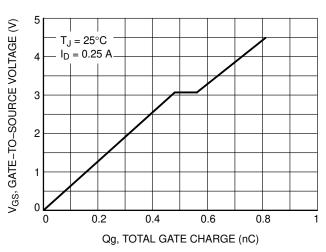


Figure 8. Gate-to-Source and Drain-to-Source Voltage vs. Total Charge

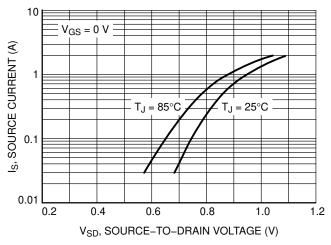


Figure 9. Diode Forward Voltage vs. Current

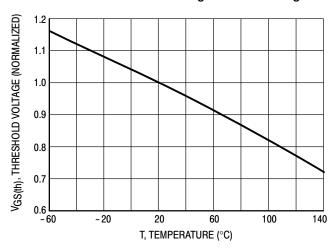
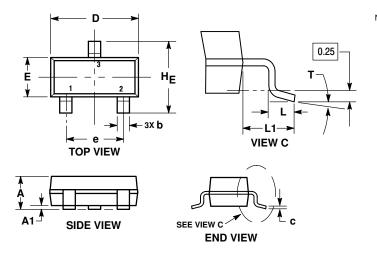


Figure 10. Temperature versus Gate Threshold Voltage

PACKAGE DIMENSIONS

SOT-23 (TO-236) CASE 318-08 **ISSUE AR**



NOTES:

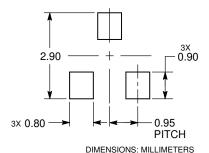
- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994. CONTROLLING DIMENSION: MILLIMETERS. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF THE BASE MATERIAL.
 DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH,
- PROTRUSIONS, OR GATE BURRS

	М	ILLIMETE	RS	INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.89	1.00	1.11	0.035	0.039	0.044
A1	0.01	0.06	0.10	0.000	0.002	0.004
b	0.37	0.44	0.50	0.015	0.017	0.020
С	0.08	0.14	0.20	0.003	0.006	0.008
D	2.80	2.90	3.04	0.110	0.114	0.120
E	1.20	1.30	1.40	0.047	0.051	0.055
е	1.78	1.90	2.04	0.070	0.075	0.080
L	0.30	0.43	0.55	0.012	0.017	0.022
L1	0.35	0.54	0.69	0.014	0.021	0.027
HE	2.10	2.40	2.64	0.083	0.094	0.104
T	0°		10°	0°		10°

STYLE 21:

- PIN 1. GATE
 - SOURCE 2.
 - DRAIN

RECOMMENDED SOLDERING FOOTPRINT



ON Semiconductor and (III) are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold ON Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that ON Semiconductor was negligent regarding the design or manufacture of the part. ON Semiconductor is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor 19521 E. 32nd Pkwy, Aurora, Colorado 80011 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free

Europe, Middle East and Africa Technical Support: Phone: 421 33 790 2910

Japan Customer Focus Center

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative

Phone: 81–3–5817–1050