# imall

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With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

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# Contact us

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#### N-CHANNEL ENHANCEMENT MODE FIELD EFFECT TRANSISTOR

#### **Features**

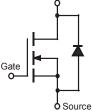
- Low-On Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Ultra-Small Surface Mount Package
- Totally Lead-Free & Fully RoHS compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

#### **Mechanical Data**

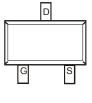
- Case: SOT-323
- Case Material: Molded Plastic, "Green" Molding Compound, Note 4. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminals: Finish Matte Tin annealed over Alloy 42 leadframe. Solderable per MIL-STD-202, Method 208
- Terminal Connections: See Diagram
- Weight: 0.006 grams (approximate)







Drain



TOP VIEW

Equivalent Circuit

#### Ordering Information (Notes 4)

Part Number	Case	Packaging
2N7002W-7-F	SOT-323	3000/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

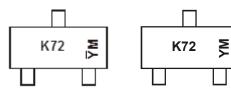
SOT-323

 See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html

### **Marking Information**



K72 = Product Type Marking Code

YM = Date Code Marking for SAT (Shanghai Assembly/ Test site)

YM = Date Code Marking for CAT (Chengdu Assembly/ Test site)

Y or  $\overline{Y}$  = Year (ex: A = 2013) M = Month (ex: 9 = September)

Chengdu A/T Site

Shanghai A/T Site

Date Code Key

Year	201	2	2013		2014	20	15	2016		2017	2	2018
Code	Z		А		В	(	2	D		E		F
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



#### **Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Drain-Source Voltage		V <sub>DSS</sub>	60	V	
Drain-Gate Voltage $R_{GS} \le 1.0M\Omega$		V <sub>DGR</sub>	60	V	
Gain-Source Voltage	Continuous Pulsed	V <sub>GSS</sub>	±20 ±40	V	
Drain Current (Note 5)	Continuous Continuous @ +100°C Pulsed	ID	115 73 800	mA	

#### **Thermal Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

	-		-
Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	В	200	mW
Derating above T <sub>A</sub> = +25°C	PD	1.60	mW
Thermal Resistance, Junction to Ambient	$R_{ ext{ heta}JA}$	625	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

#### Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 6)							
Drain-Source Breakdown Voltage		BV <sub>DSS</sub>	60	70	—	V	$V_{GS} = 0V, I_D = 10\mu A$
Zero Gate Voltage Drain Current @ $T_{C}$ = +125°C	@ T <sub>C</sub> = +25°C	I <sub>DSS</sub>			1.0 500	μA	V <sub>DS</sub> = 60V, V <sub>GS</sub> = 0V
Gate-Body Leakage		I <sub>GSS</sub>			±10	nA	$V_{GS}$ = ±20V, $V_{DS}$ = 0V
ON CHARACTERISTICS (Note 6)		•		•		•	•
Gate Threshold Voltage		V <sub>GS(th)</sub>	1.0	—	2.0	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$
Static Drain-Source On-Resistance	Static Drain-Source On-Resistance @ T <sub>1</sub> = +25°C			1.8	7.5	Ω	V <sub>GS</sub> = 5.0V, I <sub>D</sub> = 0.05A
@ T <sub>j</sub> = +125°C	-	R <sub>DS(ON)</sub>	_	2.6	13.5	12	V <sub>GS</sub> = 10V, I <sub>D</sub> = 0.5A
On-State Drain Current		I <sub>D(ON)</sub>	0.5	1.0	_	Α	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 7.5V
Forward Transconductance		<b>g</b> FS	80			mS	V <sub>DS</sub> = 10V, I <sub>D</sub> = 0.2A
DYNAMIC CHARACTERISTICS (Note 7)					_		
Input Capacitance		Ciss	_	22	50	pF	
Output Capacitance Reverse Transfer Capacitance		Coss	_	11	25	pF	V <sub>DS</sub> = 25V, V <sub>GS</sub> = 0V f = 1.0MHz
		Crss	_	2.0	5.0	pF	
SWITCHING CHARACTERISTICS (Note	7)			•			·
Turn-On Delay Time		t <sub>D(ON)</sub>	_	7.0	20	ns	V <sub>DD</sub> = 30V, I <sub>D</sub> = 0.2A,
Turn-Off Delay Time		t <sub>D(OFF)</sub>	_	11	20	ns	R <sub>L</sub> = 150Ω, V <sub>GEN</sub> = 10V, R <sub>GEN</sub> = 25Ω

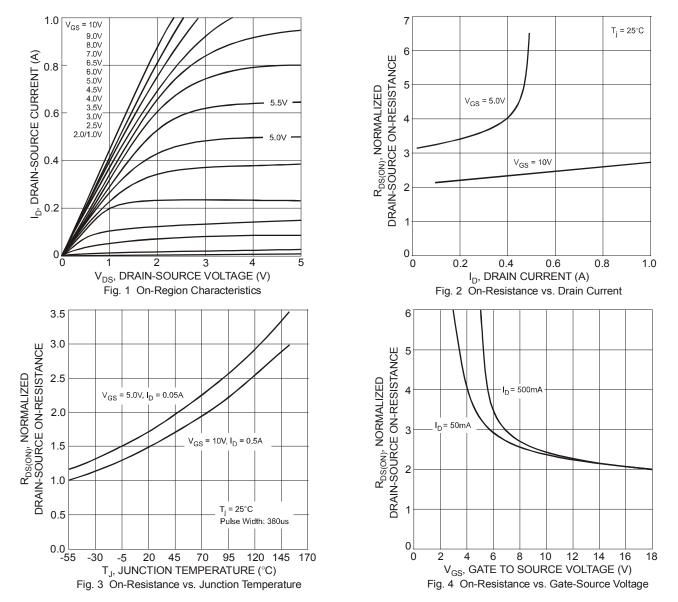
Notes: 5. Device mounted on FR-4 PCB 1.0 x 0.75 x 0.062 inch pad layout as shown on Diodes, Inc. suggested pad layout AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.

6. Short duration pulse test used to minimize self-heating effect.

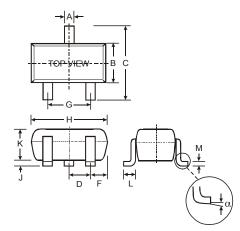
7. Guaranteed by design. Not subject to production testing.



# 2N7002W



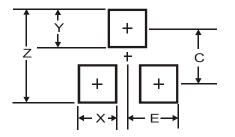
## **Package Outline Dimensions**



	SOT-323					
Dim	Min	Max				
Α	0.25	0.40				
В	1.15	1.35				
С	2.00	2.20				
D	0.65 Nominal					
F	0.30	0.40				
G	1.20	1.40				
Н	1.80	2.20				
J	0.0	0.10				
K	0.90 1.00					
L	0.25	0.40				
М	0.10	0.18				
α	0°	8°				
All Di	mensions	in mm				



### Suggested Pad Layout



Value (in mm)
2.8
0.7
0.9
1.9
1.0

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