

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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Normally – OFF Silicon Carbide Super Junction Transistor

V _{DS}	=	650 V
V _{DS(ON)}	=	1.5 V
I _D	=	15 A
R _{DS(ON)}	=	105 mΩ

Features

- 250 °C maximum operating temperature
- Temperature independent switching performance
- Electrically isolated base-plate
- Gate oxide free SiC switch
- Suitable for connecting an anti-parallel diode
- · Positive temperature coefficient for easy paralleling
- · Low gate charge
- · Low intrinsic capacitance

Advantages

- · Low switching losses
- · Higher efficiency
- High temperature operation
- · High short circuit withstand capability

Package

RoHS Compliant





TO - 257 (Isolated Base-plate Hermetic Package)

Applications

- Down Hole Oil Drilling, Geothermal Instrumentation
- Hybrid Electric Vehicles (HEV)
- Solar Inverters
- Switched-Mode Power Supply (SMPS)
- Power Factor Correction (PFC)
- · Induction Heating
- Uninterruptible Power Supply (UPS)
- Motor Drives

Maximum Ratings at T_i = 250 °C, unless otherwise specified

• , ,		•		
Parameter	Symbol	Conditions	Values	Unit
Drain – Source Voltage	V_{DS}	V _{GS} = 0 V	650	V
Continuous Drain Current	Ι _D	T _C = 155 °C	15	Α
Gate Peak Current	I _{GM}		5	Α
Reverse Gate – Source Voltage	V_{GS}		200	V
Reverse Drain – Source Voltage	V_{DS}		40	V
Power Dissipation	P _{tot}	T _C = 25 °C	22	W
Operating and Storage Temperature	T _i , T _{sta}		-55 to 250	°C

Electrical Characteristics at T_i = 250 °C, unless otherwise specified

Parameter	O. mahad	0 1141	Values		1114	
	Symbol	Conditions -	min.	typ.	max.	Unit
On Characteristics						
		$I_D = 15 \text{ A}, I_G = 500 \text{ mA}, T_j = 25 \text{ °C}$		1.5		
Drain – Source On Voltage	$V_{DS(ON)}$	$I_D = 15 \text{ A}, I_G = 1000 \text{ mA}, T_j = 175 °C$		2.4		V
· ·		$I_D = 15 \text{ A}, I_G = 1000 \text{ mA}, T_j = 250 ^{\circ}\text{C}$		3.6		
Drain – Source On Resistance		I _D = 15 A, I _G = 500 mA, T _j = 25 °C		105		
	$R_{DS(ON)}$	$I_D = 15 \text{ A}, I_G = 1000 \text{ mA}, T_i = 175 ^{\circ}\text{C}$		180		mΩ
	, ,	$I_D = 15 \text{ A}, I_G = 1000 \text{ mA}, T_j = 250 ^{\circ}\text{C}$		290		
Onto Famound Walterna	V	I _G = 500 mA, T _j = 25 °C		3		V
Gate Forward Voltage	$V_{GS(FWD)}$	$I_G = 500 \text{ mA}, T_j = 250 \text{ °C}$		2.6		
DC Current Gain	o	$V_{DS} = 5 \text{ V}, I_{D} = 20 \text{ A}, T_{j} = 25 \text{ °C}$		115		
	β	$V_{DS} = 5 \text{ V}, I_{D} = 20 \text{ A}, T_{j} = 250 ^{\circ}\text{C}$		75		
Off Characteristics						
		$V_R = 650 \text{ V}, V_{GS} = 0 \text{ V}, T_j = 25 \text{ °C}$		1		
Drain Leakage Current	I _{DSS}	$V_R = 650 \text{ V}, V_{GS} = 0 \text{ V}, T_i = 175 ^{\circ}\text{C}$		7		μA
		$V_R = 650 \text{ V}, V_{GS} = 0 \text{ V}, T_i = 250 ^{\circ}\text{C}$		45		·



Electrical Characteristics at T_i = 250 °C, unless otherwise specified

Parameter	Symbol	Conditions	Values		1114	
			min.	typ.	max.	Unit
Dynamic Characteristics						
Input Capacitance	C _{iss}	V 05VV 0V		1534		pF
Output Capacitance	C _{oss}	$V_{DS} = 35 \text{ V}, V_{GS} = 0 \text{ V},$ $f = 1 \text{ MHz}, T_{vi} = 25 ^{\circ}\text{C}$		157		pF
Reverse Transfer Capacitance	C_{rss}	1 – 1 IVII IZ, 1 _{Vj} – 25 C		157		pF
Switching Characteristics						
Turn On Delay Time	t _{d(on)}			5		ns
Rise Time	t _r	$\begin{aligned} &V_{DD} = 400 \text{ V, } I_D = 20 \text{ A,} \\ &R_{G(on)} = R_{G(off)} = 22 \Omega, \\ &V_{GS} = -8/15 \text{ V, } T_j = 175 \text{ °C} \\ &Refer \text{ to Figure 10 for gate drive} \\ ¤t \text{ waveforms} \end{aligned}$		37		ns
Turn Off Delay Time	$t_{d(off)}$			68		ns
Fall Time	t_f			78		ns
Turn-On Energy Per Pulse	E _{on}			66		μJ
Turn-Off Energy Per Pulse	E_{off}			365		μJ
Total Switching Energy	E_{ts}			431		μJ
Turn On Delay Time	$t_{d(on)}$	$V_{DD} = 400 \text{ V}, \text{ I}_D = 10 \text{ A}, \\ R_{C(on)} = R_{C(off)} = 22 \Omega, \\ V_{GS} = -8/15 \text{ V}, T_j = 250 \text{ °C} \\ \text{Refer to Figure 10 for gate drive} \\ \text{current waveforms}$		7		ns
Rise Time	t _r			38		ns
Turn Off Delay Time	$t_{d(off)}$			85		ns
Fall Time	t_f			86		ns
Turn-On Energy Per Pulse	E _{on}			64		μJ
Turn-Off Energy Per Pulse	E_{off}			395		μJ
Total Switching Energy	E_ts			459		μJ
Thermal Characteristics						
Thermal resistance, junction - case	R_{thJC}			1.4		°C/W

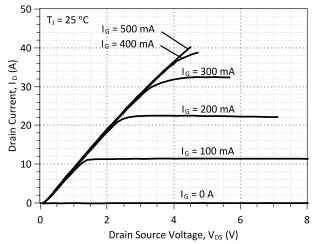


Figure 1: Typical Output Characteristics at 25 °C

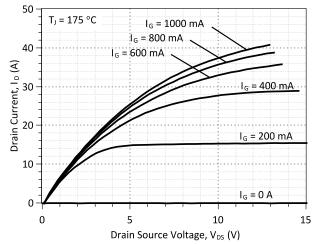


Figure 2: Typical Output Characteristics at 175 °C



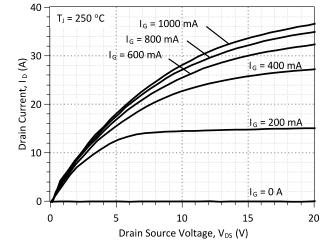


Figure 3: Typical Output Characteristics at 250 °C

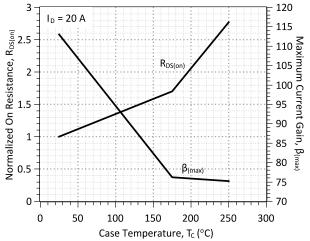


Figure 5: Normalized On-Resistance and Current Gain vs. Temperature

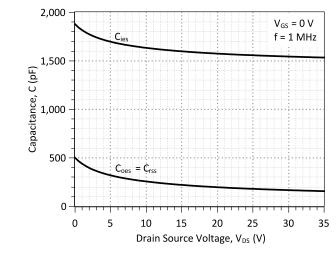


Figure 7: Typical Capacitance vs Drain-Source Voltage

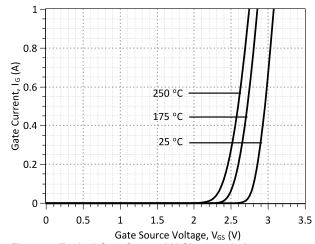


Figure 4: Typical Gate Source I-V Characteristics vs. Temperature

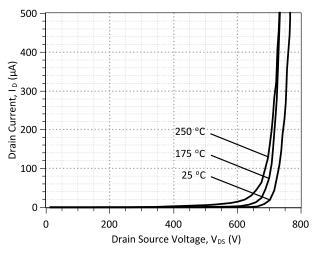


Figure 6: Typical Blocking Characteristics

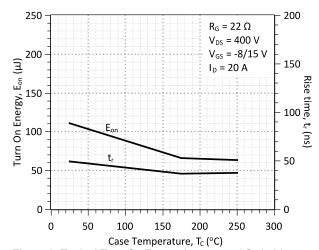


Figure 8: Typical Turn On Energy Losses and Switching Times vs. Temperature

Pg3 of 5



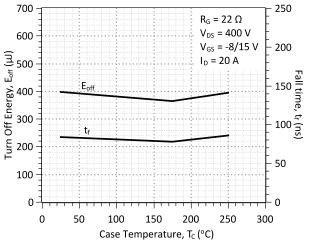


Figure 9: Typical Turn Off Energy Losses and Switching Times vs. Temperature

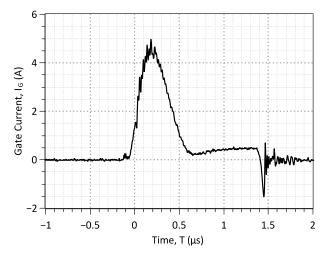
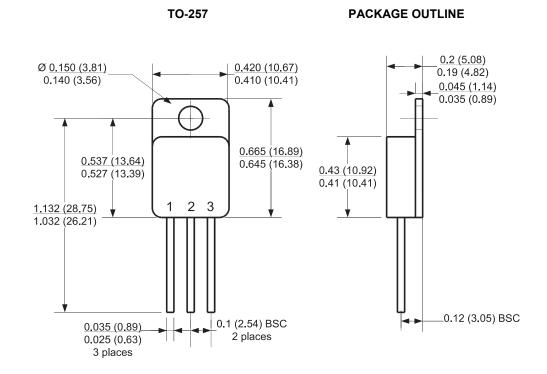


Figure 10: Typical Gate-Source Switching Waveforms

Package Dimensions:



- CONTROLLED DIMENSION IS INCH. DIMENSION IN BRACKET IS MILLIMETER.
 DIMENSIONS DO NOT INCLUDE END FLASH, MOLD FLASH, MATERIAL PROTRUSIONS



Revision History				
Date	Revision	Comments	Supersedes	
2012/08/24	0	Initial release		

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