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-1A / -60V Bipolar transistor

2SA2092

Features

- 1) High speed switching. (tf : Typ. : 30ns at Ic = -1A)
- 2) Low saturation voltage.

(Typ.: -200mV at Ic = -500mA, IB = -50mA)

- 3) Strong discharge resistance for inductive load and capacitance load.
- 4) Low switching noise.

Applications

High-speed switching, low frequency amplification

Structure

PNP epitaxial planar silicon transistor

Packaging specifications

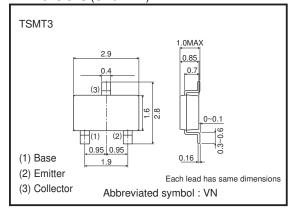
	Package	TSMT3
	Packaging type	Taping
	Code	TL
Part No.	Basic ordering unit (pieces)	3000
2SA2092		0

● Absolute maximum ratings (Ta=25°C)

Parameter		Symbol	Limits	Unit
Collector-base voltage		Vсво	-60	V
Collector-emitter voltage		VCEO	-60	V
Emitter-base voltage		VEBO	-6	V
Collector current	DC	lc	-1	Α
	PULSE	ICP *1	-2	Α
Power dissipation		Pc *2	500	mW
Junction temperature		Tj	150	°C
Range of storage temperature		Tstg	-55 to +150	°C

^{*1} Pw=10ms

●Dimensions (Unit : mm)



^{*2} Each terminal mounted on a recommended land

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-emitter breakdown voltage	BVceo	-60	_	_	V	Ic= -1mA
Collector-base breakdown voltage	ВУсво	-60	-	_	V	Ic= -100μA
Emitter-base breakdown voltage	ВУево	-6	-	_	V	I _E = -100μA
Collector cut-off current	Ісво	_	_	-1.0	μΑ	VcB= -40V
Emitter cut-off current	ІЕВО	_	_	-1.0	μΑ	V _{EB} = -4V
Collector-emitter saturation voltage	VCE(sat)	_	-200	-500	mV	Ic= -500mA, Iв= -50mA
DC current gain	hFE *3	120	_	270	-	VcE= -2V, Ic= -100mA
Transition frequency	f _T *1	_	300	_	MHz	Vc== -10V, Ie=100mA, f=10MHz
Collector output capacitance	Cob	_	15	_	pF	VcB= -10V, IE=0, f=1MHz
Turn-on time	ton	_	30	_	ns	Ic= -1A,
Storage time	tstg	_	100	_	ns	I _{B1} = -100mA I _{B2} =100mA
Fall time	tf *2	1	30	_	ns	Vcc ≃ -25V

^{*1} Pulse measurement

●hfe RANK

Q	
120-270	

•Electrical characteristic curves

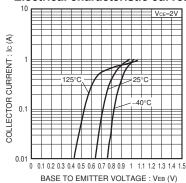


Fig.1 Grounded emitter propagation characteristics

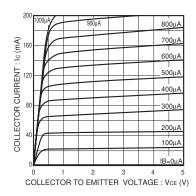


Fig.2 Typical output characteristics

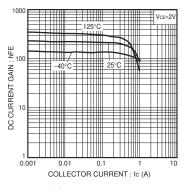


Fig.3 DC current gain vs. collector current (I)

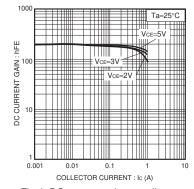


Fig.4 DC current gain vs. collector current (II)

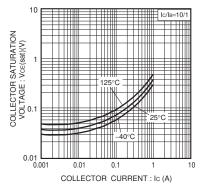


Fig.5 Collector-emitter saturation voltage vs. collector current (I)

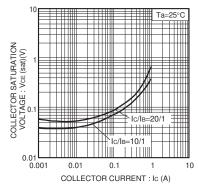


Fig.6 Collector-emitter saturation voltage vs. collector current (II)

^{*2} See switching test circuit

^{*3} hre rank

2SA2092 Data Sheet

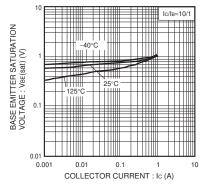


Fig.7 Base-emitter saturation voltage vs. collector current

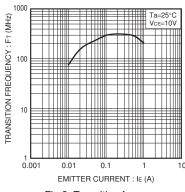


Fig.8 Transition frequency

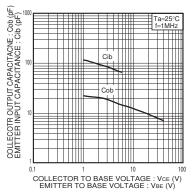


Fig.9 Collector output capacitance Emitter input capacitance

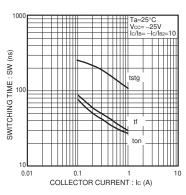
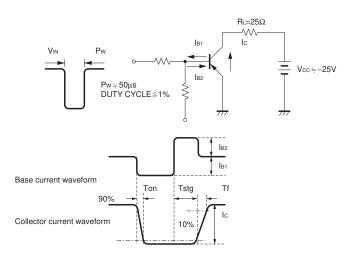


Fig.10 Switching Time

•Switching characteristics measurement circuits



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