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# 2SC4208, 2SC4208A

### Silicon NPN epitaxial planar type

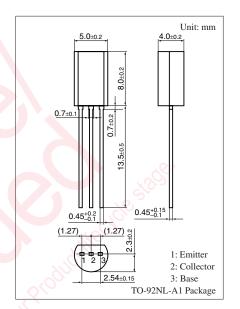
For low-frequency output amplification and driver amplification Complementary to 2SA1619 and 2SA1619A

#### ■ Features

- Low collector-emitter saturation voltage V<sub>CE(sat)</sub>
- Output of 1 W is obtained with a complementary pair with 2SA1619 and 2SA1619A
- Allowing supply with the radial taping

#### ■ Absolute Maximum Ratings $T_a = 25$ °C

Parameter	Symbol	Rating	Unit	
Collector-base voltage	2SC4208	$V_{CBO}$	30	V
(Emitter open)	2SC4208A		60	
Collector-emitter voltage	2SC4208	V <sub>CEO</sub>	25	V
(Base open)	2SC4208A		50	
Emitter-base voltage (Coll	V <sub>EBO</sub>	7	V	
Collector current	$I_{C}$	500	mA	
Peak collector current	I <sub>CP</sub>	1	A	
Collector power dissipation	$P_{C}$	1	W	
Junction temperature	T <sub>j</sub>	150	°C C	
Storage temperature	$T_{stg}$	-55 to +150	°C	



#### ■ Electrical Characteristics $T_a = 25$ °C $\pm 3$ °C

Parameter		Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage	2SC4208	$V_{CBO}$	$I_{\rm C} = 10 \; \mu \text{A}, \; I_{\rm E} = 0$	30			V
(Emitter open)	2SC4208A		9/21 9/13 NO CO	60			
Collector-emitter voltage	2SC4208	V <sub>CEO</sub>	$I_{\rm C} = 10 \text{ mA}, I_{\rm B} = 0$	25			V
(Base open)	2SC4208A		" () () (SE)	50			
Emitter-base voltage (Colle	ctor open)	$V_{EBO}$	$I_E = 10 \ \mu A, I_C = 0$	7			V
Forward current transfer ratio *1		h <sub>FE1</sub> *2	$V_{CE} = 10 \text{ V}, I_{C} = 150 \text{ mA}$	85		340	_
		h <sub>FE2</sub>	$V_{CE} = 10 \text{ V}, I_{C} = 500 \text{ mA}$	40			
Collector-emitter saturation voltage		V <sub>CE(sat)</sub>	$I_C = 300 \text{ mA}, I_B = 30 \text{ mA}$		0.35	0.60	V
Base-emitter saturation voltage		V <sub>BE(sat)</sub>	$I_C = 300 \text{ mA}, I_B = 30 \text{ mA}$		1.1	1.5	V
Transition frequency		$f_T$	$V_{CB} = 10 \text{ V}, I_E = -50 \text{ mA}, f = 200 \text{ MHz}$		150		MHz
Collector output capacitance		C <sub>ob</sub>	$V_{CB} = 10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$		6	15	pF
(Common base, input open	circuited)						

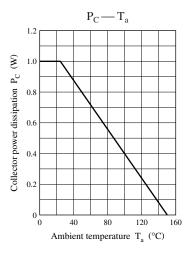
Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

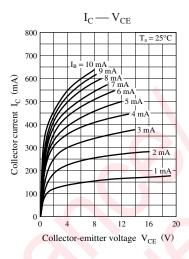
2. \*1: Pulse measurement

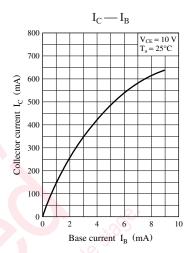
\*2: Rank classification

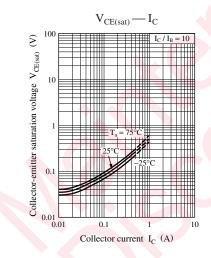
Rank	Q	R	S	
h <sub>FE1</sub>	85 to 170	120 to 240	170 to 340	

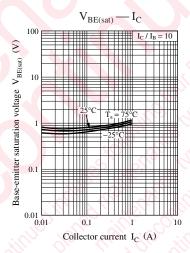
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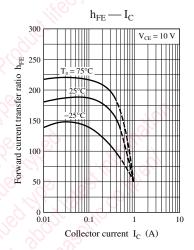


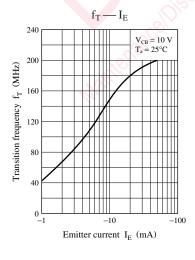


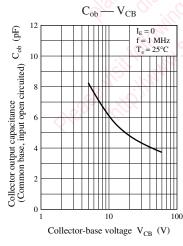


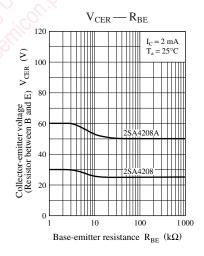






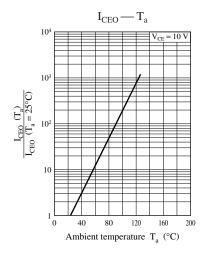


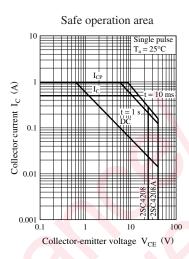




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