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High power NPN epitaxial planar bipolar transistor

Preliminary data

Features

- High breakdown voltage V_{CEO} = 230 V
- Typical f_T = 30 MHz

Application

■ Audio power amplifier

Description

This device is a NPN transistor manufactured using new BiT-LA (bipolar transistor for linear amplifier) technology. The resulting transistor shows good gain linearity behaviour.

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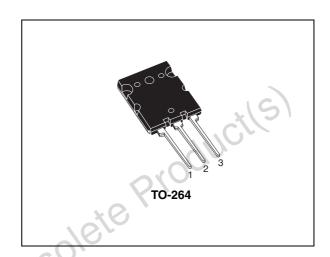


Figure 1. Internal schematic diagram

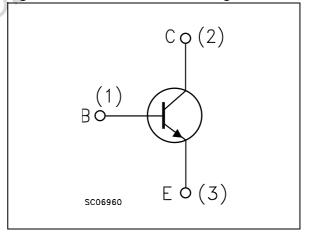


Table 1. Device summary

Order code	Marking	Package	Packaging
2SC5200	2SC5200	TO-264	Tube

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Electrical ratings 2SC5200

1 Electrical ratings

Table 2. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V _{CBO}	Collector-base voltage (I _E = 0)	230	V
V _{CEO}	Collector-emitter voltage (I _B = 0)	230	V
V _{EBO}	Emitter-base voltage (I _C = 0)	5	V
Ic	Collector current	15	Α
I _{CM}	Collector peak current	30	А
P _{TOT}	Total dissipation at T _C = 25 °C	150	W
T _{STG}	Storage temperature	-55 to 150	°C
T _J	Operating junction temperature	150	°C

Table 3. Thermal data

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Symbol Parameter		Value	Unit	
	R_{thJC}	Thermal resistance junction-case max	0.83	°C/W
Obsole	PY	oducits		•

Electrical characteristics 2

 T_{case} = 25 °C unless otherwise specified

Table 4. **Electrical characteristics**

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
I _{CBO}	Collector cut-off current (I _E = 0)	V _{CB} = 230 V			5	μΑ
I _{EBO}	Emitter cut-off current (I _C = 0)	V _{EB} = 5 V			5	μА
V _{(BR)CEO} ⁽¹⁾	Collector-emitter breakdown voltage (I _B = 0)	I _C = 50 mA	230	,	ille	V
V _{(BR)CBO}	Collector-base breakdown voltage (I _E = 0)	I _C = 100 μA	230	70,		٧
V _{(BR)EBO} ⁽¹⁾	Emitter-base breakdown voltage ($I_C = 0$)	I _E = 1 mA	5			٧
V _{CE(sat)} ⁽¹⁾	Collector-emitter saturation voltage	I _C = 8 A I _B = 800 mA			3	٧
V _{BE}	Base-emitter voltage	I _C = 7 A V _{CE} = 5 V			1.5	V
h _{FE}	DC current gain	$I_C = 1 \text{ A}$ $V_{CE} = 5 \text{ V}$ $I_C = 7 \text{ A}$ $V_{CE} = 5 \text{ V}$	55 35	80	120	
t _{on} t _s	Resistive load Turn-on time Storage time Fall time	$V_{CC} = 60 \text{ V}$ $I_C = 5A$ $I_{B1} = -I_{B2} = 0.5 \text{ A}$		0.24 4.7 0.6		μs μs μs
f _T	Transition frequency	I _C = 1 A V _{CE} = 5 V		30		MHz
C _{CBO}	Collector-base capacitance (I _E = 0)	V _{CB} = 10 V f = 1 MHz		150		pF
1. Pulsed: pu	llse duration = 300 μs, duty cycle s	≤ 1.5%				

2.1 Electrical characteristics (curves)

Figure 2. Safe operating area

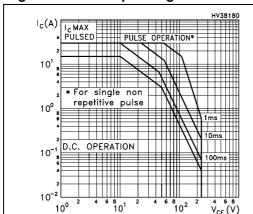
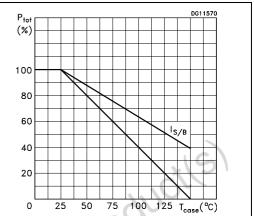
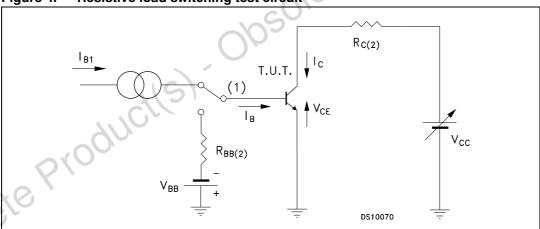


Figure 3. Derating curve



2.2 Test circuit

Figure 4. Resistive load switching test circuit



- 1. Fast electronic switch
- 2. Non-inductive resistor

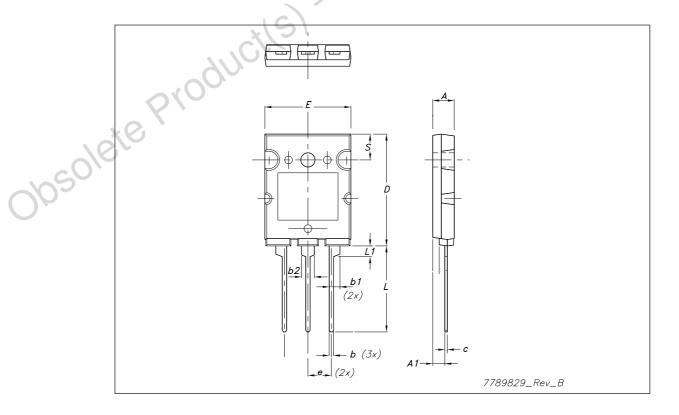
3 Package mechanical data

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: www.st.com. ECOPACK[®] is an ST trademark.

Obsolete Product(s). Obsolete Product(s)

TO-264 Mechanical data

Dim.	mm.			
Diiii.	Min. Typ		Max.	
Α	4.80		5.20	
A1	2.50		3.10	
b	0.90	1.0	1.25	
b1		2.5	,(5)	
b2		2.8	C	
С	0.50	0.60	0.85	
D	25.6		26.4	
Е	19.80		20.20	
е	5.15	9/2	5.75	
L	19.50	7/8,	20.50	
L1	2.30	1000	2.70	
øΡ	3.55	10-	3.65	



2SC5200 Revision history

4 Revision history

Table 5. Document revision history

Date	Revision	Changes
28-Sep-2009	1	Initial release.

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