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2SD1705

Silicon NPN epitaxial planar type

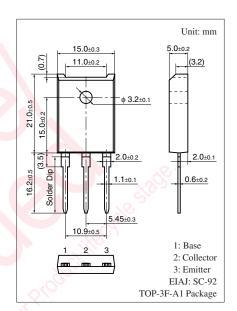
For power switching Complementary to 2SB1154

■ Features

- ullet Low collector-emitter saturation voltage $V_{\text{CE(sat)}}$
- Satisfactory linearity of forward current transfer ratio here
- Large collector current I_C
- Full-pack package which can be installed to the heat sink with one screw

■ Absolute Maximum Ratings $T_C = 25^{\circ}C$

Symbol	Rating	Unit	
V_{CBO}	130	V	
V _{CEO}	80	V	
V_{EBO}	7	V	
I_{C}	10	A	
I_{CP}	20	A	
P _C	70	W	
	3.0		
$T_{\rm j}$	150	°C	
T _{stg}	-55 to +150	°C√?	
	$\begin{array}{c} V_{CBO} \\ V_{CEO} \\ \end{array}$ $\begin{array}{c} V_{CBO} \\ \end{array}$ $\begin{array}{c} I_{C} \\ \end{array}$ $\begin{array}{c} I_{CP} \\ \end{array}$ $\begin{array}{c} P_{C} \\ \end{array}$ $\begin{array}{c} T_{j} \\ \end{array}$	V _{CBO} 130 V _{CEO} 80 V _{EBO} 7 I _C 10 I _{CP} 20 P _C 70 3.0 T _j 150	



■ Electrical Characteristics $T_C = 25^{\circ}C \pm 3^{\circ}C$

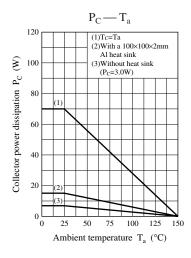
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-emitter voltage (Base open)	V _{CEO}	$I_C = 10 \text{ mA}, I_B = 0$	80	80.		V
Collector-base cutoff current (Emitter open)	I_{CBO}	$V_{CB} = 100 \text{ V}, I_{E} = 0$			10	μΑ
Emitter-base cutoff current (Collector open)	I_{EBO}	$V_{EB} = 5 \text{ V}, I_{C} = 0$	1.1		50	μΑ
Forward current transfer ratio	h _{FE1}	$V_{CE} = 2 \text{ V}, I_{C} = 0.1 \text{ A}$	45			_
	h _{FE2} *	$V_{CE} = 2 \text{ V}, I_{C} = 3 \text{ A}$	90		260	
	h _{FE3}	$V_{CE} = 2 \text{ V}, I_{C} = 6 \text{ A}$	30			
Collector-emitter saturation voltage	V _{CE(sat)1}	$I_C = 6 \text{ A}, I_B = 0.3 \text{ A}$			0.5	V
	V _{CE(sat)2}	$I_C = 10 \text{ A}, I_B = 1 \text{ A}$			1.5	
Base-emitter saturation voltage	V _{BE(sat)1}	$I_C = 6 \text{ A}, I_B = 0.3 \text{ A}$			1.5	V
	V _{BE(sat)2}	$I_C = 10 \text{ A}, I_B = 1 \text{ A}$			2.5	
Transition frequency	f_T	$V_{CE} = 10 \text{ V}, I_{C} = 0.5 \text{ A}, f = 1 \text{ MHz}$		20		MHz
Turn-on time	t _{on}	$I_C = 6 \text{ A}, I_{B1} = 0.6 \text{ A}, I_{B2} = -0.6 \text{ A}$		0.5		μs
Storage time	t _{stg}	$V_{CC} = 50 \text{ V}$		2.0		μs
Fall time	t _f			0.2		μs

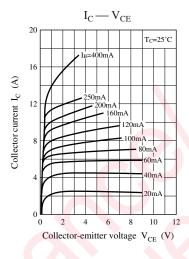
Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

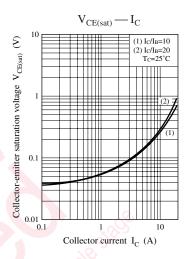
2. *: Rank classification

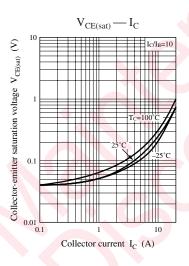
Rank	Q	Р
h _{FE2}	90 to 180	130 to 260

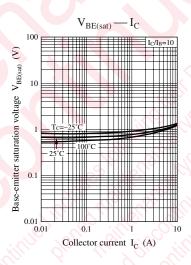
Panasonic

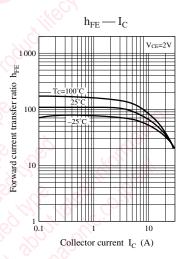


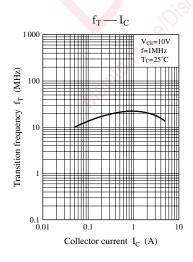


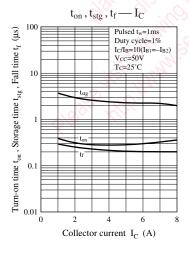


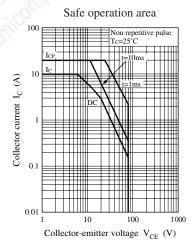




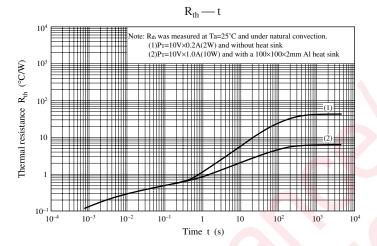








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