

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China







2SD1805

ON Semiconductor®

http://onsemi.com

Bipolar Transistor 20V, 5A, Low VCE(sat), NPN Single TP/TP-FA

Applications

· Strobes, voltage regulators, relay drivers, lamp drivers

Features

- · Low saturation voltage
- · Fast switching time
- · Large current capacity
- · Small and slim package making it easy to make 2SD1805-applied sets smaller

Specifications

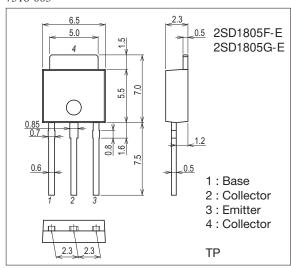
Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	VCBO		60	V
Collector-to-Emitter Voltage	VCEO		20	V
Emitter-to-Base Voltage	VEBO		6	V
Collector Current	IC		5	Α
Collector Current (Pulse)	ICP		8	Α
Collector Dissipation	PC		1	W
		Tc=25°C	15	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

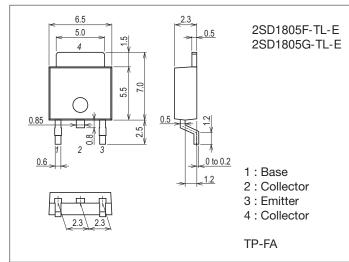
Package Dimensions unit: mm (typ)

7518-003



Package Dimensions unit: mm (typ)

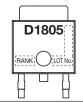
7003-003



Product & Package Information

- Package : TP
- JEITA, JEDEC : SC-64, TO-251
- Minimum Packing Quantity: 500 pcs./bag

Marking (TP, TP-FA)



- Package : TP-FA
- JEITA, JEDEC : SC-63, TO-252
- Minimum Packing Quantity: 700 pcs./reel

Packing Type (TP-FA): TL

TL T





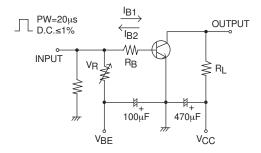
Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit	
Parameter	Symbol	Conditions	min	typ	max	Uill	
Collector Cutoff Current	ICBO	V _{CB} =50V, I _E =0A			100	nA	
Emitter Cutoff Current	IEBO	V _{EB} =5V, I _C =0A			100	nA	
DC Current Gain	hFE1	V _{CE} =2V, I _C =500mA	120*		560*		
DC Current Gain	hFE2	V _{CE} =2V, I _C =3A	95				
Gain-Bandwidth Product	fŢ	V _{CE} =10V, I _C =50mA		120		MHz	
Output Capacitance	Cob	V _{CB} =10V, f=1MHz		45		pF	
Collector-to-Emitter Saturation Voltage	V _{CE} (sat)	I _C =3A, I _B =60mA		220	500	mV	
Base-to-Emitter Saturation Voltage	V _{BE} (sat)	I _C =3A, I _B =60mA			1.5	V	
Collector-to-Base Breakdown Voltage	V(BR)CBO	IC=10μA, IE=0A	60			V	
Collector-to-Emitter Breakdown Voltage	V(BR)CEO	I _C =1mA, R _{BE} =∞	20			V	
Emitter-to-Base Breakdown Voltage	V(BR)EBO	I _E =10μA, I _C =0A	6			V	
Turn-On Time	ton			30		ns	
Storage Time	tstg	See specified Test Circuit		300		ns	
Fall Time	tf			40		ns	

*: The 2SD1805 is classified by 500mA hFE as follows.

Rank	E	F	G	
hFE	120 to 200	160 to 320	280 to 560	

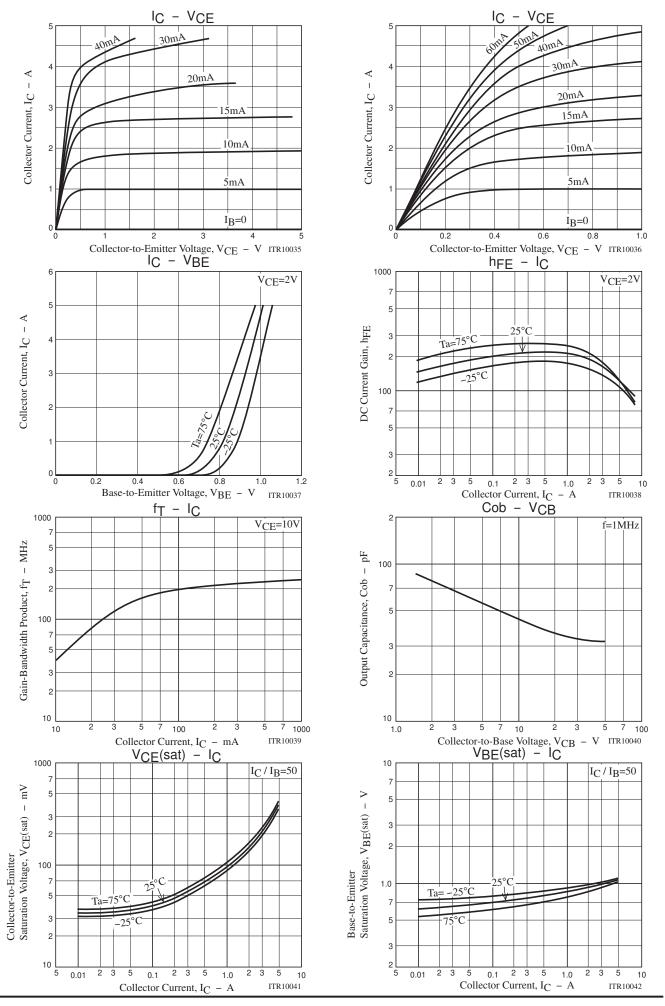
Switching Time Test Circuit

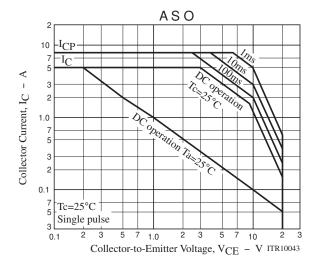


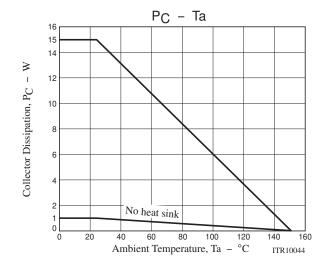
 $I_{C}=10I_{B1}=-10I_{B2}=2A, V_{CC}=10V$

Ordering Information

Device	Package	Shipping	memo	
2SD1805F-E	TP	500pcs./bag		
2SD1805G-E	TP	500pcs./bag	Pb Free	
2SD1805F-TL-E	TP-FA	700pcs./reel	Pb Free	
2SD1805G-TL-E	TP-FA	700pcs./reel		





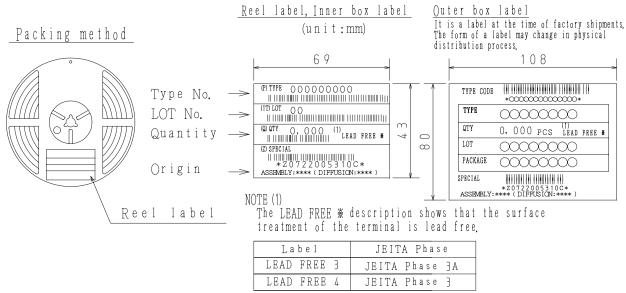


Taping Specification

2SD1805F-TL-E, 2SD1805G-TL-E

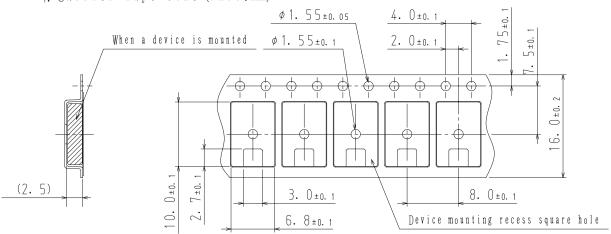
Packing Format

Package Name	Carrier Tape	Maximum Number of devices contained (pcs)			Packing	f o r m a t
	Туре	Reel	Inner box	Outer box	Inner BOX (C-1)	Outer BOX (A-7)
TP-FA	TP	700	2, 100	12, 600	3 reels contained	6 inner boxes contained
					Dimensions:mm (external)	Dimensions:mm (external)
					183×72×185	440×195×210

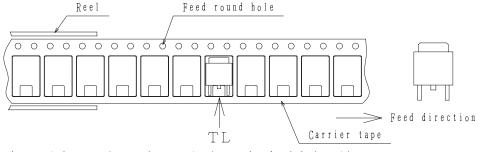


Taping configuration

1. Carrier tape size (unit:mm)



7. Device placement direction



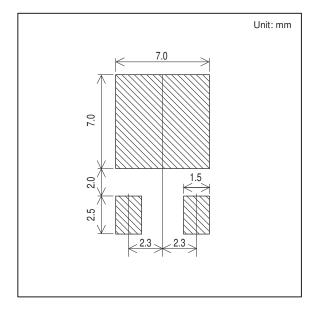
Those with one electrode terminal on the feed hole side · · · · · TL

Outline Drawing

2SD1805F-TL-E, 2SD1805G-TL-E

Mass (g) Unit 0.282 *For reference mm 6. 5±0. 2 2. 3±0. 2 5. O±0. 2 1. 5±0. 2 0. 5±0. 1 [*1] 7.0±0.3 5. 5±0. 2 LOT No. 1. 2±0. 3 0. 5±0. 15 L 0. 85±0. 2 2. 5±0. 3 3 1. 2±0. 3 0.6±0.2 0~0.2 2. 3±0. 2 2. 3±0. 2 Pin 2 is idle pin with electrical designation only carried. *1:Lot indication

Land Pattern Example



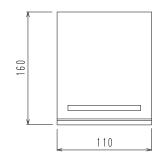
Bag Packing Specification

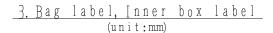
2SD1805F-E, 2SD1805G-E

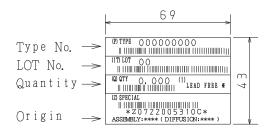
1. Packing Format

Package Name		Maximum Number of devices contained (pcs)					
1 4 0 11 4 5 0 1 1 4 4 11 0	Bag I		Outer box				
TP		B-1	A-1	A-2			
11	500	10,000	50,000	30,000			
		Packing format (Dimensions:mm (external))					
		Inner box	Outer box				
		B-1	A-1	A-2			
		445×225×55	470×250×300	470×250×190			

2. Bag dimensions (unit:mm)





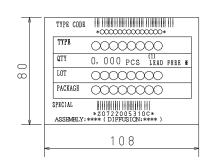


4. Outer box label (unit:mm)

It is a label at the time of factory shipments, The form of a label may change in physical distribution process,

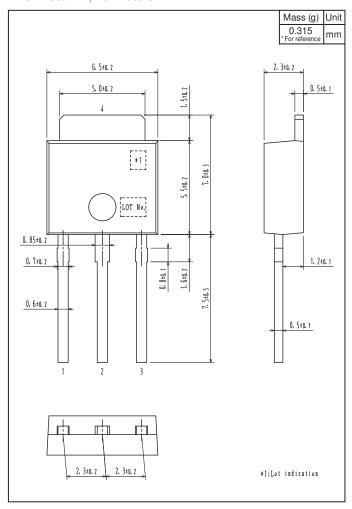
NOTE (1)
The LEAD FREE * description shows that the surface treatment of the terminal is lead free.

Label			JEITA Phase
LEAD	FREE	3	JEITA Phase 3A
LEAD	FREE	4	JEITA Phase 3



Outline Drawing

2SD1805F-E, 2SD1805G-E



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