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## 2SD2000

## Silicon NPN triple diffusion planar type

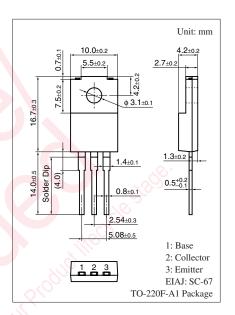
#### For power switching

#### ■ Features

- High-speed switching
- Satisfactory linearity of forward current transfer ratio h<sub>FE</sub>
- Large collector power dissipation P<sub>C</sub>
- Full-pack package which can be installed to the heat sink with one screw.

### ■ Absolute Maximum Ratings $T_C = 25^{\circ}C$

Parameter	Symbol	Rating	Unit	
Collector-base voltage (Emitter open)	$V_{CBO}$	80	V	
Collector-emitter voltage (Base open)	V <sub>CEO</sub>	60	V	
Emitter-base voltage (Collector open)	$V_{EBO}$	6	V	
Collector current	$I_{C}$	4	A	
Peak collector current	$I_{CP}$	8	A	
Base current	$I_{B}$	1	A	
Collector power	P <sub>C</sub>	35	W	
dissipation $T_a = 25^{\circ}C$		2.0	100	
Junction temperature	$T_{j}$	150	°C	
Storage temperature	$T_{stg}$	-55 to +150	S °C	
			0.47	



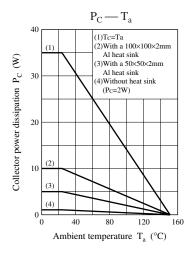
## ■ Electrical Characteristics $T_C = 25^{\circ}C \pm 3^{\circ}C$

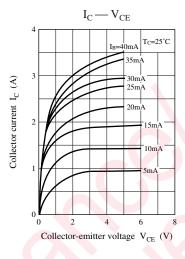
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-emitter voltage (Base open)	V <sub>CEO</sub>	$I_C = 25 \text{ mA}, I_B = 0$	60	)·		V
Base-emitter voltage	$V_{BE}$	$V_{CE} = 4 \text{ V}, I_{C} = 4 \text{ A}$	1.90		2.0	V
Collector-base cut-off current (Emitter open)	$I_{CBO}$	$V_{CB} = 80 \text{ V}, I_{E} = 0$			100	μΑ
Emitter-base cut-off current (Collector open)	$I_{EBO}$	$V_{EB} = 6 \text{ V}, I_{C} = 0$			100	μΑ
Forward current transfer ratio	h <sub>FE1</sub> *	$V_{CE} = 4 \text{ V}, I_{C} = 1 \text{ A}$	70		250	_
	h <sub>FE2</sub>	$V_{CE} = 4 \text{ V}, I_{C} = 4 \text{ A}$	20			
Collector-emitter saturation voltage	V <sub>CE(sat)</sub>	$I_C = 4 \text{ A}, I_B = 0.4 \text{ A}$			1.5	V
Transition frequency	$f_T$	$V_{CE} = 12 \text{ V}, I_{C} = 0.2 \text{ A}, f = 10 \text{ MHz}$		80		MHz
Turn-on time	t <sub>on</sub>	$I_C = 4 \text{ A}, I_{B1} = 0.4 \text{ A}, I_{B2} = -0.4 \text{ A},$		0.3		μs
Storage time	t <sub>stg</sub>	$V_{CC} = 50 \text{ V}$		1.0		μs
Fall time	$t_{\rm f}$			0.2		μs

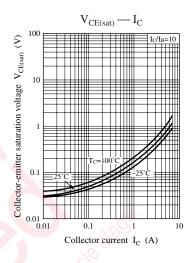
Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

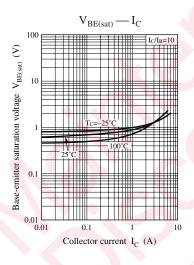
#### 2. \*: Rank classification

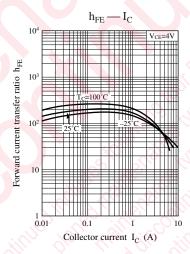
Rank	Q	Р
h <sub>FE1</sub>	70 to 150	120 to 250

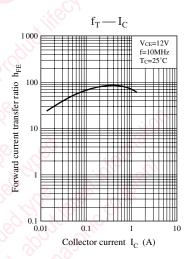


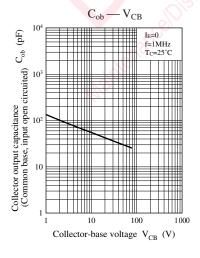


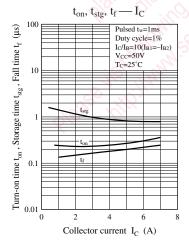


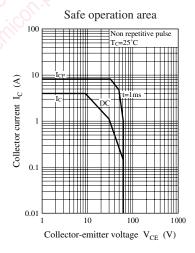




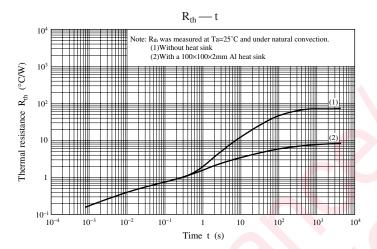








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