imall

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

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Package

Pin Name

SSSMini3-F1

• Code

2SJ0674

Silicon P-channel MOS FET

For switching circuits

Features

- Low ON resistance R_{on}
- High-speed switching
- SSSMini type package, allowing downsizing of the equipment and automatic insertion through the tape packing

Absolute Maximum Ratings $T_a = 25^{\circ}C$

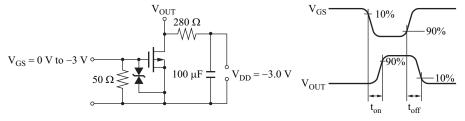
• SSSMini type package, allowing d	ownsizing o	of the equipr	nent and	• Pin Name			
automatic insertion through the tape pac	king			1: Gate		ć	<u>୍</u> ଡ ·
				2: Source		~ x ??	う
Absolute Maximum Ratings T _a	=25°C			3: Drain		esta	
Parameter	Symbol	Rating	Unit		^v O	Ň	
Drain-source surrender voltage	V _{DSS}	-30	V	Marking Syr			
Gate-source surrender voltage	V _{GSS}	±12	V	X			
Drain current	ID	-100	mA	duct			
Peak drain current	I _{DP}	-200	mA	or ^{oc}			
Power dissipation	P _D	100	mW	A CARACTER STATE			
Channel temperature	T _{ch}	125	°C	40 ¹ e			tion
Storage temperature	T _{stg}	-55 to +125	°C	19 , 19		~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~	
			N	6 0		<u> </u>	
Electrical Characteristics $T_a = 2$	5°C±3°C			10° 10° 10°	ې ب	<u>,0,</u>	
Parameter	Symbo	bl	Conditions	Min	Тур	Max	Unit

Electrical Characteristics $T_a = 25^{\circ}C \pm 3^{\circ}C$

	0.020.0		N.			
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Drain-source surrender voltage	V _{DSS}	$I_D = -10 \mu A, V_{GS} = 0$	G 30	X C		V
Drain-source cutoff current	I _{DSS}	$V_{DS} = -20 X V_{GS} = 0$			-1.0	μΑ
Gate-source cutoff current	I _{GSS}	$V_{GS} = \pm 10 \text{ V}, V_{DS} = 0$		NS IS	±10	μΑ
Gate threshold voltage	VIII	$I_{D} = -1.0 \mu A, V_{DS} = -3.0 V$	- 0.5	-1.0	-1.5	V
Drain-source ON resistance	il ·	$P_{\rm D} = -10 \text{ mA}, V_{\rm SS} = -2.5 \text{ V}$	111	13	30	Ω
	R _{DS(on)}	$I_D = -10 \text{ mA}, V_{GS} = 4.0 \text{ V}$		9	18	
Forward transfer admittance	Y _{fs}	$I_D = -10 \text{ mA}, V_{DS} = -3 \text{ V}, f = 1 \text{ kHz}$	20	40		mS
Short-circuit input capacitance	C _{iss}	10.116		12		pF
(Common source)	CISS			12		PI
Short-circuit output capacitance (Common source)	C _{oss}	$V_{DS} = -3$ $(\xi V_{GS} = 0, f = 1 \text{ MHz})$		13		pF
Reverse transfer capacitance	C _{rss}	CO CO		7		pF
(Common source)	Crss	₽ `		,		Ъг
Turn-on time *	t _{on}	$V_{DD} = -3 V, V_{GS} = 0 V \text{ to} -3 V, I_D = -10 \text{ mA}$		300		ns
Turn-off time *	t _{off}	$V_{DD} = -3 V$, $V_{GS} = -3 V$ to $0 V$, $I_D = -10 mA$		400		ns

Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

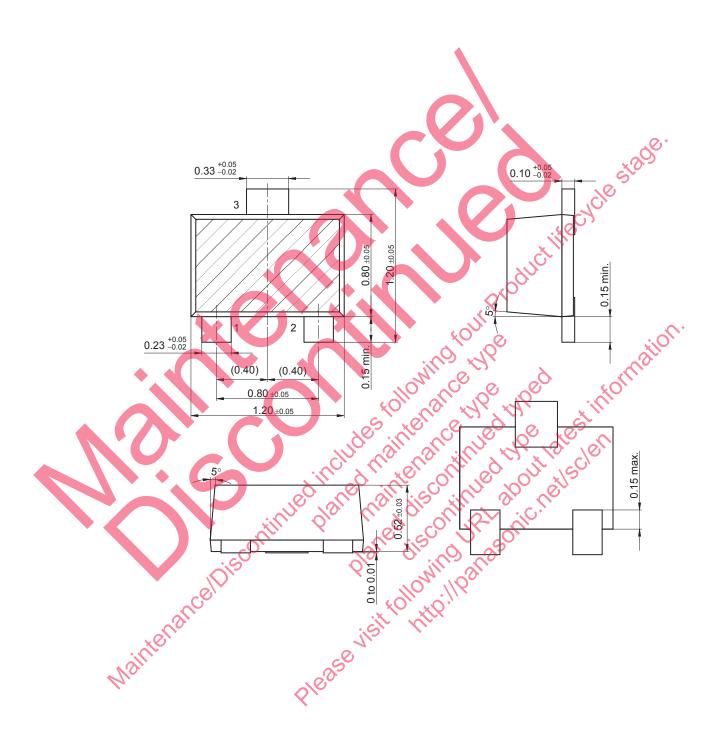
2. * : t_{on} , t_{off} measurement circuit



Panasonic

SSSMini3-F1

Unit: mm



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