



Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of “Quality Parts,Customers Priority,Honest Operation,and Considerate Service”,our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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2SK3268

Silicon N-channel power MOS FET

■ Features

- Avalanche energy capability guaranteed
- High-speed switching
- Low ON resistance R_{on}
- No secondary breakdown
- Low-voltage drive
- High electrostatic energy capability

■ Applications

- Non-contact relay
- Solenoid drive
- Motor drive
- Control equipment
- Switching mode regulator

■ Absolute Maximum Ratings $T_C = 25^\circ\text{C}$

Parameter	Symbol	Rating	Unit
Drain-source surrender voltage	V_{DSS}	100	V
Gate-source surrender voltage	V_{GSS}	± 20	V
Drain current	I_D	± 15	A
Peak drain current	I_{DP}	± 60	A
Avalanche energy capability *	EAS	22.5	mJ
Power dissipation	P_D	20	W
		$T_a = 25^\circ\text{C}$	1
Channel temperature	T_{ch}	150	$^\circ\text{C}$
Storage temperature	T_{stg}	-55 to +150	$^\circ\text{C}$

Note) *: $L = 0.2 \text{ mH}$, $I_L = 15 \text{ A}$, 1 pulse

■ Electrical Characteristics $T_C = 25^\circ\text{C} \pm 3^\circ\text{C}$

Parameter	Symbol	Conditions	Min	Typ	Max	Unit	
Drain-source surrender voltage	V_{DSS}	$I_D = 1 \text{ mA}$, $V_{GS} = 0$	100			V	
Drain-source cutoff current	I_{DSS}	$V_{DS} = 80 \text{ V}$, $V_{GS} = 0$			10	μA	
Gate-source cutoff current	I_{GSS}	$V_{GS} = \pm 20 \text{ V}$, $V_{DS} = 0$			± 1	μA	
Gate threshold voltage	V_{th}	$V_{DS} = 10 \text{ V}$, $I_D = 1 \text{ mA}$	2.0		4.0	V	
Forward transfer admittance	$ Y_{fs} $	$V_{DS} = 10 \text{ V}$, $I_D = 12 \text{ A}$	6	11		S	
Drain-source ON resistance	$R_{DS(on)}$	$V_{GS} = 10 \text{ V}$, $I_D = 12 \text{ A}$		70	100	$\text{m}\Omega$	
Diode forward voltage	V_{DF}	$I_{DR} = 15 \text{ A}$, $V_{GS} = 0$			-1.4	V	
Short-circuit forward transfer capacitance (Common source)	C_{iss}	$V_{DS} = 10 \text{ V}$, $V_{GS} = 0$, $f = 1 \text{ MHz}$		960		pF	
					285		pF
					85		pF
Reverse transfer capacitance (Common source)	C_{rss}					pF	
Turn-on delay time	$t_{d(on)}$	$V_{DD} = 30 \text{ V}$, $I_D = 12 \text{ A}$, $R_L = 2.5 \Omega$ $V_{GS} = 10 \text{ V}$		15		ns	
Rise time	t_r			10		ns	
Fall time	t_f			35		ns	
Turn-off delay time	$t_{d(off)}$			65		ns	
Thermal resistance (ch-c)	$R_{th(ch-c)}$				6.25	$^\circ\text{C/W}$	
Thermal resistance (ch-a)	$R_{th(ch-a)}$				125	$^\circ\text{C/W}$	

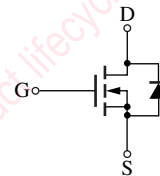
Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

■ Package

- Code
U-DL
- Pin Name
1: Gate
2: Drain
3: Source

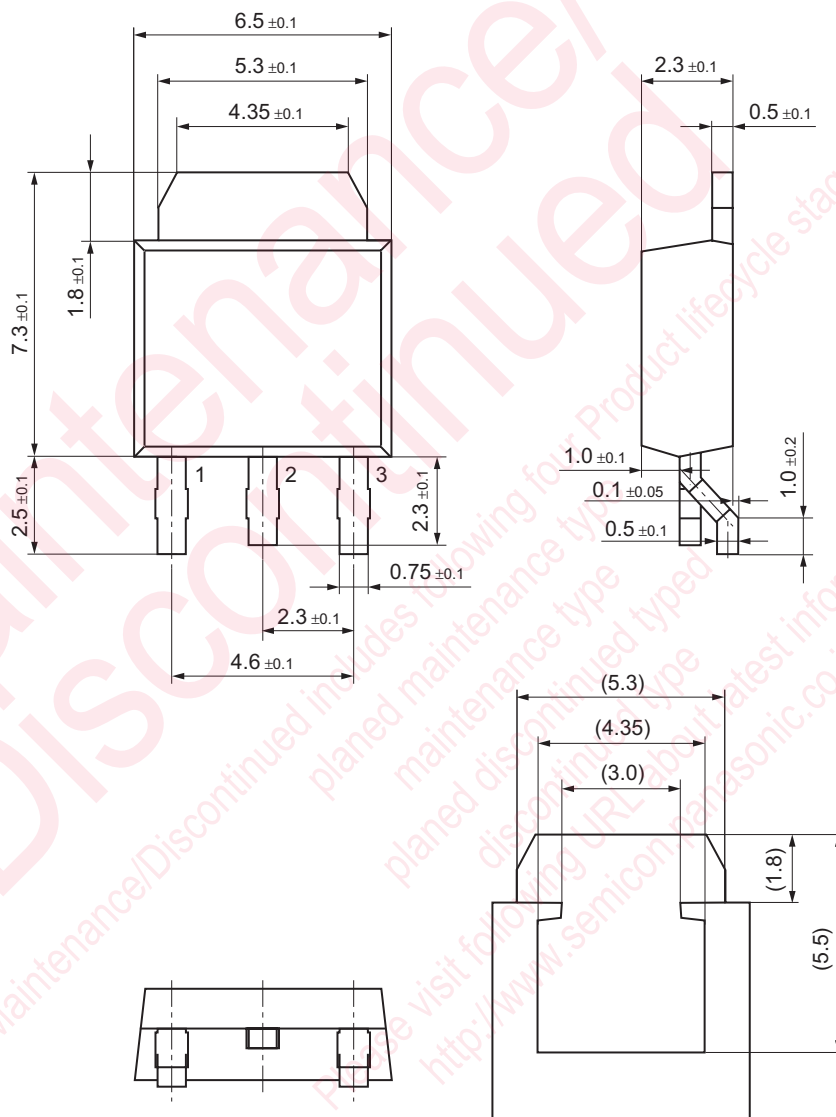
■ Marking Symbol: K3268

■ Internal Connection



U-DL

Unit: mm



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