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With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

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## 2SK3546J

## Silicon N-Channel MOSFET

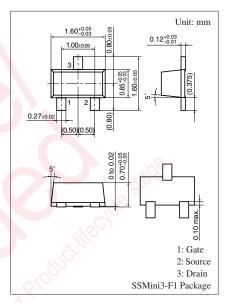
#### For switching

#### ■ Features

- High-speed switching
- Wide frequency band

## ■ Absolute Maximum Ratings $T_a = 25$ °C

Parameter	Symbol	Rating	Unit	
Drain-source voltage	V <sub>DS</sub>	50	V	
Gate-source voltage (Drain open)	V <sub>GSO</sub>	±7	V	
Drain current	$I_D$	100	mA	
Peak drain current	$I_{DP}$	200	mA	
Power dissipation	$P_{D}$	125	mW	
Channel temperature	T <sub>ch</sub>	125	°C	
Storage temperature	T <sub>stg</sub>	-55 to +150	°C	



Marking Symbol: 5F

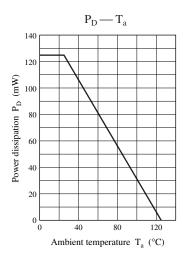
## ■ Electrical Characteristics $T_a = 25^{\circ}C \pm 3^{\circ}C$

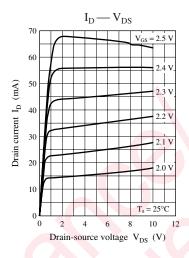
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Drain-source surrender voltage	V <sub>DSS</sub>	$I_D = 10  \mu A,  V_{GS} = 0$	50		40	V
Drain-source cutoff current	$I_{DSS}$	$V_{DS} = 50 \text{ V}, V_{GS} = 0$	.)	X	1.0	μΑ
Gate-source cutoff current	$I_{GSS}$	$V_{GS} = \pm 7 \text{ V}, V_{DS} = 0$	100	100	±5.0	μΑ
Gate threshold voltage	V <sub>th</sub>	$I_D = 1.0 \mu\text{A},  V_{DS} = 3  \text{V}$	0.9	1.2	1.5	V
Drain-source ON resistance	R <sub>DS(on)</sub>	$I_D = 10 \text{ mA}, V_{GS} = 2.5 \text{ V}$	70,	8	15	Ω
	dille	$I_D = 10 \text{ mA}, V_{GS} = 4.0 \text{ V}$	2	6	12	
Forward transfer admittance	$ Y_{fs} $	$I_D = 10 \text{ mA}, V_{DS} = 3 \text{ V}, f = 1 \text{ kHz}$	20	60		mS
Short-circuit forward transfer capacitance (Common source)	C <sub>iss</sub>	$V_{DS} = 3 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$		12		pF
Short-circuit output capacitance (Common source)	C <sub>oss</sub>	$V_{DS} = 3 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$		7		pF
Reverse transfer capacitance (Common source)	C <sub>rss</sub>	$V_{DS} = 3 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$		3		pF
Turn-on time *	t <sub>on</sub>	$V_{DD} = 3 \text{ V}, V_{GS} = 0 \text{ V to } 3 \text{ V}, R_L = 470 \Omega$		200		ns
Turn-off time *	t <sub>off</sub>	$V_{DD} = 3 \text{ V}, V_{GS} = 3 \text{ V to } 0 \text{ V}, R_{L} = 470 \Omega$		200		ns

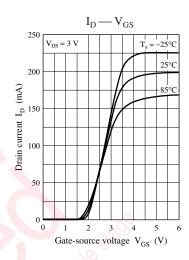
Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

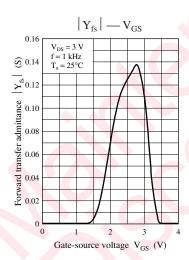
2. \*:  $t_{on}$ ,  $t_{off}$  test circuit  $V_{OUT} = 3.0 \text{ V}$   $S_{OUT} = 3.0 \text{ V}$ 

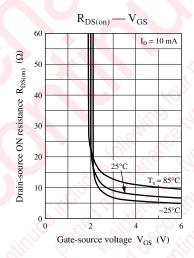
Publication date: July 2007 SJF00037BED 1

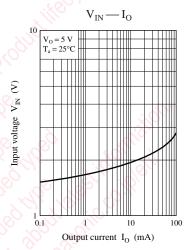












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