



Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts,Customers Priority,Honest Operation,and Considerate Service",our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

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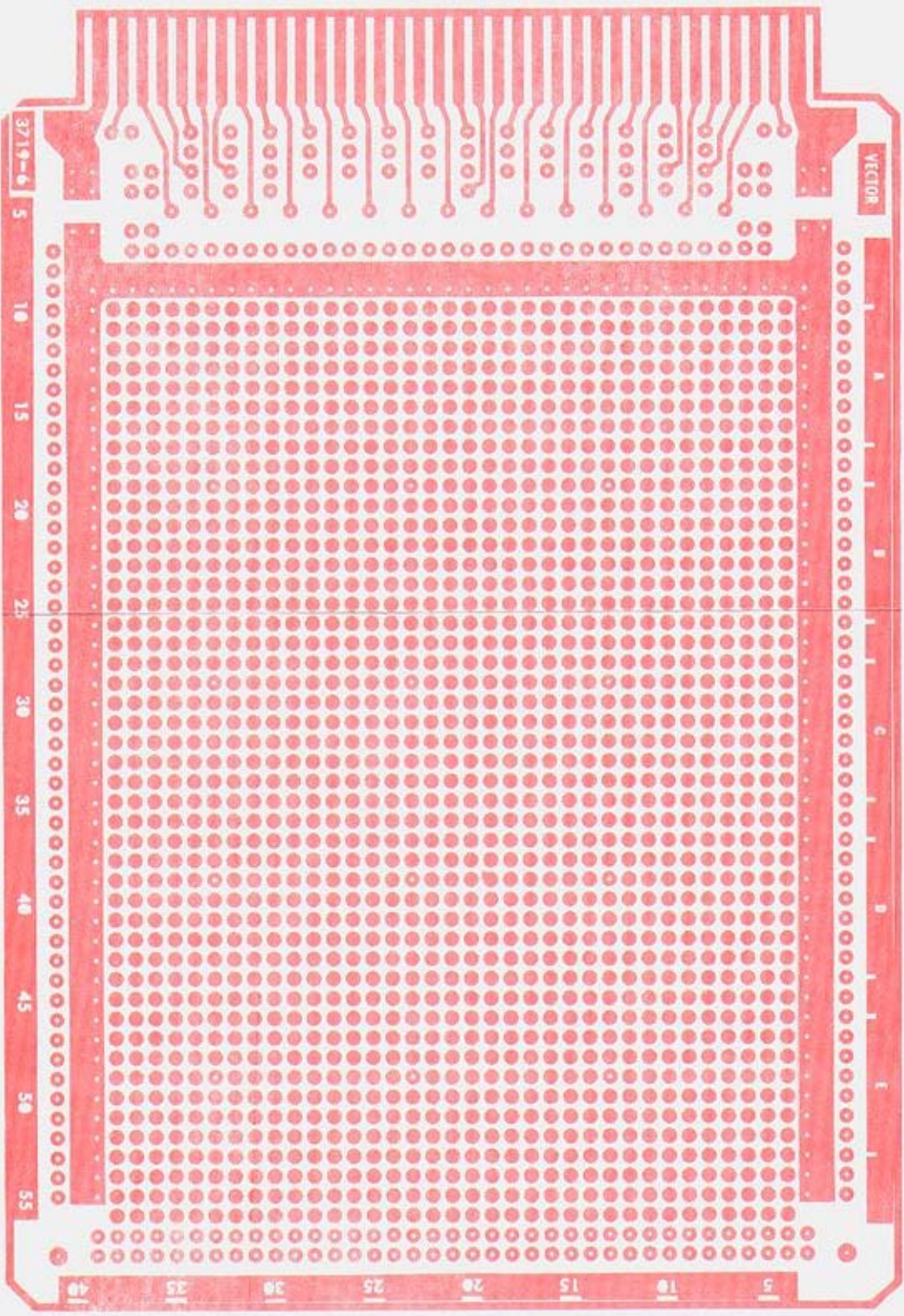
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CAUTION: Note that the holes adjacent to the inside edge of the peripheral bus, when looking at the wiring side, are connected to the bus on the component side. Therefore, use these holes only for bus connections.

- NOTES:**
1. Intended for use in non-hostile environments up to 200 volts RMS or 300 volts DC.
 2. Where tin coated circuitry exists, a small percentage of the holes may have solder blockage. This is usually a light "skin" easily penetrated by component leads. In some cases a soldering iron may be required.
 3. In any plug contact area on either side of Plugboard, use only those holes having pads. Holes without pads may have insufficient clearance to adjacent circuitry and using them could cause shorting.
 4. Before pressing terminals into board, position (rotate) terminals to maximize the clearance between the widest part of the terminal and the nearest adjacent conductor.



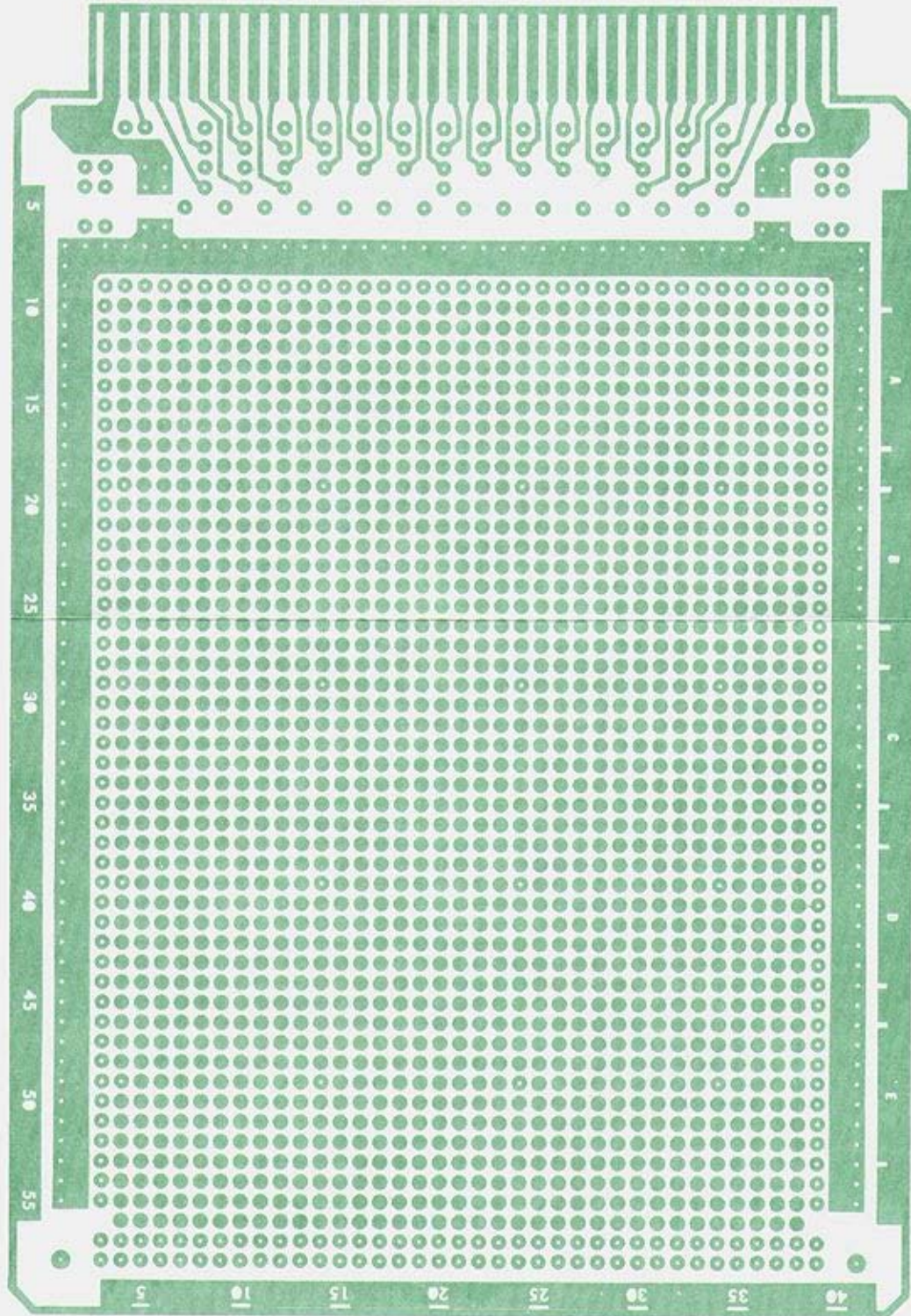
3719-6
COMPONENT SIDE
LAYOUT PAPER



VECTOR PAD/HOLE PLUGBOARD
PATTERN 0.043" DIA. HOLES
0.10" x 0.10" GRID

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3719-6
WIRING SIDE
LAYOUT PAPER



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