



Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts,Customers Priority,Honest Operation,and Considerate Service",our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

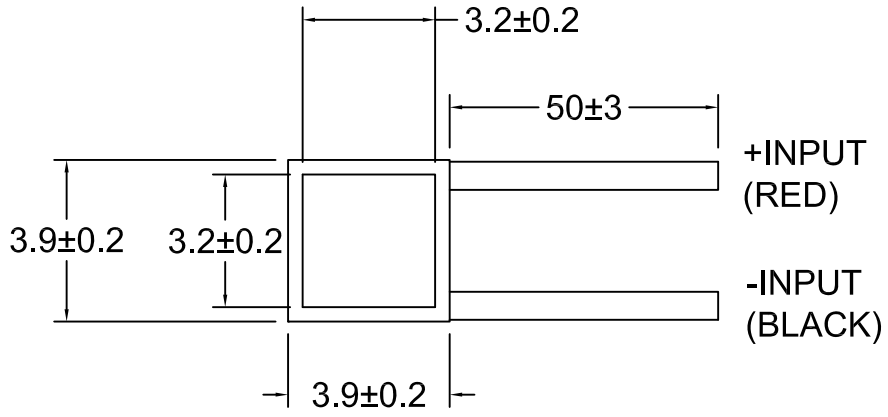
Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China

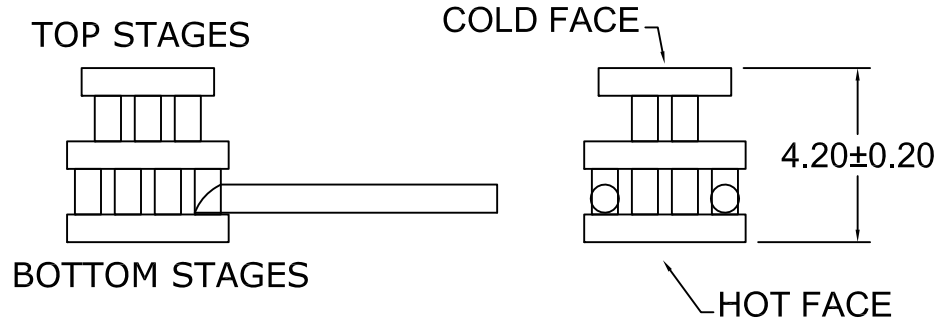


REV	BY	DATE	ECN/ERO	DESCRIPTION
00	Jinlong	09/16/17	-	Production release



NOTES:

1. Use 5%Sb,95%Sn for solder.
2. Wire are UL1213, AWG28,7/36,PTFE.
3. Lapped both side, height is 4.20±0.20mm, flatness and parallel is 0.05mm.
4. ACR range @23°C: 0.522-0.580-0.638 ohm



REFERENCE ONLY!	
Nominal Specs (@25°C)	
I _{max} (A)	1.4
U _{max} (V)	0.8
Q _{max} (W)	0.36
ΔT _{max} (°C)	87

NOTE:
ALL DIMENSIONS = mm

<p>4707 Detroit Ave Cleveland, OH 44102</p> <p>Ph.(216) 939-2300 Fax(216) 939-2310</p>		P/N	DESCRIPTION										
		387001820	MS2,010,06,06,11,11,11,W2,ET										
<p>SURFACE FINISH 3.2 √μm 125 √μin (UNLESS OTHERWISE SPECIFIED)</p> <p>BREAK ALL EDGES; 0.015 [0.40mm] x 45°</p>		TITLE											
		STANDARD 2-STAGE CASCADE CATALOG											
		232° SbSn CONSTRUCTION											
<table border="1"> <thead> <tr> <th>NAME</th> <th>DATE</th> </tr> </thead> <tbody> <tr> <td>DRAWN BY</td> <td>Jinlong 09/16/17</td> </tr> <tr> <td>INITIATED BY</td> <td></td> </tr> <tr> <td>APPROVAL</td> <td></td> </tr> </tbody> </table>		NAME	DATE	DRAWN BY	Jinlong 09/16/17	INITIATED BY		APPROVAL		DWG NO	387001820	REV	00
NAME	DATE												
DRAWN BY	Jinlong 09/16/17												
INITIATED BY													
APPROVAL													