

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

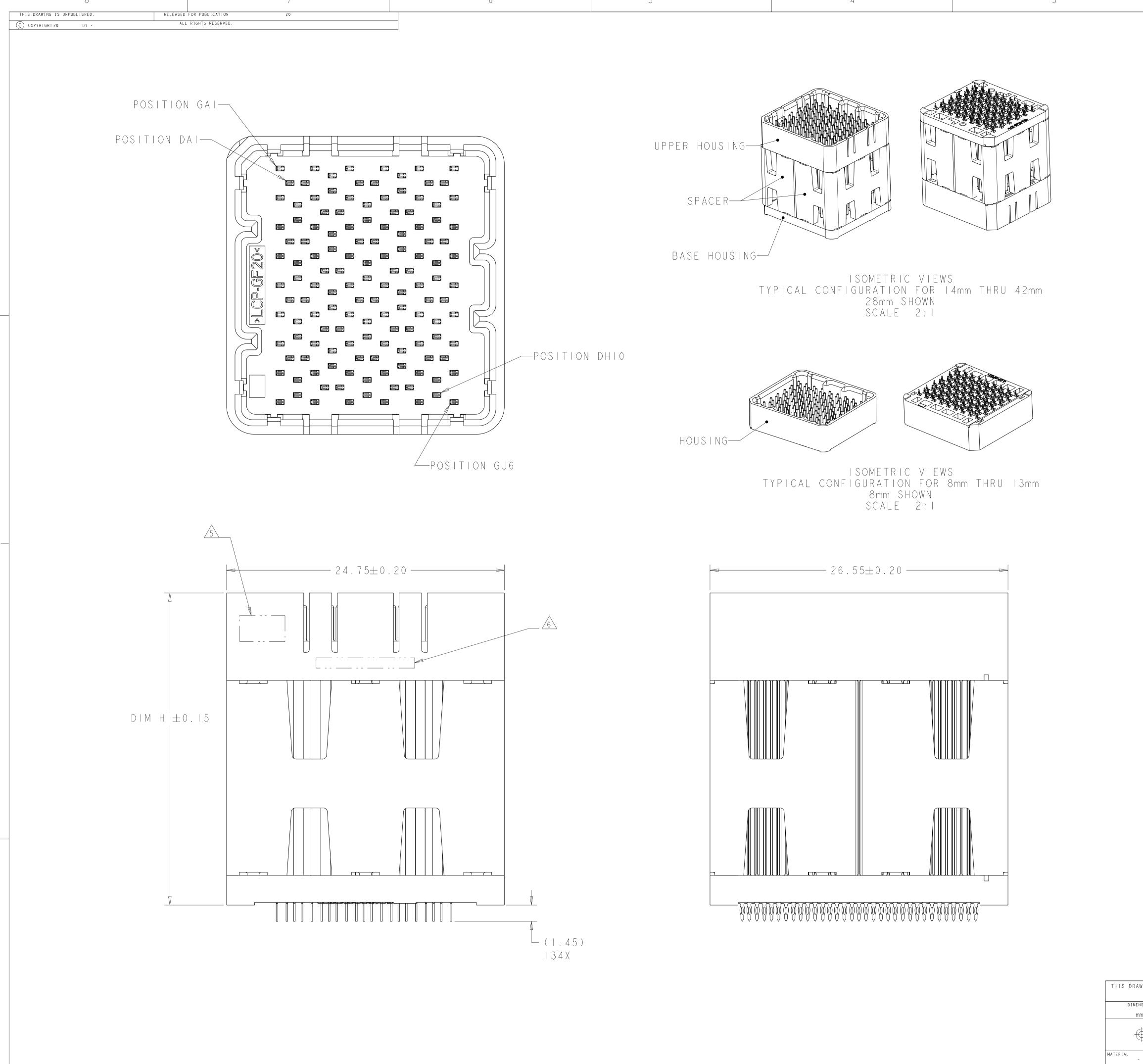
Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China









COC DIST REVISIONS

P LTR DESCRIPTION DATE DWN APVD

A REVISED PER ECO-13-006286 22APR2013 KH MH

MATERIAL:
BASE HOUSING, UPPER HOUSING, ORGANIZER,
AND SPACER: THERMOPLASTIC, FLAMMABILITY
RATING UL94-VO
CONTACT: COPPER ALLOY

2. CONFORMS TO THE REQUIREMENTS OF TE PRODUCT SPECIFICATION, 108-2375; BASED ON TELCORDIA GR-1217-CORE FOR SYSTEM QUALITY LEVEL 111, APPLICATIONS IN CONTROLLED ENVIRONMENTS (CENTRAL OFFICE).
SEE TE PRODUCT SPECIFICATION 108-2375 FOR TEST SEQUENCES.

ROWS GA THRU GJ (SHOWN DARKENED) ARE TYPICALLY USED AS GROUNDS.

SPECIFIED POSITIONAL TOLERANCE DEFINES HOLE TO HOLE LOCATION WITHIN HOLE PATTERN. POSITIONAL TOLERANCE OF HOLE PATTERN TO FIDUCIAL MARKS OR PCB DATUMS SHALL BE DEFINED BY CUSTOMER.

AREA RESERVED FOR TE CONNECTIVITY LOGO.

AREA RESERVED FOR PART NUMBER (X-XXXXXXXXXX)
AND DATE CODE (YYWW).

USE CENTERLINES INDICATED ON PCB HOLE PATTERN TO ESTABLISH ALIGNMENT BETWEEN HEADER AND RECEPTACLE BOARDS.

PLATED THROUGH HOLE REQUIREMENTS:
HOLE SIZE PRIOR TO PLATING = \$\infty 0.420\pmu 0.013
COPPER PLATING THICKNESS = 0.038\pmu 0.013
CALCULATED FINISHED HOLE SIZE = \$\infty 0.344\pmu 0.039
THESE DIMENSIONS APPLY TO THE TOP 1.25mm OF
THE PCB THICKNESS FROM THE CONNECTOR MOUNTING
SIDE.

CONTACT SALES FOR AVAILABILITY OF THIS STACK HEIGHT.

SIZE 1 HOUSING * 40 DIFFERENTIAL PAIRS 134 TOTAL SIGNAL CONTACTS

* SIZE 2 AND SIZE 3 ARE ALSO AVAILABLE

THIS DRAWING IS A (CONTROLLED DOCUMENT.	D.RINGLER CHK 14FEB2009 D.TROUT	
DIMENSIONS:	TOLERANCES UNLESS OTHERWISE SPECIFIED:	APVD 14FEB2009	NAME
mm		J.FEDDER	HEADER ASSEMBLY
	0 PLC ±- 1 PLC ±-	PRODUCT SPEC	40/134 SIGNAL
	2 PLC ±0.13 3 PLC ±0.013	108-2375 APPLICATION SPEC	STRADA MESA MEZZANINE CONNECTOR
+ 7	4 PLC ±- ANGLES ±1		SIZE CAGE CODE DRAWING NO RESTRICTED TO
MATERIAL -	FINISH -	WEIGHT _	A = A = A = A = A = A = A = A = A = A =
- /	-	Customer Drawing	SCALE 6: SHEET OF 4 REV A



