imall

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832 Email & Skype: info@chipsmall.com Web: www.chipsmall.com Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China





Ready...

- For embedded software tools designed EXCLUSIVELY for PIC[®] MCUs
- For a complete, low-cost, powerful C solution
- For ready-to-run example programs and tested peripheral drivers to start any project

Set...

- Using a C Compiler with IDE and C Aware Real-time Debugger
- Using In-Circuit Debugger and Programmers
- Using Prototyping Boards or complete Development Kits





www.ccsinfo.com sales@ccsinfo.com Phone: 262.522.6500 Sales x35 Tech Support x32

PIC® MCU and dsPIC®DSC are registered trademarks of Microchip Technology, Inc.

CCS C Compiler

Features

The compiler is comprised with Standard C operators and built-in libraries that are specific to PIC [®] MCU registers, and access to hardware features from C.

PIC10 / PIC12 / PIC14 / PIC16 / PIC18

1, 8, 16, 32-bit integer types & 32-bit floating point

Bit Arrays and Fixed Point Decimals

#BIT and #BYTE will allow C variables to be placed at absolute addresses to map registers to C variables

Standard one-bit type (Short Int) permits the compiler to generate very efficient Bit-oriented code Constants (including strings and arrays) are

saved in program memory

Flexible Handling of Constant Data

Variable length Constant Strings

AddressMod capability to create user defined address spaces in memory device

Advanced Features in PIC24 & dsPIC[®] DSCs

Also 48 & 64-bit floating point make calculations requiring greater precision or broader range easier

#BIT, #BYTE and #WORD will allow C variables to be placed at absolute addresses to map registers

Constants in ROM

Enhanced oscillator control to choose from a multitude of clock sources, PLL and power saving options

Function recursion allows for interactive processing algorithms

Processor & Peripheral Controls

The CCS C Compiler for PIC10, PIC12, PIC14, PIC16, PIC18 and PIC24 microcontrollers has over 180 Built-in-Functions to access PIC® MCU hardware is easy and produces efficient and highly optimized code.

Functions such as timers, A/D, EEPROM, SSP, PSP, USB, I2C and more:

- Built-in libraries that work with all chips for RS-232 serial I/O, I²C, discrete I/O and precision delays
- Serial I/O functions allow standard functions such as GETC() and PRINTF() to be used for RS-232 like I/O
- Formatted printf allows for easy formatting and display in HEX or decimal
- Multiple I²C and RS232 ports may be easily defined
- #use rs232() offers options to specify a maximum wait time for getc
- Hardware transceiver used when possible, but for all other occasions
 the compiler generates a software serial transceiver
- Microcontroller clock speed may be specified in a PRAGMA to permit built-in functions to delay for a given number of microseconds or milliseconds
- Functions such as INPUT() and OUTPUT_HIGH() properly maintain the tri-state registers
- Compiler directives determine if tri-state registers are refreshed on every I/O or if the I/O is as fast as possible
- #USE SPI ()
- Simple functions like READ_ADC() to read a value from A/D converter
- Source code drivers included for LCD modules, keypads, 24xx and 94xx serial EEPROM, X10, DS1302 and NJU6355 real time clocks, Dallas touch memory devices, DS2223 and PCF8570, LTC1298 and PCF8591 A/D converters, temperature sensors, digital pots, I/O expander and much more

Advanced Functions

The compiler can handle inline or separate functions, as well as parameter passing in re-usable registers.

Transparent to the user, the compiler handles calls across pages automatically and analyzes program structure and call tree processes to optimize RAM and ROM Usage.

Additional features include:

- Efficient function implementation allow call trees deeper than the hardware stack
- Automatic linking handles multiple code pages
- Assembly code may be inserted anywhere in the source and may reference C variables
- Function Overloading allows for several functions with the same name, but differences in number and type of parameters
- Default Parameters can be used in a function if arguments are not used in a call
- Interrupt functions supported on PCM/PCH. The compiler generates all startup and clean up code as well as identifying the correct function to be called
- Reference parameters may be used to improve code readability and inline function efficiency
- Generation Of Multiple HEX Files For Chips With External Memory
- Variable Number Of Parameters in a function
- Relocatable Objects / Multiple Compilation Unit (IDE Only)
- Automatic #fuses Configuration

Complete Examp	le Programs			
LCD	Frequency counter	Fixed Point	DTMF Tones	Boot Loader
A/D	7 Seg LED	TCP/IP	CRC Calculator	CAN Bus
PWM	Data Logger	Floating Point	CCP	I/O for 8-in Parts
Comparator	Pattern Generator	ICD Debugging	Watchdog Timer	Sleep
PSP	Stepper Motors	Advanced Macros	Analog Comparator	Timers
Serial Interrupts	Tone Generation	Memory Management	Optical Encoder	
Magnetic Card Reader	Temperature Sensor	12C	USB	

Included C Driver:

Se	rial EEPROM/Flash	A/I	D & D/A Converters	Re	eal-Time Clock	LC	D	E	cpanded Input/Output	Ot	her
	2041		AD7705AD7715		DS1302		GLCD		74165		Digital Compass
	24xx		ADS8320		NJU6355		KS0108		74595		Keypad
	25xx		LTC1298		DS1305		LCD		MAX7300		Mag Card REader
	93xx		MAX517		ISL1209		LCD420		SC28L19x		PLL Inteface
	AT2421		MCP4921				SED1335				Dallas One Wire
	AT25256		MCP3204	So	ounds		HDM64GS12	Se	erial RAM		IR Decoder
	AT29C1024		MCP3208		WTS701				68HC68R1		Line Tracker
	AT45DB021		TLC545N		TONES	Те	mperature		68HC68R2		Servo Control
	CE51x				ISD4003		DS1621		M68AF031		X10
	CE62x	Di	gital Pots				DS162M		PCF8570		Cyclic Redundancy Code
	CE67x		AD8400	R	FID		DS1631		D41256		RS485
	9512		DS1868		EM4095		DS1624		MT4264		N9085UD
	MMC/SD		MCP41010		EM4402		LM75CIM3				PNI11096
	DS2?? (1-wire EEPROM)				EM4150			N	etworking/Internet		LMX2326
		US	B			CA	N Functionality		TCP		
Ro	botics		USBN960x	A	ccelerometer		MCP251x		PPP		
	GP2D12		PIC_USB		ADXL210		8xxx8		S7600		
	Line Tracking Sensors		PIC18_USB				18F4580		RTL8019		
									ENC28J60		

Seconds Counter

```
#include <18F4520.h>
#fuses HS, NOWDT, NOPROTECT, NOLVP
#use_delay(clock=20000000)
#use rs232(baud=9600, xmit=PIN_C6, rcv=PIN_C7)
#define INTS_PER_SECOND 76
                                   // (20000000/(4*256*256))
BYTE seconds;
                                    // A running seconds counter
BYTE int_count:
                                   // Number of interrupts left before a // second has elapsed
                                   // This function is called every time
// the RTCC (timer0) overflows (255->0).
// For this program this is
#int_rtcc
void clock_isr() {
      if(--int_count=0) {
                                    // apx 76 times per second.
            ++seconds;
            int_count=INTS_PER_SECOND;
}
void main() {
    BYTE start:
      int_count=INTS_PER_SECOND;
      set_timer0(0);
      setup_coutner(RTCC_INTERNAL, RTCC_DIV_256 | RTCC_*BIT);
      enable_interrupts(INT_RTCC);
enable_interrupts(GLOBAL);
      {
            printf("Press any key to begin.\n\r");
            getc();
            start=seconds;
            printf("Press any key to stop.\n\r");
            getc();
printf("%u seconds.\n\r",seconds-start);
      }while (TRUE);
}
```

Simple A/D

```
#include<16F877a.h>
#fuses HS, NOLVP, NOWDT, PUT
#use delay(clock=2000000)
#use rs232(baud=9600,xmit=PIN_C6,rcv=PIN_C7)
void main() {
     int i, value, min, max;
     printf("Sampling:");
     setup_adc_ports(RA0_ANALOG);
setup_adc(ADC_CLOCK_INTERNAL);
     set_adc_channel(0);
                                       //Takes 30 samples from
     do {
          min=255;
                                       //pin A0 and displays the min and
           max=0;
for(i=0; I <= 30; ++i) {</pre>
                                       //max values for that 100ms peroid
                delay_ms(100);
                value=read_adc();
                if(value < min)
                 min = value:
                if(value > max)
                 max = value;
     printf("\n\rMin:%x MAX: %x", min, max);
} while (TRUE);
```

Specific Features

atoi()

atol()

STANDARD C CHAR

Built-in Functions

uilt-in Functions	
RS-232	I ² C
getc()	i2c_start()
putc()	i2c_stop()
gets()	i2c_read()
puts()	i2c_write()
printf()	i2c_poll()
kbhit() assert()	i2c_isr_state()
fgetc()	i2c_slaveaddr()
fgets()	PARALLEL SLAVE I/O
fprintf()	setup_psp()
fputc()	psp_input_full()
fputs()	psp_output_full()
getch()	psp_overflow()
getchar()	
perror()	
putchar()	abs()
setup_uart()	acos()
set_uart_speed()*	asin()
	atan()
SPI TWO-WIRE I/O	atan2()
setup_spi()	ceil()
setup_spi2()	cos()
spi_read()	exp()
spi_read2()	floor()
spi_write()	labs()
spi_write2()	log()
spi_data_is_in()	log10()
spi_data_in2()	sin()
spi_xfer()	sqrt()
VOLTAGE REF	tan() cosh()
setup_vref()	div()
setup_low_volt_detect()	fabs()
setup_comparator()*	fmod()
,	frexp()
A/D CONVERSION	ldexp()
setup_adc_port()	ldiv()
setup_adc()	modf()
set_adc_channel()	pow()
read_adc_channel()	sinh()
adc_done()	tanh()
setup_adc_port()*	
setup_adc()*	STANDARD C MEMOR
set_adc_channel()*	memset()
read_adc()*	memcpy()
adc_done()*	calloc()
CYCLILC REDUNDANCY CHECK	free() longjmp()
crc_calc()*	malloc()
crc_init()*	memcmp()
crc_psuedo_code()*	memmove()
help_crc()*	offsetof()
setup_crc()*	offsetofbit()
,	realloc()
STANDARD C SPECIAL	setjmp()
bsearch()	
rand()	MOTOR PWM
srand()	get_motor_pwm_event()*
qsort()	set_motor_pwm_event()*
	setup_motor_pwm()*
	, \/

()
atan()
atan2()
ceil()
cos()
exp()
floor()
labs()
log()
log10()
sin()
sqrt()
tan()
cosh()
div()
fabs()
fmod()
frexp()
ldexp()
ldiv()
modf()
pow()
sinh()
tanh()
STANDARD C MEN
memset()
memcpy()

MORY

DTOR PWM

_motor_pwm_event()* _motor_pwm_event()* up_motor_pwm()* setup_motor_pwm_duty()* setup_motor_unit()*

DISCRETE I/O
output_low()
output_high()
output_float()
output_bit()
input()
output_x()
input_x()
port_b_pullups()
set_trix_x()
get_tris_s()
output_drive()
input_state()
port_a_pullups()

DELAYS

delay_cycles() delay_us() delay_ms()

CAPTURE/COMPARE PWM setup_ccp_x() set_pwmX_duty() set_power_pwm_override() set_power_pwmx_duty() setup_power_pwm() setup_power_pwm_pins() setup_capture()* get capture()* set_compare_time()*

PROCESSOR CONTROLS

sleep() reset cpu() restart cause() disable interrupts() enable interrupts() ext int edge() read bank() interrupt active() getenv() setup oscillator() clear interrupt() goto address() jump to isr() lavel_address()

REAL TIME CLOCK

rtc_alarm_read()* rtc_alarm_write()* rtc_read()* rtc_write()* setup_rtc()* setup_rtc _alarm()*

QUADRATURE ENCODER INTERFACE

qui_get_count()*
qui_set_count()*
qui_status()*
setup_qui()*

ANALOG COMPARE

setup_comparator()

LCD
lcd_load()
lcd_symbol()
setup_lcd()
TIMERS
setup_timer x()
set_timer_x()
get_timer_x()
setup_counters()
setup_wdt()
restart_wdt()
set_timer_xy()*
get_timer_xy()*
BIT MANIPULATION
shift_right()
shift_left()
rotate_right()
rotate_left()
bit_clear()
bit_set()
bit_test()
swap()
make8()
make16()
make32()
_mul()
RTOS
rtos_await()
rtos_enable()
rtos_msg_poll()
rtos_msg_read()
rtos_msg_send()
rtos_overrun()
rtos_run()
rtos_signal()
rtos_stats()
rtos_terminate()
rtos_wait() rtos_yield()
(RTOS only in PCW
and PCWH packages)
(office packageo)
INTERNAL EEPROM
read_eeprom()
write_eeprom()
read_program_eeprom()
write_program_eeprom()

read_calibration()

erase_program_eeprom()

read_external_memory()

read_program_memory()

setup_external_memory()

write_external_memory()

write_program_memory() erase_program_memory()*

read_rom_memory()*

read_configuration_memory()*

write_configuration_memory()

atoi32() atof() tolower() toupper() isalnum() isalpha() ismoung() isdigit() islower() isspace() isupper() isxdigit() iscntrl() isgraph() isprint() ispunct() itoa() strlen() strncpy() strcmp() stricmp() strncmp() strncmp() strcat() strstr() strchr() strrchr() strtok() strspn() strpbrk() strlwr() sprintf() strcoll() strncat() strtod() strtol() strtoul() strxfrm()

PARALLEL MASTER PORT

pmp overflow input() pmp overflow output() pmp_address() pmp_input_full() pmp_output_full() pmp_overflow() pmp_read() pmp_write() psp_input_full() psp_overflow() psp_read() psp_write() setup_pmp() setup_psp()

dma_start()* dma status()* setup dma()*

DIRECT MEMORY ACCESS

Preprocessors

STANDARD C	FUNCTION QUALIFIERS	RTOS	PRE-DEFINED IDENTIFIERS	MEMORY CONTROL	COMPILER CONTROL
#define	#inline	#use rtos	_date_	#asm	#case
#else	#int_default	#task	_device_	#bit	#opt
#elif	#int_global	(RTOS only in PCW	_file_	#byte	#priority
#endif	#int_xxx	and PCWH packages)	_line_	#endasm	#ignore_warnings
#error	#separate		_pcb_	#fill_rom	#export
#if		BUILT-IN LIBRARIES	_pcm_	#locate	#import
#ifdef	DEVICE SPECIFICATION	#use delay	_pch_	#reserve	#module
#include	#device chip	#use fast_io	_time_	#rom	
#pragma	#fuses	#use fixed_io	_filename_	#zero_ram	
#undef	#id	#use i2c		#org	
#ifndef	#id checksum	#use rs232	LINKER	#type	
#list	#id number	#use standard_io	#import	#word	
#nolist	#serialize	#use spi	#export		
	#hexcomment		#build		

Standard C Syntax

Example C/ASM Listing

done=H 09C: BCF 3B, 1 while 09D: BTFSC 3B, 1 09E: GOTO 0BC 09F: BTFSS 06, 2 0A0: GOTO 0BC	(!done&input(PIN_B2)) {	<pre>• if, else, while, do, switch, case, for, return, goto, break, continue ! ~ ++ * = = , & • */% << >> ^ & & ? : • <= < > >= = != • = += -= *= /= %= >>= <<= &= ^=m != • typedef, static, suto, const, enum, struct, union</pre>
0A1: MOVF 3D, W 0A2: MOVWF 3C 0A3: SWAPF 3C, F 0A4: MOVLW F0 0A5: ANDWF 3C, F	level=limit*16;	 Arrays up to 5 subscripts Structures and Unions may be nested Custom bit fields (1-8 bits) within structures ENUMerated types CONstant variables, arrays, structures, and strings Full function parameter support (any number and kind)
0A6: MOVF 01, W 0A7: MOVWF 20 0A8: MOVLW 48 0A9: SUBWF 20, W 0AA: BTFSS 03, 0 0AB: GOTO OAE	if(get_rtcc()>71)	• C++ reference parameters and comments allowed
0AC: BSF 06, 1	output_high(PIN_B1);	
0AC: BSF 00, 1 0AD: GOTO 0AF	else	Standard C Syntax
0AE: BCF 06, 1	<pre>output_low(PIN_B1);</pre>	 Supports user defined data storage locations C data types may reside in any type of storage
0AF: INCF 3D, F 0B0: MOVLW 24 0B1: SUBWF 3D, W 0B2: BTFSC 03,2	if(++limit==0x24)	 User-defined access routines Implements a virtual memory scheme Located C data in program memory Targets with external memory can use the external bus for data
0B3: CLRF 3D	limit=0;	
0B4: BCF 03, 0 0B5: RLF 2D, F 0B6: BTFSC 03, 0 0B7: GOTO 0BA 0B8: BCF 06, 3 0B9: GOTO 0BB	<pre>output_bit(PIN_B3, shift_left(&data,1,0));</pre>	

CCS C Windows IDE & C Aware Real-time Debugger



Project Navigation

File bar shows all project related files and can quickly open or compile a file.

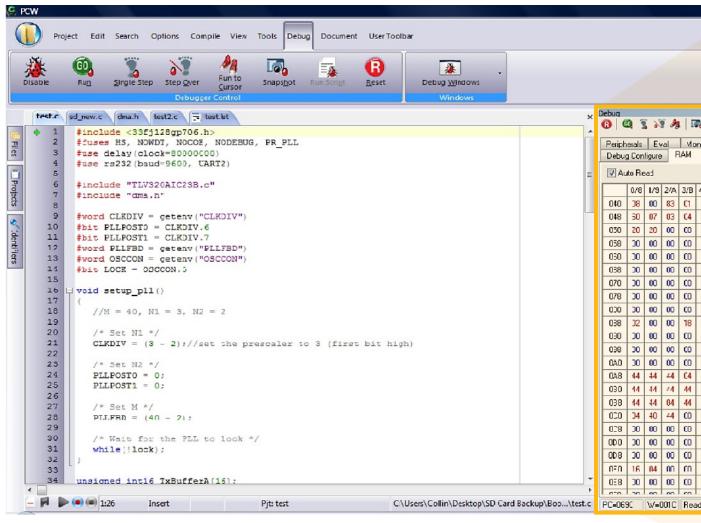
Identifier bar shows all project functions and identifiers.



Wizards

Simplifies configuration of drivers and peripherals to start projects quickly. Forms based on interactive questions to aid in set-up of options, such as calculating and showing the timer options based on your clocks.

Included in PCW: CAN Bus, USB, RS-485, and many more







Device Selector Edit/Add to Device Database Generate C Constant Declarations from hex/binary

Special Function Register Reference Serial Port Monitor File compare for list or source files



Editor Features

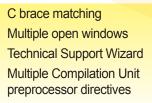
RTOS--integrated for maximum efficiency and multi-tasking allowed with deterministic scheduling Automated C indenting Context Sensitive Help Color Syntax Highlighting IDE compiler capabilities include many utilities to aid in program design and editing. The C Aware Real-time Debugger allows for high-level debugging in a C. It is included in all IDE compilers and can be used with the CCS ICD and Mach X, and Microchip ICD2 and RealICE.



Special Viewers

Include quick and easy access to data sheets, valid fuses, interrupts for devices, hex file disassembler, .COD file interpreter, and an advanced source/list file compare.

3 22 09 50 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 1 44 10 01 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00							o x	
Image: Stand Row Data EE Breaks SFR Registers C 5/0 6/2 7/f Seresh 0 00 00 00 00 Seresh 0 00 00 00 Seresh Seresh Seresh 0 00 00 00 Seresh Seresh Seresh Seresh 1 44 10 01 00 Seresh Seresh Seresh 1 44 44 44 Seresh Seresh Seresh Seresh 1 44 44 44 Seresh Seresh Seresh 1 1 1 <th1< th=""> 1<th></th><th></th><th></th><th></th><th></th><th></th><th>2</th><th></th></th1<>							2	
Image: Stand Row Data EE Breaks SFR Registers C 5/0 6/2 7/f Seresh 0 00 00 00 00 Seresh 0 00 00 00 Seresh Seresh Seresh 0 00 00 00 Seresh Seresh Seresh Seresh 1 44 10 01 00 Seresh Seresh Seresh 1 44 44 44 Seresh Seresh Seresh Seresh 1 44 44 44 Seresh Seresh Seresh 1 1 1 <th1< th=""> 1<th></th><th></th><th></th><th></th><th></th><th></th><th></th><th>1</th></th1<>								1
Image: Stand Row Data EE Breaks SFR Registers C 5/0 6/2 7/f Seresh 0 00 00 00 00 Seresh 0 00 00 00 Seresh Seresh Seresh 0 00 00 00 Seresh Seresh Seresh Seresh 1 44 10 01 00 Seresh Seresh Seresh 1 44 44 44 Seresh Seresh Seresh Seresh 1 44 44 44 Seresh Seresh Seresh 1 1 1 <th1< th=""> 1<td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></th1<>								
Image: Stand Row Data EE Breaks SFR Registers C 5/0 6/2 7/f Seresh 0 00 00 00 00 Seresh 0 00 00 00 Seresh Seresh Seresh 0 00 00 00 Seresh Seresh Seresh Seresh 1 44 10 01 00 Seresh Seresh Seresh 1 44 44 44 Seresh Seresh Seresh Seresh 1 44 44 44 Seresh Seresh Seresh 1 1 1 <th1< th=""> 1<td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></th1<>								
Image: Stand Row Data EE Breaks SFR Registers C 5/0 6/2 7/f Seresh 0 00 00 00 00 Seresh 0 00 00 00 Seresh Seresh Seresh 0 00 00 00 Seresh Seresh Seresh Seresh 1 44 10 01 00 Seresh Seresh Seresh 1 44 44 44 Seresh Seresh Seresh Seresh 1 44 44 44 Seresh Seresh Seresh 1 1 1 <th1< th=""> 1<td></td><td></td><td></td><td></td><td></td><td></td><td>×</td><td>1</td></th1<>							×	1
ROM Data EE Breaks Stack Wateres C 5/0 6/E 7/F Stack Wateresh C 5/0 6/E 7/F Stack Wateresh 0 00 00 00 00 00 00 0 00 00 00 00 00 00 0 00 00 00 00 00 00 0 00 00 00 00 00 00 0 00 00 00 00 00 00 0 00 00 00 00 00 00 0 00 <th< td=""><td>1</td><td>2</td><td></td><td></td><td></td><td></td><td>-</td><td></td></th<>	1	2					-	
ROM Data EE Breaks Stack Wateres C 5/0 6/E 7/F Stack Wateresh C 5/0 6/E 7/F Stack Wateresh 0 00 00 00 00 00 00 0 00 00 00 00 00 00 0 00 00 00 00 00 00 0 00 00 00 00 00 00 0 00 00 00 00 00 00 0 00 00 00 00 00 00 0 00 <th< td=""><td>tor</td><td></td><td>a a k I</td><td></td><td>PTOS Tecko</td><td>CED</td><td>Registern</td><td></td></th<>	tor		a a k I		PTOS Tecko	CED	Registern	
C 5/C 5/F 7/F 0 00 00 00 0 22 09 50 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 1 44 44 44 4 44 44 44 4 44 44 44 4 44 44 44 0 00 00 00 0 00 <	_		_					
C 5/C 5/F 7/F 0 00 00 00 0 22 09 50 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 1 44 44 44 4 44 44 44 4 44 44 44 4 44 44 44 0 00 00 00 0 00 <	-					×	Betresh	
3 00 00 00 3 22 09 50 5 00 00 00 5 00 00 00 5 00 00 00 6 00 00 00 6 00 00 00 7 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00	10	5/0	6/F	7/5		0	(Honean	
3 22 09 50 3 00 00 00 3 00 00 00 3 00 00 00 3 00 00 00 3 00 00 00 3 00 00 00 3 00 00 00 3 00 00 00 3 00 00 00 3 00 00 00 44 10 01 00 3 00 00 00 44 44 44 44 44 44 44 44 44 44 44 44 44 50 00 00 00 00 50 00 00 00 00 00 50 00 00 00 00 00 50 00 00 00 00 00 50 00 00 00	20		-					
0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 1 44 10 01 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00	50							
0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 1 44 10 01 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 4 44 44 44 4 44 44 44 4 44 44 44 4 44 44 44 1 44 44 44 1 44 44 44 1 44 44 44 1 14 44 44 44 44 1 14 44 44 44 44 44 44 44 44 44 44 44 44 44 44 44 44 44 44	00	00	00	00				
0 00 00 00 0 00 00 00 0 00 00 00 1 44 10 01 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 4 44 44 4 44 44 4 44 44 4 44 44 4 44 44 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00	00	00	00	00				
0 00 00 00 0 00 00 00 1 44 10 01 0 00 00 00 0 40 00 00 0 00 00 00 0 00 00 00 0 00 00 00 1 44 44 44 4 44 44 44 4 44 44 44 4 44 44 44 4 44 44 44 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00	00	00	00	00				
0 00 00 00 1 44 10 01 0 00 00 00 0 40 00 00 0 00 00 00 0 00 00 00 0 00 00 00 1 44 44 44 4 44 44 44 4 44 44 44 4 44 44 44 1 44 44 44 2 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00	00	00	00	00				N.
1 44 10 01 0 00 00 00 0 40 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 1 44 44 44 4 44 44 44 4 44 44 44 4 44 44 44 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00	00	00	00	00				
Image: Normal State	00							DAM
0 40 00	01							KAW
a 40 60	00							The RAM window
4 44 44 44 4 44 44 4 44 44 4 44 44 4 44 44 4 44 44 4 44 44 3 04 44 3 04 44 3 04 44 3 04 44 3 04 44 3 04 44 3 04 44 3 04 44 3 04 44 3 04 44 3 04 44 3 04 44 3 00 00 3 00 00 00 3 00 00 00 4 44 44 44 4 44 44 44 4 44 44 44 5 00 00 00 44 <td< td=""><td>DD DD</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></td<>	DD DD							
4 40 44 44 1 41 47 41 4 44 44 44 0 04 44 44 0 04 44 44 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00	_							
4 44 4/4 4 44 4/4 3 04 4/2 4 4/4 4/4 0 00 00 0 00 00 0 00 00 0 00 00 0 00 00 0 00 00 0 00 00 0 00 00 0 00 00 0 00 00 0 00 00	44 44		-					
4 44 44 44 3 04 44 44 0 00 00 00 0 00 00 00 0 00 00 00 0 00 00 00 1 00 00 00 1 00 00 00 2 00 00 00 3 00 00 00 5 00 0	14							
0 04 44 0 00 00 0 00 00 0 00 00 0 00 00 0 00 00 0 00 00 0 00 00 0 00 00 0 00 00	14							device RAM.
0 00 00 00 00 00 00 00 00 00 00 00 00 0	4) (1		-					
0 00 00 00 00 n n n n n n n n n n n n n	00							
n nn nn nn D 000 00 00 D 000 00 00	00	00	00	00				
0 00 00 00	00	00	00	00				
	n	00	nn	nn				
	00			_			_	
MCU at 23.70 Minz								
	M	cu a	123.1	UMH	z			



Documentation Generator Flow Chart Editor **RTF** Documentation Generator Spellchecker Download Manager

C Aware Real-time Debugger

و و ال	* =
	Eval [Monitor [Log] Configure] RAM Peripherals RUM
Expression	Value 🗠
butter[next_out]	49
nest_in	5
(etatue>>3)%T	1
config[current]	(invert.1.testmode.0.cal.27)
history	(83,85,81,82,85,84)
config[0], invert	1
(sum*100/count],x	Ox5C



Full C expressions can be specified. Arrays and structures are understood and shown in natural form. Variables can be modified and break points can be set in file line.

Data EE Stack Eval Monitor Log Configure ^program starting

Enter TC to use: 5 Check ADC called .DC value = 89 iter algorithm called itered value = 89 Check ADC called DC value = 88 iter algorithm called itered value = 89,



A monitor allows character I/O to and from the target platform. The target platform can printf to this debugger window and getc from it.

	Parale	al SI	ave Port (PSP)
POR	ID 08:	0	0110001 0x31 🏼
POR	IN 09:	0	0000010 0x02
TRI	83 89:	0	0000111 0x07
PSPMODE	89.4	ı	PSP Enabled
IBOU	89.5	0	No Input Overflow
OBF	89.6	0	No Output Overflow
IBF	89.7	0	Buffer not full
PSPIF	C. 7	0	No Interrupt Pendi
PSPIR	8C.7	1	Interrupt Enabled



Special function registers are grouped by a function and each bit is fully interpreted in the debugger window.

Data EE Stack Eval Mo	nitor Log Configure
Break: Expression: II Image: history[last_in]	Clear
00:06.11 history[last_in] = 94 00:10.27 history[last_in] = 91 00:14.43 history[last_in] = 92 00:18.51 history[last_in] = 97 00:22.74 history[last_in] = 94 00:26.90 history[last_in] = 95	



Logging

Configurable to save data each time a specified source line is executed. Set-up profiles can be saved and used with any project.

Debugger data can be printed or saved to disk file.

Products for Production & Development

PRIME8 Production **Programmer**

in 2008

RRODUCTION. In-circuit gang programmer for Microchip Flash devices:

- 8 Selectable Targets
- 2.5V, 3.3V, 5V target vdds available
- 2MB flash that can store up to 4 programs
- SD card reader to load targets in the field
- CCS Programmer Control Software
- LCD screen with user interface for functions

LOAD-n-GO Handheld **Programmer**

Low-cost in-circuit programmer for Microchip Flash devices:

- AA battery operated or 9V DC Adapter
- 2MB flash that can store up to 4 programs

PROOLCTION

- Automatic shut-off to conserve batteries
- 2.5V, 3.3V, 5V target vdds available
- CCS Programmer Control Software
- Free software updates to add devices

ICD-U64 In-Circuit Debugger/ Programmer

- Programs all Microchip Flash devices
- In-circuit programmer uses modular jack
- Powered from USB bus
- Target voltages from 2V to 5V
- Debug capability with CCS PCW, PCWH and PCWHD
- Standalone utility for easy downloading
- · Command-line interface for integration with other software
- Supports automatic serial numbering
- Windows & linux host supported
- **Over 30% FASTER than an ICD-U40!**

USB Master Development Board

Create embedded devices that function as the master of a USB bus and control slave devices.

This development board combines the PIC18F67J10 device with the Vinculum VNC1L to create:

- USB Hub
- Read and Write to FAT formatted Flash drives
- · Communicate with printers and CDC devices
- Utilize HID class devices in embedded applications

Board designed for audio signal processing development. Uses a TI audio codec chip that can acquire data from microphone and drive headphones.

DSP Analog

Board

Development

- Equipped to record, process and playback audio signals up to 24 buts at 44.1 Khz
- SD card reader to store recorded audio or signal data
- LCD display and large number of digital inputs and 2 ADC inputs
- Learn to use Direct Memory Access for real-time digital signal applications

Development Kits Basic Kits

CCS offers a wide variety of all-inclusive Development Kits that bring hardware and software together to provide an innovative package of development tools. The CCS Development Kit allows engineers to design, develop, implement, and test applications directly on PIC® MCUs and dsPIC® DSCs. Novices and experienced developers alike save both time and money with the CCS Development Kits without a need to buy any additional equipment.

	Push				I/O Pins		IDE		
Kit name	Button	3 LEDs	POTS	RS-232	Total	Analog	Compiler	Special Feature	
PIC12F675	\checkmark	\checkmark	2	1	6	4	PCW	Board includes the 14-pin part for ICSP and debugging at the C level	
PIC12F683	\checkmark	\checkmark	2	1	6	4	PCW	Similar to PIC12F675 with twice the RMA and EE- PROM, 3 timers and Capture/Compare/PWM module	
PIC16F877A	\checkmark	\checkmark	1	1	30	7	PCWH	Basic features for quick and easy learning	
PIC16F887	\checkmark	\checkmark	1	1	30	12	PCWH	Enhanced features of 877A family with additional I/O	
PIC18F4520	\checkmark	\checkmark	1	1	30	11	PCWH	Basic features that require more RMA and Data EEPROM space	
PIC18F6722	\checkmark	\checkmark	1	2	48	11	PCW	RS-232 Level Converter connected to the C6/C7 UART and G1/G2 UART	
PIC18F8722	\checkmark	\checkmark	1	2	29	13	PCW	External Flash and RAM	
PIC18F67J10	\checkmark	\checkmark	1	2	48	10	PCWH	Basic features for 3.3V applications	
PIC24F	\checkmark	\checkmark	1	2	48	16	PCDIDE	Runs at 16 MIPS	
PIC24H	\checkmark	\checkmark	1	2	48	18	PCDIDE	Runs at 40 MIPS –Low drive current	
DSP Starter	\checkmark	\checkmark	1	1	10	1	PCDIDE	Real ICE [™] connector and a Header to access the available GPIO, Runs at 30 MIPS	



Internet Connectivity Kits

Embedded Ethernet

On-board ENC28J60 chip and MMC/SD card reader. SPI controlled, 10Mbit/sec, full duplex Ethernet transceiver IC. Drivers and example TCP/IP code included.



3.3V Ethernet Controller

The PIC18F67J60 chip offers more memory at operating voltages of 2.0 to 3.6V and is 10-BaseT (10Mbps) compliant.

The PIC18F67J60 can run at speeds up to 40 Mhz and Ethernet transfers up to 10 Mbit/sec at 3V.

Example programs include a complete web server, e-mail generator and SD Card Read/Write.

Wireless Kits

CCS Wireless-Ember Zigbee™ Edition

Introduction to developing ZigBeeTM applications for the Ember EM260 processor with PIC16 and PIC18 devices. Includes full communication protocols with the Ember-ZigBeeTM stack.

The minuscule EM260 module is a 2.4 Ghz IEEE 802.15.4 compliant transceiver (3.3V operation) with a SPI interface and Insight Port for advanced network debugging with Insight Desktop.

Base Station board combines the EM260 module with PIC18LF4620 device that has 10 I/O Pins (3 may be Analog). The two batteryoperated Sensor boards combine the EM260 module with PIC16LF886 device that has 5 I/O Pins (1 may be Analog).





Embedded Internet

Demonstrates TCP/IP and Internet connectivity. Both a 56k modem and 10MB Ethernet connection provide access to the Internet.

CCS provides a port of Microchip's TCP/IP stack and an API for developing applications.

Examples include a simple web server to allow web clients to view readings from around the world and a SMTP/E-mail client to send E-mails.



RFID

Simple read-only and read/write transponder to demonstrate multiple contactless communication possibilities. A manual with source code examples explains how to use the drivers, enabling you to quickly develop your own RFID applications.

RFID Prototype Board has a short range RFID antenna connected to an external RFID transceiver IC. The RFID prototype board connects to external components using a four-wire RS485 bus. The RS485 connection on the RFID Prototype board is to accomodate multi-drop/ multinode network of RFID units and other RFID related components

Kit also includes: RFID Prototyping Board, RS485-to-RS232 Prototyping Board, Two Read-Only RFID Transponders, and One Read/Write RFID Transponder (Password Protection and can be made Read-Only or Write-Only)



Human Interface Kits

ACE Kit

The ACE Kit is the perfect solution to to an advanced engineer's development needs. This kit provides multiple accessories packaged together to provide diverse programming situations.

This board contains connectors and expansions to debug a variety of situations before the final target platform is designed.

The ACE Kit utilizes the CCS Software Prototyping Board - A PIC® MCU designer's best friend.

Kit also includes: Software Prototyping Board, LCD/ Keypad Set, Experimenter's Set, USB Add-On (does NOT include PIC18F4550 chip), PIC16F877A Reprogrammable Chip, 30 I/O Pins (7 Can Be Analog), and 2-digit 7-segment LED



Robotics

An introduction into the world of robots for both beginners and advanced robot enthusiasts. Included devices allow the robot to see, sense magnetic fields, speak, accept external commands, and move.. fit for robot sumo competition!



Each device has its own chapter in the included exercise book, describing how to operate and use its drivers. Bonus chapters on Real Time Operating Systems (RTOS) and advanced project ideas are included.

The electronic compass and text-to-speech converter are unique to the CCS Robotics Kit. The compass allows the robot to move freely and still know its heading and location. The text-to-speech converter provides a more personal way to interact with people.

Kit also includes: Controller Board and TV Remote



Bus Introductory Kits

USB

Has a PIC18F4550, an external USB controller, and a function generator. Applications for a simple human interface

applications and a high speed example emulating an oscilloscope. PC software (with source) is included to communicate with the USB board. Board uses the PIC18F4550, Microchip's PIC® MCU with full speed USB peripheral.

Example programs with the CCS C compiler and C source code are provided: How to configure the board to act as a HID device. How to configure the board to act as a USB device that accepts and receives bulk mode transfers.

USB examples are also compatible with the Microchip PIC16C7x5 USB peripheral, which CCS also provides C source code with the CCS C compiler.



CAN Bus

Controller Area Network (CAN) is a serial bus system for a network of controllers. There are four nodes that are able to transmit and receive messages from the network.

CAN Bus offers a secure communication channel to exchange up to 8 bytes between several network nodes.

Node 1: The PIC18F4580 includes an integrated CAN peripheral. Node 2: A PIC16F876A is connected to a MCP2515 (external CAN peripheral with SPI interface) Node 3 & 4: MCP25050s (stand-alone CAN expanders) pre-pro-

grammed by CCS to respond to specific CAN IDs.

Nodes 1-3 have a potentiometer, three LEDs and three pushbuttons connections. Node 4 is connected to a 7-segment LED.



Embedded Serial Busses

An introduction to SPI and I²C serial busses, with no wiring needed. The board has two nodes and shares common components between different devices, but maintain their own potentiometer, LEDs, pushbutton and RS-232 port.

Node 1: A PIC16F877A chip is connected to a 74HC165 chip expanding to bank of 8 DIP switch inputs, 74HC595 chips expands output to display information on three 7-Segment LEDs, and a serial real-time clock. U A PIC16F876A chip, which shares an I2C temperature sensor and a serial EEPROM with the first node. This allows for the investigation into data collision, while accessing shared components. Both nodes have.

Programmers/ Debuggers

CCS has a complete line of Programmers and Debuggers for all Microchip PIC10, PIC12, PIC14, PIC16, PIC18, PIC24 and dsPIC[®] devices. The entire line supports in-circuit debugging at the C level with any IDE compiler, and in-circuit programming with the IDE or the stand-alone CCS Programmer Control Software for all Flash-supported devices.

The CCS ICD units work with the CCS C Aware Real-Time Debugger for detailed debugging information at the C level. ICD-U40 is powered by the USB bus and can be modified to power the target board at 5V. ICD-S40 communicates via a RS232 serial bus and is powered by the target board.

The Mach X Programmer is a full-featured device programmer with a standard ICD connector and a 40-pin ZIF socket with advanced ICSP signal routing logic to accommodate various pin-outs (8-40 pins). All Flash memory, One-Time Programmable (OPT) and MCPxxxx(CAN Bus chips) can be programmed at the user selectable voltage range of 2V to 5V.

	ICD-U40	ICD-S40	МАСН Х	LOAD-n-GO	Prime 8
In-Circuit Programming	\checkmark	\checkmark	\checkmark	\checkmark	✓
In-Circuit Debugging	\checkmark	\checkmark	~	\checkmark	\checkmark
ZIF Socket			~		_
PC Interface	USB	RS-232	USB	USB	USB
Power	USB	Target	USB	USB, 4 AA Batteries and AC Adapter	USB and AC Adapter
Target Boards	1	1	1	1	8

