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**SCOPE: DUAL-POWER MOSFET DRIVER**

<u>Device Type</u>	<u>Generic Number</u>	<u>SMD Number</u>
01	ICL7667M(x)/883B	5962-87660

**Case Outline(s).** The case outlines shall be designated in Mil-Std-1835 and as follows:

<u>Outline Letter</u>		<u>Mil-Std-1835</u>	<u>Case Outline</u>	<u>Package Code</u>
<u>SMD</u>	<u>MAXIM</u>			
G	TV	MACY1-X8	8 LEAD CAN	TO99
P	JA	GDIP1-T8 or CDIP2-T8	8 LEAD CERDIP	J8

**Absolute Maximum Ratings**

V <sub>DD</sub> to GND .....	18V
Input Voltage .....	(V <sub>DD</sub> +0.3V) to (GND -0.3V)
Lead Temperature (soldering, 10 seconds) .....	+300°C
Storage Temperature .....	-65°C to +150°C
Continuous Power Dissipation .....	T <sub>A</sub> =+70°C
8 lead CERDIP(derate 8.0mW/°C above +70°C) .....	640mW
8 lead CAN (derate 6.7mW/°C above +70°C) .....	533mW
Junction Temperature T <sub>J</sub> .....	+150°C
Thermal Resistance, Junction to Case, Θ <sub>JC</sub> :	
Case Outline 8 lead CERDIP.....	55°C/W
Case Outline 8 lead CAN.....	45°C/W
Thermal Resistance, Junction to Ambient, Θ <sub>JA</sub> :	
Case Outline 8 lead CERDIP.....	125°C/W
Case Outline 8 lead CAN .....	150°C/W

**Recommended Operating Conditions.**

Ambient Operating Range (T <sub>A</sub> ) .....	-55°C to +125°C
V <sub>DD</sub> .....	+4.5Vdc to 15.5Vdc

Stresses beyond those listed under “Absolute Maximum Ratings” may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

**TABLE 1. ELECTRICAL TESTS**

TEST	Symbol	CONDITIONS		Group A Subgroup	Device type	Limits Min	Limits Max	Units
		-55 °C <=T <sub>A</sub> <= +125°C V <sub>DD</sub> =+15V Unless otherwise specified						
<b>SWITCH</b>								
Logic 1 Input Voltage	V <sub>IH</sub>	V <sub>DD</sub> =4.5V	1,2,3	All	2.0			V
		V <sub>DD</sub> =15V			2.0			
Logic 0 Input Voltage	V <sub>IL</sub>	V <sub>DD</sub> =4.5V	1 2,3	All		0.8 0.5		V
		V <sub>DD</sub> =15V	1,2,3			0.8		
Input Current	I <sub>IN</sub>	V <sub>DD</sub> =15V, V <sub>IN</sub> =0V and 15V		1,2,3	All	-0.1	0.1	μA
Output Voltage High	V <sub>OH</sub>	V <sub>DD</sub> =4.5V and 15V		1 2,3	All	V <sub>CC</sub> -0.05 V <sub>CC</sub> -0.1		V
Output Voltage Low	V <sub>OL</sub>	V <sub>DD</sub> =4.5V and 15V		1 2,3	All		.05 0.1	V
Output Resistance	R <sub>OUT</sub>	V <sub>IN</sub> =V <sub>IL</sub> , I <sub>OUT</sub> =+10mA, V <sub>CC</sub> =15V		1 2,3	All		10 12	Ω
Output Resistance	R <sub>OUT</sub>	V <sub>IN</sub> =V <sub>IH</sub> , I <sub>OUT</sub> =-10mA, V <sub>CC</sub> =15V		1 2,3	All		12 13	Ω
Power-Supply Current	I <sub>CC</sub>	V <sub>IN</sub> =0V, both inputs		1,2,3	All		0.4	mA
		V <sub>IN</sub> =3V, both inputs		1 2,3	All		7 8	
Delay Time	t <sub>D1</sub>	Figure 1		9 10,11	All		30 40	ns
	t <sub>D2</sub>	Figure 1		9 10,11	All		50 60	
Rise Time	t <sub>R</sub>	Figure 1		9 10,11	All		30 40	ns
Fall Time	t <sub>F</sub>	Figure 1		9 10,11	All		30 40	

FIGURE 1: Timing Diagram/Test Circuit. See Commercial Datasheet.

ORDERING	INFORMATION:		Terminal Number	ICL7667	
	Maxim #	Pkg.		J8	TO99
5962-8766001PA	ICL7667MJA/883B	J8	1	NC	OUTA
5962-8766001GC	ICL7667MTV/883B	TO99	2	INA	NC
			3	V-	INA
			4	INB	V-
			5	OUTB	INB
			6	V <sub>DD</sub>	NC
			7	OUTA	OUTB
			8	NC	V <sub>DD</sub>

## QUALITY ASSURANCE

Sampling and inspection procedures shall be in accordance with MIL-Prf-38535, Appendix A as specified in Mil-Std-883.

Screening shall be in accordance with Method 5004 of Mil-Std-883. Burn-in test Method 1015:

1. Test Condition, A, B, C, or D.
2. TA = +125°C minimum.
3. Interim and final electrical test requirements shall be specified in Table 2.

Quality conformance inspection shall be in accordance with Method 5005 of Mil-Std-883, including Groups A, B, C, and D inspection.

Group A inspection:

1. Tests as specified in Table 2.
2. Selected subgroups in Table 1, Method 5005 of Mil-Std-883 shall be omitted.

Group C and D inspections:

- a. End-point electrical parameters shall be specified in Table 1.
- b. Steady-state life test, Method 1005 of Mil-Std-883:
  1. Test condition A, B, C, D.
  2. TA = +125°C, minimum.
  3. Test duration, 1000 hours, except as permitted by Method 1005 of Mil-Std-883.

**TABLE 2. ELECTRICAL TEST REQUIREMENTS**

Mil-Std-883 Test Requirements	Subgroups per Method 5005, Table 1
Interim Electric Parameters Method 5004	1
Final Electrical Parameters Method 5005	1*, 2, 3, 9
Group A Test Requirements Method 5005	1, 2, 3, 9, 10, 11
Group C and D End-Point Electrical Parameters Method 5005	1

\* PDA applies to Subgroup 1 only.