



Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of “Quality Parts,Customers Priority,Honest Operation,and Considerate Service”,our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



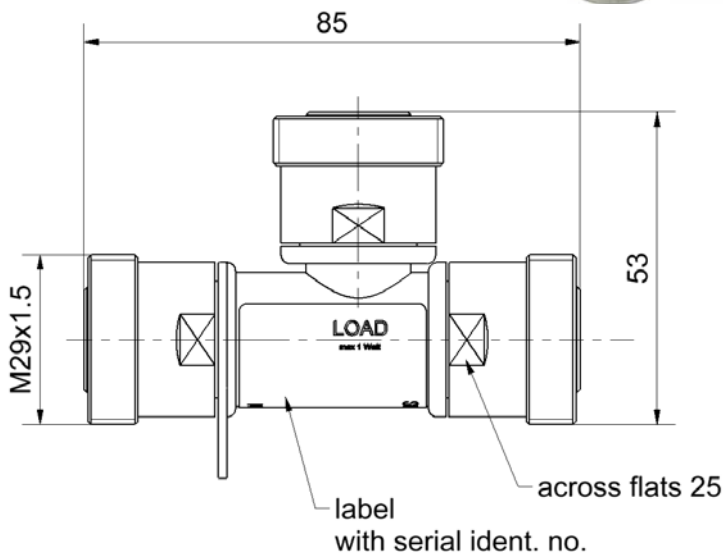
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All dimensions are in mm; tolerances according to ISO 2768 m-H

Interface

According to

IEC 61169-4, EN 122190, DIN 47223

Documents

This kit is delivered with

- **Standard Definitions Card**
Printed Standard Definitions that can be used on nearly all Vector Network Analyzers
- **Test Results Documentation**
- **Lanyard**
- **Hard Shell Case**

Material and plating

Connector parts

Center conductor
Outer conductor
Body
Dielectric
Substrate

Material

CuBe
Brass
Brass
PP
Al₂O₃

Plating

Gold, min. 1.27 µm, over nickel
Flash white bronze over silver(e.g. Optargen®)
powder coated

Electrical data

Frequency range DC to 6 GHz

Open

Return loss ≤ 0.15 dB, DC to 6 GHz
Error from nominal phase¹ $\leq 3.0^\circ$, DC to 6 GHz

Short

Return loss ≤ 0.15 dB, DC to 6 GHz
Error from nominal phase² $\leq 3.0^\circ$, DC to 6 GHz

Load

Return loss ≥ 40 dB, DC to 2.5 GHz
 ≥ 38 dB, 2.5 GHz to 6 GHz
DC-Resistance $50 \Omega \pm 0.5 \Omega$
Power handling ≤ 1.0 W

¹ The nominal phase is defined by the Offset Delay, the Offset Loss and the Fringing Capacitances

² The nominal phase is defined by the Offset Delay, the Offset Loss and the Short Inductance

Mechanical data

Mating cycles ≥ 500
Maximum torque 30 Nm
Recommended torque 2.26 Nm
Gauge 1.77 mm to 2.07 mm

General standard definitions

For proper operation the vector network analyzer (VNA) needs a model describing the electrical behaviour of this calibration standard. The different models, units, and terms used will depend on the VNA type and they will have to be entered into the VNA. All values are based on typical geometry and plating.

Open

Offset Z_o / Impedance / Z_o 50 Ω
Offset Delay 87.394 ps
Length (electrical) / Offset Length 26.20 mm
Offset Loss 0.50 G Ω /s
Loss 0.0076 dB/ $\sqrt{\text{GHz}}$
Fringing Capacitances $C_0 = 177.000 \times 10^{-15}$ F / 177.000 fF
 $C_1 = 7200.00 \times 10^{-27}$ F/Hz / 7.20000 fF /GHz
 $C_2 = -3300.00 \times 10^{-36}$ F/Hz² / -3.30000 fF /GHz²
 $C_3 = 386.000 \times 10^{-45}$ F/Hz³ / 0.38600 fF /GHz³

Short

Offset Z_o / Impedance / Z_o	50 Ω		
Offset Delay	96.734 ps		
Length (electrical) / Offset Length	29.00 mm		
Offset Loss	0.50 G Ω /s		
Loss	0.0084 dB/ $\sqrt{\text{GHz}}$		
Short Inductance	$L_0 = 0.0000 \times 10^{-12}$ H	/	0.0000 pH
	$L_1 = 0.0000 \times 10^{-24}$ H/Hz	/	0.0000 pH/GHz
	$L_2 = 0.0000 \times 10^{-33}$ H/Hz ²	/	0.0000 pH/GHz ²
	$L_3 = 0.0000 \times 10^{-42}$ H/Hz ³	/	0.0000 pH/GHz ³

Load

Offset Z_o / Impedance / Z_o	50 Ω
Offset Delay	0.0000 ps
Length (electrical) / Offset Length	0.000 mm
Offset Loss	0.00 G Ω /s
Loss	0.0000 dB/ $\sqrt{\text{GHz}}$

Environmental data

Operating temperature range ³	0 °C to +50 °C
Storage temperature range	- 55 °C to +90 °C
RoHS	compliant

³ Temperature range over which these specifications are valid.

Declaration of documentation

Standard delivery for this kit includes Test Results. The documentation issued reports which quantities were tested individually, traceable to national / international standards. Model based standard definitions of the calibration standards are reported in Agilent / Keysight, Rohde & Schwarz and Anritsu compatible VNA format.

Inspection interval

Recommendation	12 months
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Packing

Standard	1 pce in bag
Weight	310 g/pce

While the information has been carefully compiled to the best of our knowledge, nothing is intended as representation or warranty on our part and no statement herein shall be construed as recommendation to infringe existing patents. In the effort to improve our products, we reserve the right to make changes judged to be necessary.

Draft	Date	Approved	Date	Rev.	Engineering change number	Name	Date
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