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 $\mathsf{ModSTACK^{TM}}$ 

# 6MS24017P43W39873



### **Preliminary data**

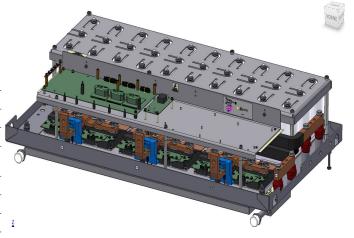
#### **General information**

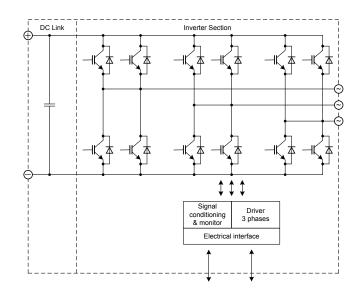
#### IGBT Stack for typical voltages up to 690 $V_{\text{RMS}}$ Rated output current 1100 A<sub>RMS</sub>

- High power converterWind powerMotor drives

- · IHM module with IGBT4
- · AlSiC baseplate

Topology	B6I
Application	Inverter
Load type	Resistive, inductive
Semiconductor (Inverter Section)	6x FF1200R17KP4_B2
DC Link	12 mF
Heatsink	Water cooled
Implemented sensors	Current, voltage, temperature
Driver signals IGBT	Electrical
Sales - name	6MS24017P43W39873
SP - No.	SP001151298





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### **Preliminary data**

#### Absolute maximum rated values

Collector-emitter voltage	IGBT; T <sub>vj</sub> = 25°C	V <sub>CES</sub>	1700	V
Repetitive peak reverse voltage	Diode; T <sub>vj</sub> = 25°C	V <sub>RRM</sub>	1700	V
DC link voltage	No switching; t= 5s, once a day	V <sub>DC</sub>	1450	V
Insulation management	according to installation height of 2000 m	V <sub>line</sub>	690	V <sub>RMS</sub>
Insulation test voltage	according to EN 50178, f = 50 Hz, t = 5 s	V <sub>ISOL</sub>	2.5	kV <sub>RMS</sub>
Continuous current inverter section		I <sub>AC2</sub>	1100	Arms
Junction temperature	under switching conditions	T <sub>vjop</sub>	150	°C
Storage temperature min.		T <sub>stor</sub>	-40	°C
Storage temperature max.		T <sub>stor</sub>	65	°C
Operational ambient temperature min.		T <sub>amb</sub>	-25	°C
Operational ambient temperature max.		T <sub>amb</sub>	55	°C
Inlet temperature coolant min.		T <sub>inlet</sub>	-25	°C
Inlet temperature coolant max.		T <sub>inlet</sub>	65	°C
Auxiliary voltage		V <sub>aux</sub>	30	V
Switching frequency inverter section		f <sub>sw2</sub>	3.5	kHz

#### Notes

Further maximum ratings are specified in the following dedicated sections

### **Characteristic values**

DC Link			min.	typ.	max.	
Rated voltage		V <sub>DC</sub>		1100		V
Over voltage shutdown	within 150 μs			1250		V
Capacitor	1 s, 30 p, rated tol. ±10 %	C <sub>DC</sub>		12		mF
		type		Foil		
Maximum ripple current	per device, T <sub>amb</sub> = 55 °C	I <sub>ripple</sub>			49	A <sub>RMS</sub>
Balance or discharge resistor	per DC link unit	R₀		6		kΩ

#### Notes

Operation above 1100 V subject to reduced operating time according to EN 61071

Inverter Section			min.	typ.	max.	
Rated continuous current	$ \begin{vmatrix} V_{DC} = 1050 \text{ V}, V_{AC} = 690 \text{ V}_{RMS}, \cos(\phi) = 0.9, \\ f_{AC \text{ sine}} = 50 \text{ Hz}, f_{sw} = 2600 \text{ Hz}, T_{inlet} = 40^{\circ}\text{C}, T_{j} \leq 150 ^{\circ}\text{C} \\ \end{vmatrix} $	I <sub>AC</sub>		1000		A <sub>RMS</sub>
Continuous current at low frequency	$ \begin{vmatrix} V_{DC} = 1050 \text{ V}, \ V_{AC} = 690 \ V_{RMS}, \ cos(\phi) = -0.9, \\ f_{AC \text{ sine}} = 12 \ Hz, \ f_{sw} = 2300 \ Hz, \ T_{inlet} = 40 \ ^{\circ}C, \ T_{j} \leq 150 \ ^{\circ}C $	I <sub>AC low</sub>		1100		A <sub>RMS</sub>
Rated continuous current for 150% overload capability	I <sub>AC 150%</sub> = 1100 A <sub>RMS</sub> , t <sub>on over</sub> = 0.01 s, t <sub>recovery</sub> = 135 s	I <sub>AC over1</sub>			1767	A <sub>RMS</sub>
Over current shutdown	within 15 μs	I <sub>AC OC</sub>		2500		A <sub>peak</sub>
Power losses	$ \begin{vmatrix} I_{AC} = 1000 \text{ A, } V_{DC} = 1050 \text{ V, } V_{AC} = 690 \text{ V}_{RMS}, \\ \cos(\phi) = 0.9, f_{AC \text{ sine}} = 50 \text{ Hz, } f_{sw} = 2600 \text{ Hz,} \\ T_{inlet} = 40 \text{ °C, } T_j \leq 150 \text{ °C} $	P <sub>loss</sub>			14500	W

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## Preliminary data

#### **Controller interface**

Driver and interface board	ref. to separate Application Note		DR111			
			min.	typ.	max.	
Auxiliary voltage		V <sub>aux</sub>	18	24	30	V
Auxiliary power requirement	V <sub>aux</sub> = 24 V	Paux		40		W
Digital input level	resistor to GND 1.8 kΩ, capacitor to GND 4 nF,	V <sub>in low</sub>	0		4	V
	logic high = on, min. 15 mA	V <sub>in high</sub>	11		15	V
Digital output level	open collector, logic low = no fault, max. 15 mA	V <sub>out low</sub>	0		1.5	V
		V <sub>out high</sub>		15		V
Analog current sensor output inverter section	load max 1 mA, @ 1100 A <sub>RMS</sub>	VIU ana2 VIV ana2 VIW ana2		5		V
Analog DC link voltage sensor output	load max 1 mA, @ 1100 V	V <sub>DC ana</sub>		7.9		V
Analog temperature sensor output inverter section (NTC)	$@T_{NTC} = 65  ^{\circ}C$ , corresponds to $T_j = 137  ^{\circ}C$ at rated conditions	V <sub>Theta NTC2</sub>		8.5		V
Analog temperature sensor output inverter section (Simulated)	$@T_{NTC} = 68  ^{\circ}C$ , corresponds to $T_j = 137  ^{\circ}C$ at rated conditions	V <sub>Theta sim2</sub>		9.4		V
Over temperature shutdown inverter section	load max 1 mA	V <sub>Error OT2</sub>		9.9		V
	1		I	ı		
Minimum on time (IGBT)		t <sub>on min</sub>	10			μs
Minimum off time (IGBT)		t <sub>off min</sub>	11			μs

System data

System data				min.	typ.	max.	
EMC robustness	according to IEC 61800-3 at named	power	$V_{\text{Burst}}$		2		kV
	interfaces	control	V <sub>Burst</sub>		1		kV
		aux (24V)	V <sub>surge</sub>		1		kV
Storage temperature		·	T <sub>stor</sub>	-40		65	°C
Operational ambient temperature	PCB, DC link capacitor, bus bar, excluding	ng cooling	T <sub>op amb</sub>	-25		55	°C
Cooling air velocity	PCB, DC link capacitor, bus bar, standa	PCB, DC link capacitor, bus bar, standard atmosphere		2			m/s
Humidity	no condensation		Rel. F	0		85	%
Vibration	according to IEC 60721					10	m/s²
Shock	according to IEC 60721					100	m/s²
Protection degree					IP00	•	
Pollution degree					2		
Dimensions	width x depth x height			1090	596	260	mm
Weight						105	kg

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### **Preliminary data**

Heatsink water cooled			min.	typ.	max.	
Water flow	according to coolant specification from Infineon	ΔV/Δt	20			dm³/min
Water pressure					8	bar
Coolant inlet temperature		T <sub>inlet</sub>	-40		45	°C
Thermal resistance heatsink to ambient	per switch	R <sub>th,ha</sub>		0.03		K/W
Cooling channel material			P	Aluminu	n	

#### Notes

Composition of coolant: Water and 52 vol. % Antifrogen N

Overview of optional components	Unit 1 (not installed	Inverter Section	Unit 3 (not installed)
Voltage sensor		×	
Current sensor		×	
Temperature sensor		×	
Temperature simulation		×	
DC link capacitors		×	
Collector-emitter Active Clamping		×	

#### Notes

Setting of Active Clamping TVS-Diodes: V<sub>Z</sub> = 1200V/1600V MA111. Reduce short circuit protection above 1200V DC.

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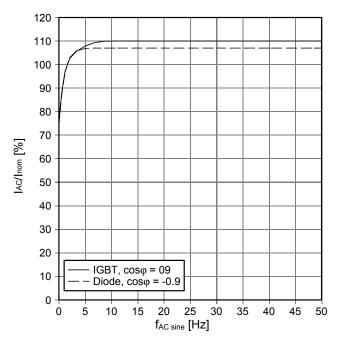
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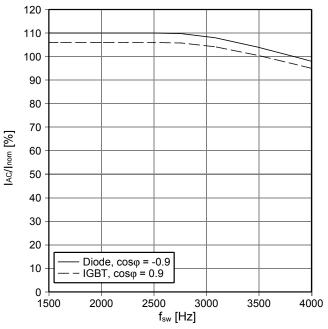


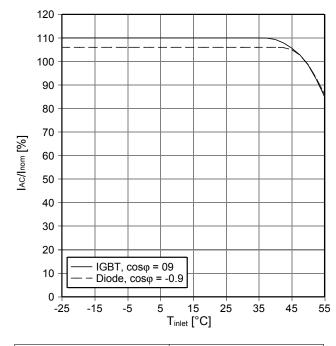
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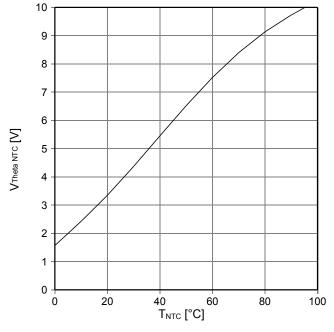
 $f_{\text{AC sine}}$  - derating curve IGBT (motor), Diode (generator)  $V_{\text{DC}}$  = 1050 V,  $V_{\text{AC}}$  = 690 V,  $f_{\text{sw}}$  = 2.6 kHz,  $cos\phi$  = 0.9  $T_{\text{inlet}}$  = 40 °C and nom. cooling conditions

 $f_{sw}$  - derating curve IGBT (motor), Diode (generator)  $V_{DC}$  = 1050 V,  $V_{AC}$  = 690 V,  $f_{AC}$  sine = 50 Hz,  $cos \phi$  = 0.9  $T_{inlet}$  = 40 °C and nom. cooling conditions









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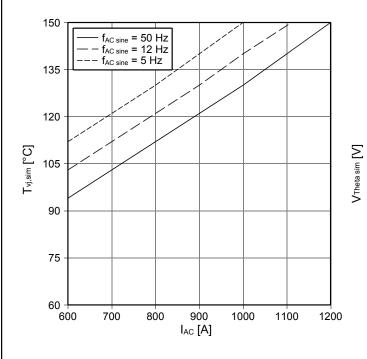
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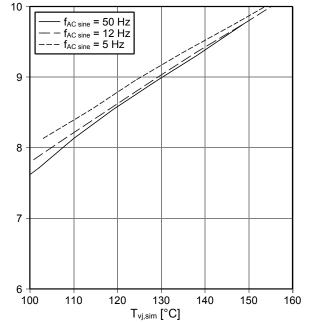


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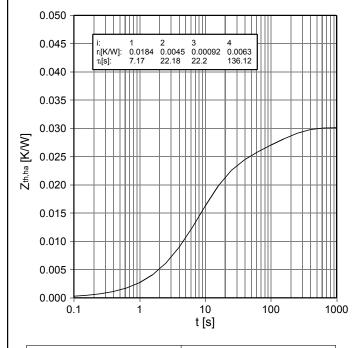
 $T_{vj,sim}$  vs.  $I_{AC}$  - Simulated junction temperatur  $V_{DC}$  = 1100 V,  $V_{AC}$  = 690  $V_{RMS},\,f_{sw}$  = 2.6 kHz,  $T_{inlet}$  = 40 °C and nom. cooling conditions

Analog temperature sensor output  $V_{\text{Theta sim}}$   $V_{\text{DC}}$  = 1100 V,  $V_{\text{AC}}$  = 690  $V_{\text{RMS}}$ ,  $f_{\text{sw}}$  = 2.6 kHz, nom. cooling conditions





 $Z_{\text{th,ha}}$  - thermal impedance heatsink to ambient per switch nom. cooling conditions



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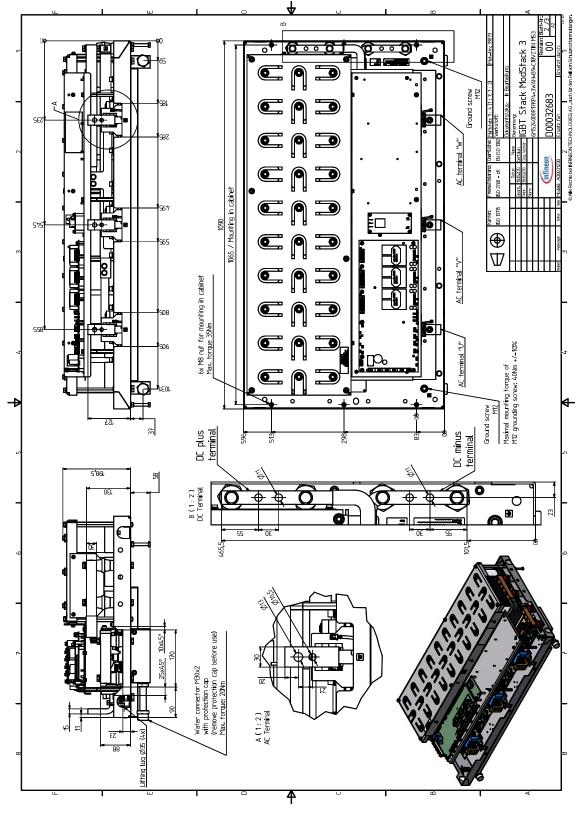
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## **Preliminary data**

## Mechanical drawing



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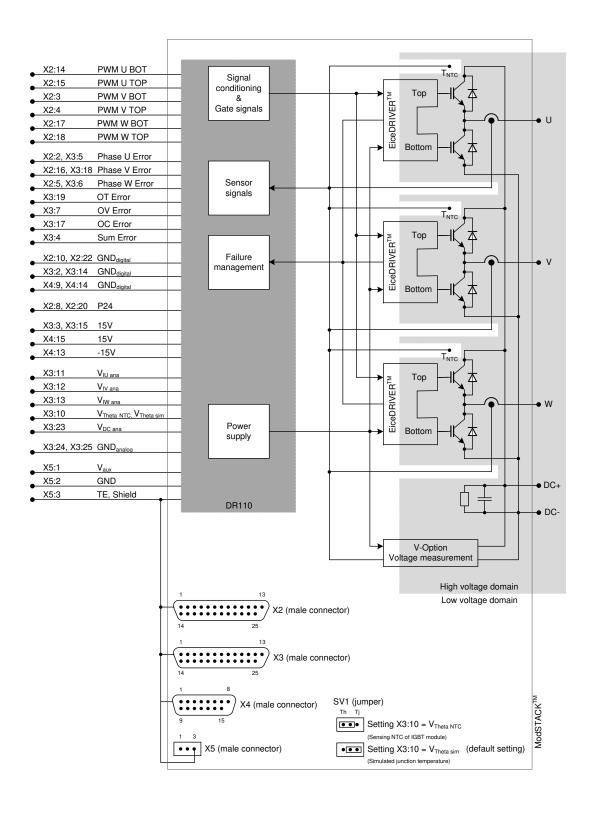
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### **Preliminary data**

## Circuit diagram



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#### Preliminary data

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