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#### **Description**

The 74AHCT08 provides provides four independent 2-input AND gates with standard push-pull outputs. The device is designed for operation with a power supply range of 4.5V to 5.5V.

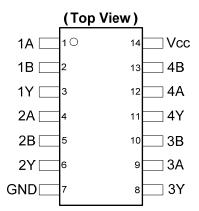
The gates perform the Boolean function:

$$Y = A \bullet B$$
 or  $Y = \overline{\overline{A} + \overline{B}}$ 

#### **Features**

- Wide Supply Voltage Range from 4.5V to 5.5V
- Inputs Are TTL Voltage Level Compatible
- Outputs Sink or Source 8mA at V<sub>CC</sub> = 4.5V
- CMOS Low Power Consumption
- Schmitt Trigger Action at All Inputs
- ESD Protection Exceeds JESD 22
  - 200-V Machine Model (A115-A)
  - 2000-V Human Body Model (A114-A)
  - Exceeds 1000-V Charged Device Model (C101C)
- Latch-Up Exceeds 250mA per JESD 78, Class II
- Range of Package Options SO-14 and TSSOP-14
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

#### **Pin Assignments**



SO-14 / TSSOP-14

#### **Applications**

- General Purpose Logic
- Wide array of products such as:
  - PCs, Networking, Notebooks, Netbooks
  - Computer Peripherals, Hard Drives, CD/DVD ROM
  - TV, DVD, DVR, Set Top Box

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

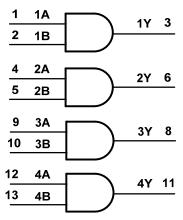
Click here for ordering information, located at the end of datasheet



## **Pin Descriptions**

Pin Number	Pin Name	Function
1	1A	Data Input
2	1B	Data Input
3	1Y	Data Output
4	2A	Data Input
5	2B	Data Input
6	2Y	Data Output
7	GND	Ground
8	3Y	Data Output
9	3A	Data Input
10	3B	Data Input
11	4Y	Data Output
12	4A	Data Input
13	4B	Data Input
14	V <sub>CC</sub>	Supply Voltage

## **Logic Diagram**



### **Function Table**

Inp	Output	
Α	В	Y
L	L	L
L	Н	L
Н	L	L
Н	Н	Н

### Absolute Maximum Ratings (Note 4) (@TA = +25°C, unless otherwise specified.)

Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	KV
ESD CDM	Charged Device Model ESD Protection	1	KV
ESD MM	Machine Model ESD Protection	200	V
V <sub>CC</sub>	Supply Voltage Range	-0.5 to +7.0	V
Vı	Input Voltage Range	-0.5 to +7.0	V
lıĸ	Input Clamp Current V <sub>I</sub> < -0.5V	-20	mA
lok	Output Clamp Current V <sub>O</sub> < 0V	-20	mA
lok	Output Clamp Current V <sub>O</sub> > V <sub>CC</sub>	20	mA
Io	Continuous Output current 0V < V <sub>O</sub> < V <sub>C</sub>	+/- 25	mA
Icc	Continuous Current Through V <sub>CC</sub>	50	mA
I <sub>GND</sub>	Continuous Current Through GND	-50	mA
TJ	Operating Junction Temperature	-40 to +150	°C
T <sub>STG</sub>	Storage Temperature	-65 to +150	°C
Ртот	Total Power Dissipation	500	mW

Note:

4. Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.



## Recommended Operating Conditions (Note 5) (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Symbol	Parameter	Min	Max	Unit
Vcc	Supply Voltage	4.5	5.5	V
$V_{I}$	Input Voltage	0	5.5	V
Vo	Output Voltage	0	$V_{CC}$	V
Δt/ΔV	Input transition rise or fall rate		20	ns/V
T <sub>A</sub>	Operating Free-Air Temperature	-40	+125	°C

Note:

### **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Cumbal	Darameter	Test Conditions	V	T <sub>A</sub> = -40°0	C to +85°C	T <sub>A</sub> = -40°C	to +125°C	Unit
Symbol	Parameter	rest Conditions	V <sub>CC</sub>	Min	Max	Min	Max	Unit
V <sub>IH</sub>	High-Level Input Voltage		4.5V to 5.5V	2.0		2.0		V
V <sub>IL</sub>	Low-Level Input Voltage		4.5V to 5.5V		0.8		0.8	V
\/	High-Level	I <sub>OH</sub> = -50μA	4.5V	4.4		4.4		V
V <sub>OH</sub>	Output Voltage	I <sub>OH</sub> = -8mA	4.5V	3.80		3.70		v
	Low-Level Output	I <sub>OL</sub> = 50μA	4.5V		0.1		0.1	V
V <sub>OL</sub>	\ / =   <b>t</b> = = = =	I <sub>OL</sub> = 8mA	4.5V		0.44		0.55	V
I <sub>I</sub>	Input Current	V <sub>I</sub> =GND to 5.5V	3.6V		±1		±2	μA
Icc	Supply Current	$V_1 = GND \text{ or } V_{CC}, I_0 = 0$	3.6V		20		40	μΑ
ΔI <sub>CC</sub>		One input at V <sub>CC</sub> -2.1V Other pins at V <sub>CC</sub> or GND	5.5V		1.35		5	mA

## **Operating Characteristics**

Parameter		Test Conditions	V <sub>CC</sub> = 5.5V Typ	Unit
$C_{\sf pd}$	Power Dissipation Capacitance per Gate	f = 1MHz	14.8	pF
Ci	Input Capacitance	$V_i = V_{CC} - or$ GND	4.0	pF

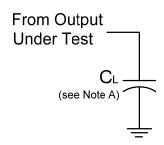
## **Switching Characteristics**

Symbol	Parameter	Test	V	1	Γ <sub>A</sub> = +25°	С	-40°C to	+85°C	-40°C to	+125°C	Unit
Syllibol	Farameter	Conditions	V <sub>CC</sub>	Min	Тур	Max	Min	Max	Min	Max	
4	Propagation Delay A <sub>N</sub> to Y <sub>N</sub>	Figure 1 $C_L = 15pF$	4.5V to 5.5V	0.5	3.4	6.9	0.5	8.0	0.5	9.0	20
ŀРD		Figure 1 C <sub>L</sub> = 50pF	4.5V to 5.5V	0.5	4.9	10.0	0.5	10.0	0.5	11.0	ns

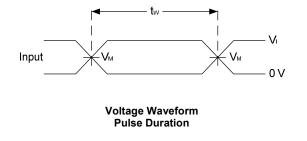
<sup>5.</sup> Unused inputs should be held at  $V_{\text{CC}}$  or Ground.

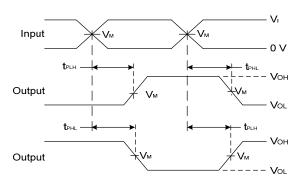


#### **Parameter Measurement Information**



V	Inputs		V <sub>M</sub>	V <sub>M</sub>	
V <sub>CC</sub>	VI	t <sub>r</sub> /t <sub>f</sub>	Inputs	Outputs	C <sub>L</sub>
4.5V to 5.5V	3.0V	3ns	1.5V	V <sub>CC</sub> /2	15pF, 50pF





**Voltage Waveform Propagation Delay Times Inverting and Non Inverting Outputs** 

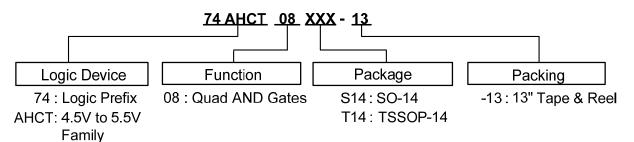
Figure 1 Load Circuit and Voltage Waveforms

Notes: A. Includes test lead and test apparatus capacitance.

- B. All pulses are supplied at pulse repetition rate ≤ 1 MHz.
  C. Inputs are measured separately one transition per measurement.
- D.  $t_{PLH}$  and  $t_{PHL}$  are the same as  $t_{PD}$ .



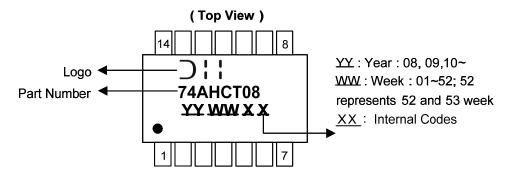
### **Ordering Information**



	Part Number	Backage Code	Dockoning	7" Tape	and Reel
	Part Number	Package Code	Packaging	Quantity	Part Number Suffix
Pb, offree Green	74AHCT08S14-13	S14	SO-14	2500/Tape & Reel	-13
Physican Green	74AHCT08T14-13	T14	TSSOP-14	2500/Tape & Reel	-13

### **Marking Information**

(1) SO-14, TSSOP-14



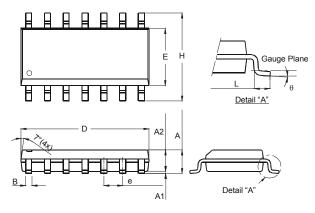
Part Number	Package
74AHCT08S14	SO-14
74AHCT08T14	TSSOP-14



## Package Outline Dimensions (All dimensions in mm.)

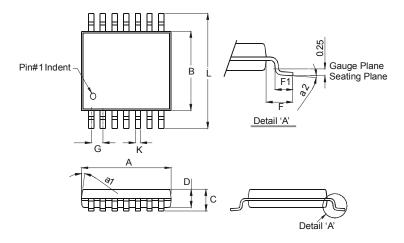
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.

#### Package Type: SO-14



	SO-14				
Dim	Min	Max			
Α	1.47	1.73			
A1	0.10	0.25			
A2	1.45	Тур			
В	0.33	0.51			
D	8.53	8.74			
Е	3.80	3.99			
е	1.27	Тур			
Н	5.80	6.20			
L	0.38	1.27			
θ	0°	8°			
All Din	All Dimensions in mm				

#### Package Type: TSSOP-14



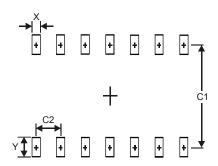
TSSOP-14					
Dim	Min	Max			
a1	7° (	(4X)			
a2	0°	8°			
Α	4.9	5.10			
В	4.30	4.50			
C		1.2			
D	0.8	1.05			
F	1.00	Тур 💮			
F1	0.45	0.75			
G	0.65	Тур			
K	0.19	0.30			
٦	6.40 Typ				
All Dimensions in mm					



## **Suggested Pad Layout**

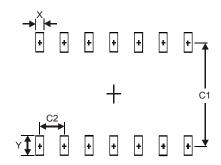
Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.

#### Package Type: SO-14



Dimensions	Value (in mm)
Х	0.60
Υ	1.50
C1	5.4
C2	1.27

#### Package Type: TSSOP-14



Dimensions	Value (in mm)
X	0.45
Υ	1.45
C1	5.9
C2	0.65



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