# imall

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74HC594

#### 8-BIT SHIFT REGISTER WITH 8-BIT OUTPUT REGISTER

## Description

The 74HC594 is a high speed CMOS device.

An eight bit shift register accepts data from the serial input (DS) on each positive transition of the shift register clock (SHCP). When asserted low, the shift regisister reset function ( $\overline{SHR}$ ) sets all shift register values to zero and is independent of all clocks. Also when asserted low, the storage register reset function ( $\overline{STR}$ ) sets all shift register values to zero and is independent of all clocks.

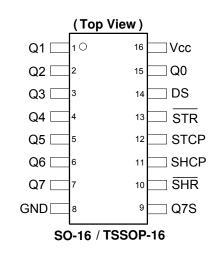
Data from the input serial shift register is placed in the output register with a rising pulse on the storages resister clock (STCP). The storage resister includes output Q7S which is used for cascading information between devices. As the information moves into the storage register, it is asserted on the push-pull outputs Q0-Q7.

All registers capture data on rising edge and change output on the falling edge. If both clocks are connected together, the input shift register is always one clock cycle ahead of the output register.

### **Features**

- Wide Supply Voltage Range from 2.0V to 6.0V
- Sinks or sources 8mA at V<sub>CC</sub>= 4.5V
- CMOS low power consumption
- Schmitt Trigger Action at All Inputs
- Inputs accept up to 6.0V
- ESD Protection Tested per JESD 22
  - Exceeds 200-V Machine Model (A115-A)
  - Exceeds 2000-V Human Body Model (A114-A)
  - Exceeds 1000-V Charged Device Model (C101C)
- Latch-Up Exceeds 250mA per JESD 78, Class II
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

## **Pin Assignments**



## **Applications**

- General Purpose Logic
- Serial to Parallel Data conversion
- Capture and hold data for extended periods of time.
- Allow simple serial bit streams from a microcontroller to control as many peripheral lines as needed.
- Wide array of products such as:
  - Computer Peripherals
  - Appliances
  - Industrial Control
- Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
  - 2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  - 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

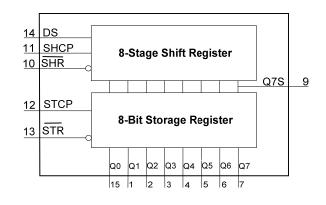
Click here for ordering information, located at the end of datasheet



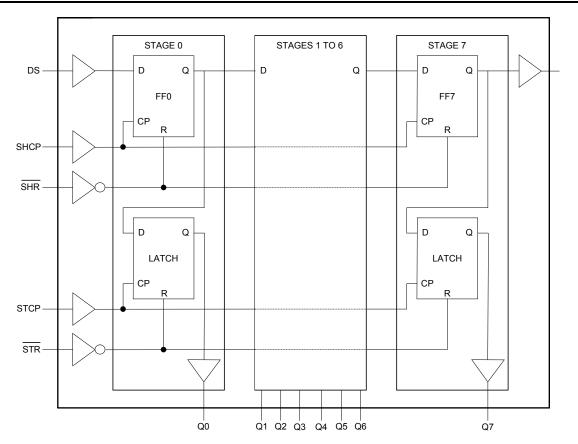
## **Pin Descriptions**

Pin Number	Pin Name	Description	
1	Q1	Parallel Data Output 1	
2	Q2	Parallel Data Output 2	
3	Q3	Parallel Data Output 3	
4	Q4	Parallel Data Output 4	
5	Q5	Parallel Data Output 5	
6	Q6	Parallel Data Output 6	
7	Q7	Parallel Data Output 7	
8	GND	Ground	
9	Q7S	Serial Data Output	
10	SHR	Shift Register Reset active low	
11	SHCP	Shift Register Clock Input	
12	STCP	Storage Register Clock Input	
13	STR	Storage Register Reset active low	
14	DS	Serial Data input	
15	Q0	Parallel Data Output 0	
16	Vcc	Supply Voltage	

## **Functional Diagram**



## Logic Diagram





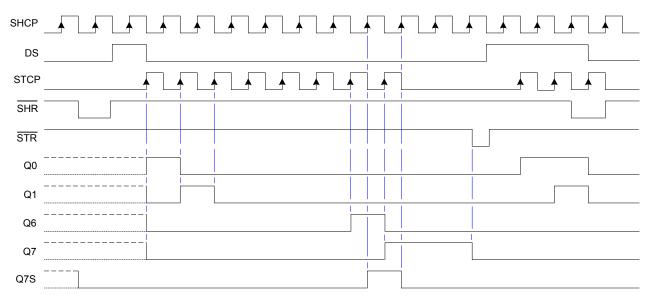
## **Functional Description and Timing Diagram**

	Control			Input Output		utput	Function
SHR	STR	SHCP	STCP	DS	Q7S		
L	Х	Х	Х	Х	L	NC	Clear Shift Register
Х	L	Х	Х	Х	NC L		Clear Storage Register
Н	Х	1	L	H or L	Q6S NC		Loads DS into shift register stage 0. All Q <sub>S</sub> shifted
Н	Н	Х	1	Х	NC	C Qs Contents of shift register moved to starge register all Q <sub>S</sub> -> Q	
Н	Н	1	1	H or L	Q6S	QnS	Shift Register one pulse count ahead of storage register.

H=HIGH voltage state L=LOW voltage state ^=LOW to HIGH transition

X= don't care - high or low (not floating)

NC= No change



## Absolute Maximum Ratings (Note 4) (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Symbol	Description		Rating	Unit
ESD HBM	Human Body Model ESD Protec	ction	2	KV
ESD CDM	Charged Device Model ESD Pro	otection	1	KV
ESD MM	Machine Model ESD Protection		200	V
V <sub>CC</sub>	Supply Voltage Range		-0.5 to +7.0	V
VI	Input Voltage Range		-0.5 to +7.0	V
Vo	Voltage applied to output in hig	h or low state	-0.3 to V <sub>CC</sub> +0.5	V
Ік	Input Clamp Current VI < -0.5V		-20	mA
l <sub>IK</sub>	Input Clamp Current VI > Vcc +0.5V		20	mA
I <sub>OK</sub>	Output Clamp Current V <sub>O</sub> <-0.5V		-20	mA
Іок	Output Clamp Current V <sub>O</sub> > V <sub>O</sub>	<sub>CC</sub> + 0.5V	20	mA
	Continuous output ourront	Q7 standard output	±25	mA
Ι <sub>Ο</sub>	Continuous output current	Qn bus driver outputs	±35	mA
I <sub>CC</sub>	Continuous current through Vcc		70	mA
I <sub>GND</sub>	Continuous current through GND		-70	mA
TJ	Operating Junction Temperature		-40 to +150	°C
T <sub>STG</sub>	Storage Temperature		-65 to +150	°C
P <sub>TOT</sub>	Total Power Dissipation		500	mW

Note: 4. Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.



## Recommended Operating Conditions (Note 5) (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Symbol	Parameter	Conditions	Min	Max	Unit
Vcc	Supply Voltage	-	2.0	6.0	V
VI	Input Voltage	_	0	Vcc	V
Vo	Output Voltage	_	0	Vcc	V
		V <sub>CC</sub> = 2.0V	-	1000	
Δt/ΔV	Input transition rise or fall rate	$V_{CC} = 4.5V$	-	500	ns/V
		V <sub>CC</sub> = 6.0V	-	400	-
TA	Operating free-air temperature	-	-40	+125	°C

Note: 5. Unused inputs should be held at V<sub>CC</sub> or Ground.

## Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Symbol	Parameter	Test Conditions	N	т	a = +25°	С	T <sub>A</sub> = -40°C	C to +85°C	T <sub>A</sub> = -40°C	to +125°C	Unit
Symbol	Faranieter	Test Conditions	V <sub>CC</sub>	Min	Тур	Max	Min	Max	Min	Max	Unit
		-	2.0V	1.5	1.2	-	1.5	-	1.5	-	
VIH	High-level Input Voltage	-	4.5V	3.15	2.4	-	3.15	-	3.15	-	V
	input voltage	-	6.0V	4.2	3.2	-	4.2	-	4.2	-	
		-	2.0V	-	0.8	0.5	-	0.5	-	0.5	
VIL	Low-level input voltage	-	4.5V	-	2.1	1.35	-	1.35	-	1.35	V
	input voltage	-	6.0V	-	2.8	1.8	-	1.8	-	1.8	
	High Level		2.0V	1.9	2.0	-	1.9	-	1.9	-	
	Output	I <sub>OH</sub> = -20µA All outputs	4.5V	4.4	4.5	-	4.4	-	4.4	-	
	Voltage	All outputs	6.0V	5.9	6.0	-	5.9	-	5.9	-	
V <sub>OH</sub>	070 autout	I <sub>OH</sub> = -4mA	4.5V	3.98	4.32	-	3.84	-	3.7	-	V
	Q7S output	I <sub>OH</sub> = -5.2mA	6.0V	5.48	5.81	-	5.34	-	5.2	-	
	Qn Bus	I <sub>OH</sub> = -6.0mA	4.5V	3.98	4.32	-	3.84	-	3.7	-	
	Outputs	I <sub>OH</sub> = -7.8mA	6.0V	5.48	5.81	-	5.34	-	5.2	-	
	Low-level		2.0V	-	0	0.1	-	0.1	-	0.1	
	Output	I <sub>OL</sub> = 20μA All outputs	4.5V	-	0	0.1	-	0.1	-	0.1	
	Voltage	All outputs	6.0V	-	0	0.1	-	0.1	-	0.1	
V <sub>OL</sub>	070 autout	I <sub>OL</sub> = 4.0mA	4.5V	-	.15	0.26	-	0.33	-	0.4	V
	Q7S output	I <sub>OL</sub> = 5.2mA	6.0V	-	.16	0.26	-	0.33	-	0.4	
	Qn Bus	I <sub>OL</sub> = 6.0mA	4.5V	-	.15	0.26	-	0.33	-	0.4	
	Outputs	I <sub>OL</sub> = 7.8mA	6.0V	_	.16	0.26	_	0.33	-	0.4	
lı	Input Current	$V_1$ = GND to 5.5V	6.0V	-	-	±0.1	-	± 1	_	± 1	μA
Icc	Supply Current	$V_1 = GND \text{ or } V_{CC}$ $I_0 = 0$	6.0V	_	-	8.0	_	80	_	160	μΑ
Ci	Input Capacitance	$V_i = V_{CC} - or GND$	6.0V	-	3.5	10	_	10	_	10	pF

## Operating Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

	Parameter	Test Conditions	V <sub>CC</sub> = 5V Typ	Unit
C <sub>pd</sub>	Power dissipation capacitance	f = 1 MHz all outputs switching-no load	51	pF

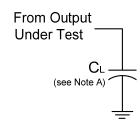


## **Switching Characteristics**

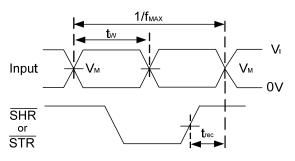
Symbol /		_		-	Γ <sub>A</sub> = +25°	С	-40°C to	o +85°C	-40°C to	+125°C	Unit	
Parameter	Pins	Test Conditions	Vcc	Min	Тур	Max	Min	Max	Min	Max		
			2.0V	6	30	_	4.8	_	4	_		
fMAX	SHCP or	Figure 2	4.5V	30	92	_	24	_	20	_		
Maximum	STCP	Ŭ.	5.0V		100	_		_		_	MHz	
Frequency			6.0V	35	109	_	28	_	24	-		
	SHCP		2.0V	80	10	_	100	_	120	_		
	HIGH or	Figure 2	4.5V	16	4	-	20	_	24	_		
	LOW	-	6.0V	14	3	_	17	_	20	_		
	STCP		2.0V	80	10	_	100	_	120	-		
t <sub>W</sub>	HIGH or	Figure 2	4.5V	16	4	_	20	_	24	-	ns	
Pulse Width	LOW	-	6.0V	14	3	_	17	_	20	_	113	
	SHR and		2.0V	80	14	_	100	_	120	_		
	STR	Figure 2	4.5V	16	5	_	20	_	24	_		
	HIGH or LOW		6.0V	14	4	_	17	-	20	-		
			2.0V	100	10	-	125	-	150	_		
	DS to SHCP	Figure 2	4.5V	20	4	-	25	_	30	-	ns	
	SHOP		6.0V	17	3	-	21	_	26	-		
			2.0V	100	14	-	125	_	150	-		
t <sub>SU</sub>	SHR to STCP	Figure 2	4.5V	20	5	_	25	-	30	-	ns	
Set-up Time	3105		6.0V	17	4	-	21	-	26	-		
			2.0V	100	17	-	125	-	150	_		
	SHCP to STCP	Figure 2	4.5V	20	6	_	25	-	30	-	ns	
	SICP	5100		6.0V	17	5	-	21	_	26	_	
			2.0V	-	44	150	_	185	_	225		
	SHCP to	Figure 2	4.5V	-	16	30	-	37	-	45		
	Q7S	Ű,	5.0V	-	13	-	-	-	-	-	ns	
t <sub>PD</sub>			6.0V	_	14	26	-	31	_	38		
Propagation Delay			2.0V	-	44	150	-	185	-	225		
Delay	STCP to	Figure 2	4.5V	-	16	30	-	37	-	45		
	Qn	-	5.0V	_	13	-	-	_	-	-	ns	
			6.0V	_	14	26	-	31	-	38		
			2.0V	25	-8	-	30	_	35	_		
t <sub>H</sub>	DS to SHCP	Figure 2	4.5V	5	-3	-	6	_	7	_	ns	
Hold Time	SHOP		6.0V	4	-2	-	5	_	6	_		
	SHR to		2.0V	50	-14	-	65	-	75	_		
t <sub>REC</sub>	SHCP and	Figure 2	4.5V	10	-5	-	13	-	15	_	ns	
Recovery Time	STR to STCP		6.0V	9	-4	-	11	_	13	_	110	
			2.0V	-	39	150	-	185	-	225		
	SHR to	Figure 0	4.5V	-	14	30	-	37	-	45		
	Q7S	Figure 2	5.0V	_	11	-	-	-	-	_	ns	
t <sub>PHL</sub>			6.0V	-	12	26	-	31	-	38		
Propagation Delay			2.0V	-	39	125	-	155	-	185		
Delay		Figure 0	4.5V	-	14	25	-	31	-	37		
	STR to Qn	Figure 2	5.0V	-	11	_	-	-	-	-	ns	
			6.0V	-	12	21	-	26	-	31		
			2.0V	-	19	75	-	95	-	110		
	Serial data output Q7S	Figure 2	4.5V	-	7	15	-	19	-	22	ns	
tтнL	output Q/S		6.0V	-	6	13	-	16	-	19		
Transition Time	Parallel		2.0V	-	14	60	-	75	-	90		
	Data	Figure 2	4.5V	-	5	12	-	15	-	18	ns	
	$\text{Outputs } Q_N$		6.0V	-	4	10	-	13	-	15		



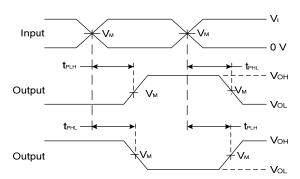
## **Parameter Measurement Information**



V	Inj	outs	V	0
V <sub>CC</sub>	VI	t <sub>r</sub> /t <sub>f</sub>	V <sub>M</sub>	C∟
2.0V	V <sub>CC</sub>	6ns	V <sub>CC</sub> /2	50pF
4.5V	Vcc	6ns	V <sub>CC</sub> /2	50pF
5.0V	V <sub>CC</sub>	6ns	V <sub>CC</sub> /2	15pF
6.0V	V <sub>CC</sub>	6ns	V <sub>CC</sub> /2	50pF



#### Voltage Waveform Pulse Duration and Recovery Time



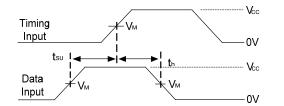
#### Voltage Waveform Propagation Delay Times Inverting and Non Inverting Outputs

Notes:

- A. Includes test lead and test apparatus capacitance.
- B. All pulses are supplied at pulse repetition rate  $\leq$  10 MHz.
- C. Inputs are measured separately one transition per measurement.

D.  $t_{\mathsf{PLH}}$  and  $t_{\mathsf{PHL}}$  are the same as  $t_{\mathsf{PD.}}$ 

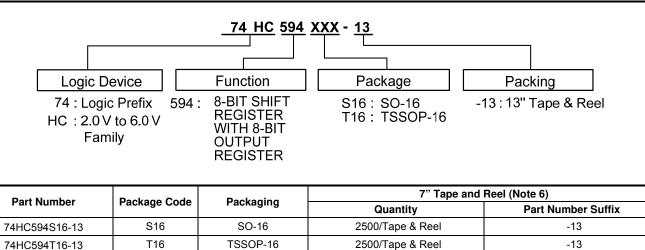
#### Figure 2 Load Circuit and Voltage Waveforms



#### Voltage Waveform Set-up and Hold Times



## **Ordering Information**

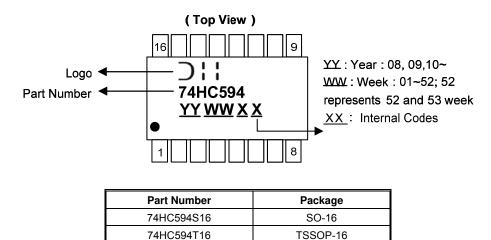


	74HC594116-13	116	1550P-16	2500/Tape & Reel	

#### Note: 6. The taping orientation is located on our website at http://www.diodes.com/datasheets/ap02007.pdf

## **Marking Information**

#### (1) SO-16, TSSOP16

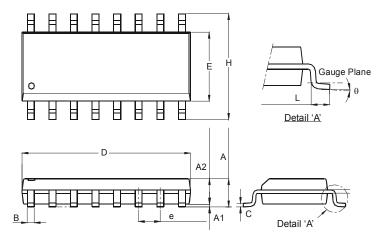




## Package Outline Dimensions (All dimensions in mm.)

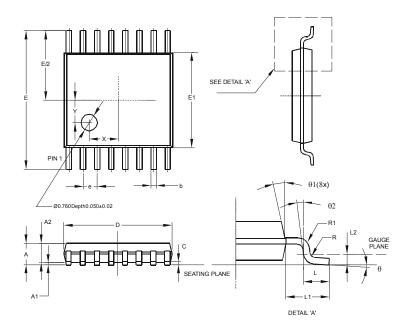
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.

#### Package Type: SO-16



	SO-16					
Dim	Min	Max				
Α	1.40	1.75				
A1	0.10 0.25					
A2	1.30 1.50					
В	0.33 0.51					
С	0.19	0.25				
D	9.80 10.00					
Е	<b>E</b> 3.80 4.00					
е	1.27	Тур				
н	5.80 6.20					
L	0.38 1.27					
Θ	0°	8°				
All D	imension	s in mm				

#### Package Type: TSSOP-16



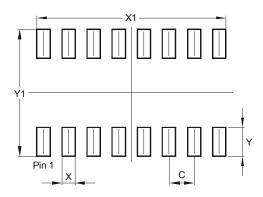
	TSSOP-16							
Dim	Min Max Typ							
Α	- 1.08 -							
<b>A</b> 1	0.05 0.15 -							
A2	0.80	0.80 0.93 -						
b	0.19	0.19 0.30 -						
С	0.09	0.20	-					
D	4.90	5.10	-					
Ε		6.40 BS	SC					
E1	4.30 4.50 -							
е	0.65 BSC							
L	0.45 0.75 -							
L1	1	.00 RI	EF					
L2	0	.25 BS	SC					
R	0.09	-	-					
R1	0.09	-	-					
Х	1	-	1.350					
Y	-	-	1.050					
Θ	0°	8°	-					
Θ1	5°	15°	-					
Θ2	0°	-	-					
All D	Dimen	sions	in mm					



## **Suggested Pad Layout**

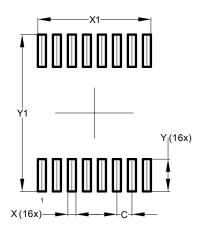
Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.

#### Package Type: SO-16



Dimensions	Value (in mm)
С	1.270
Х	0.670
X1	9.560
Y	1.450
Y1	6.400

Package Type: TSSOP-16



Dimensions	Value (in mm)
С	0.650
Х	0.350
X1	4.900
Y	1.400
Y1	6.800



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