imall

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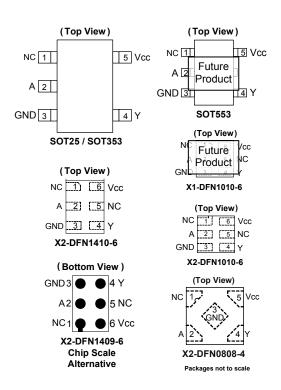
SINGLE SCHMITT-TRIGGER INVERTER

Description

The 74LVC1G14 is a single 1-input Schmitt-trigger inverter with a standard push-pull output. The device is designed for operation with a power supply range of 1.65V to 5.5V. The inputs are tolerant to 5.5V allowing this device to be used in a mixed voltage environment. The device is fully specified for partial power down applications using I_{OFF} . The I_{OFF} circuitry disables the output preventing damaging current backflow when the device is powered down. The gate performs the positive Boolean function:

 $\mathsf{Y}=\overline{\mathsf{A}}$

Pin Assignments



Features

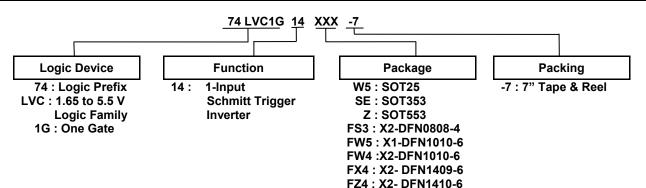
- Wide Supply Voltage Range from 1.65V to 5.5V
- ± 24mA Output Drive at 3.3V
- CMOS low power consumption
- IOFF Supports Partial-Power-Down Mode Operation
- Inputs accept up to 5.5V
- ESD Protection Exceeds JESD 22
 - o 200-V Machine Model (A115)
 - 2000-V Human Body Model (A114)
- Latch-Up Exceeds 100mA per JESD 78, Class I
- Range of Package Options
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Applications

- Voltage Level Shifting
- General Purpose Logic
- Power Down Signal Isolation
- Wide array of products such as:
 - o PCs, networking, notebooks, netbooks, PDAs
 - Computer peripherals, hard drives, CD/DVD ROM
 - TV, DVD, DVR, set top box
 - o Cell Phones, Personal Navigation / GPS
 - o MP3 players ,Cameras, Video Recorders
- Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
 - See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and < 1000 ppm antimony compounds.



Ordering Information



Deviee	Package	Package	Package	7" Tape	and Reel
Device	Code	(Notes 4,5)	Size	Quantity	Part Number Suffix
74LVC1G14W5-7	W5	SOT25	3.0mm X 2.8mm X 1.2mm 0.95mm lead pitch	3000/Tape & Reel	-7
74LVC1G14SE-7	SE	SOT353	2.0mm X 2.0mm X 1.1mm 0.65mm lead pitch	3000/Tape & Reel	-7
74LVC1G14Z-7 Future Product	Z	SOT553 Future Product	1.6mm X 1.6 mm X 0.62mm 0.5mm lead pitch	4000/Tape & Reel	-7
74LVC1G14FS3-7	FS3	X2-DFN0808-4	0.9mm X 0.9 mm X 0.35mm 0.5mm pad pitch (diamond)	5000/Tape & Reel	-7
74LVC1G14FW5-7 (Future Product)	FW5	X1-DFN1010-6 (Future Product)	1.0mm X 1.0mm X 0.5mm 0.35mm pad pitch	5000/Tape & Reel	-7
74LVC1G14FW4-7	FW4	X2-DFN1010-6	1.0mm X 1.0mm X 0.4mm 0.35mm pad pitch	5000/Tape & Reel	-7
74LVC1G14FX4-7	FX4	X2-DFN1409-6 (Chip scale alternative)	1.4mm X 0.9mm X 0.4mm 0.5mm pad pitch	5000/Tape & Reel	-7
74LVC1G14FZ4-7	FZ4	X2-DFN1410-6	1.4mm X 1.0mm X 0.4mm 0.5mm pad pitch	5000/Tape & Reel	-7

Notes:

4. Pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at

http://www.diodes.com/datasheets/ap02001.pdf.

5. The taping orientation is located on our website at http://www.diodes.com/datasheets/ap02007.pdf

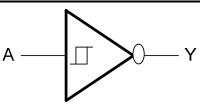
Pin Descriptions

Pin Name	Description
А	Data Input
GND	Ground
Y	Data Output
V _{CC}	Supply Voltage

Function Table

Inputs	Output
Α	Y
Н	L
L	Н

Logic Diagram





Absolute Maximum Ratings (Notes 6, 7)

Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	kV
ESD MM	Machine Model ESD Protection	200	V
Vcc	Supply Voltage Range	-0.5 to 6.5	V
VI	Input Voltage Range	-0.5 to 6.5	V
Vo	Voltage Applied to Output in High Impedance or IOFF State	-0.5 to 6.5	V
Vo	Voltage Applied to Output in High or Low State	-0.5 to V _{CC} +0.5	V
lıĸ	Input Clamp Current V ₁ < 0	-50	mA
loк	Output Clamp Current	-50	mA
lo	Continuous Output Current	±50	mA
ICC, IGND	Continuous Current Through V _{CC} or GND	±100	mA
TJ	Operating Junction Temperature	-40 to 150	°C
T _{STG}	Storage Temperature	-65 to 150	°C

Notes: 6. Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.

7. Forcing the maximum allowed voltage could cause a condition exceeding the maximum current or conversely forcing the maximum current could cause a condition exceeding the maximum voltage. The ratings of both current and voltage must be maintained within the controlled range.

Recommended Operating Conditions (Note 8)

Symbol		Parameter	Min	Max	Unit		
N/		Operating	1.65	5.5	V		
V _{CC}	Input Voltage Output Voltage	Data retention only	1.5	_	V		
VI	Input Voltage		0	5.5	V		
Vo	Output Voltage		0	Vcc	V		
		V _{CC} = 1.65V	_	-4			
		V _{CC} = 2.3V	_	-8			
		V _{CC} = 2.7V	_	-12			
I _{ОН}	High-Level Output Current		_	-16	mA		
		$V_{CC} = 3V$	_	-24			
		$V_{CC} = 4.5V$	_	-32			
		V _{CC} = 1.65V	_	4			
		V _{CC} = 2.3V	_	8			
		V _{CC} = 2.7V	_	12			
I _{OL}	Low-Level Output Current		_	16	mA		
		V _{CC} = 3V	_	24			
		V _{CC} = 4.5V	_	32			
TA	Operating Free-Air Temperature	—	-40	+125	°C		

Notes: 8. Unused inputs should be held at V_{CC} or Ground.



Electrical Characteristics $T_A = -40^{\circ}C$ to $+85^{\circ}C$ (All typical values are at $V_{CC} = 3.3V$, $T_A = +25^{\circ}C$)

Symbol	Parameter	Test Conditions	V _{CC}	Min	Тур.	Max	Unit
		—	1.65V	0.70	_	1.20	_
		_	2.3V	1.11	_	1.60	_
V_{T+}	Positive-Going Input Threshold Voltage	_	3V	1.50	_	2.00	
	Theshold Voltage	_	4.5V	2.16	_	2.74	_
		—	5.5V	2.61	_	3.33	_
		—	1.65V	0.30	_	0.72	—
		—	2.3V	0.58	_	1.00	
V_{T-}	Negative- Going Input	—	3V	0.80	_	1.30	_
	Threshold Voltage	—	4.5V	1.21	_	1.95	_
		_	5.5V	1.45	_	2.35	_
		_	1.65V	0.30	_	0.62	_
		_	2.3V	0.40	_	0.80	
ΔV_{T}	Hysteresis	_	3V	0.35	_	1.00	
	(V _{T+} - V _{T-)}	_	4.5V	0.55	_	1.10	
		_	5.5V	0.60		1.20	
		I _{OH} = -100μA	1.65V to 5.5V	V _{cc} -0.1	_		
		I _{OH} = -4mA	1.65V	1.2	_		
		I _{OH} = -8mA	2.3V	1.9	_		
V _{OH}	High Level Output Voltage	I _{OH} = -12mA	2.7V	2.2	_		V
		I _{OH} = -16mA	e) /	2.4	_		
		I _{OH} = -24mA	- 3V	2.3	_	1.20 1.60 2.00 2.74 3.33 0.72 1.00 1.30 1.95 2.35 0.62 0.80 1.00 1.10	
		I _{OH} = -32mA	4.5V	3.8	_		
		I _{OL} = 100μA	1.65V to 5.5V	_	_	0.1	
		I _{OL} = 4mA	1.65V	_	_	0.45	
		I _{OL} = 8mA	2.3V	_	_	0.3	
V _{OL}	Low-Level Output Voltage	I _{OL} = 12mA	2.7V	_	_	0.4	v
		I _{OL} = 16mA	a 1/	_	_	0.4	_
		I _{OL} = 24mA	- 3V		_	0.55	
		I _{OL} = 32mA	4.5		_	0.55	
I _I	Input Current	V _I = 5.5 V or GND	0 to 5.5V	_	_	± 5	μA
I _{OFF}	Power Down Leakage Current	$V_{\rm I}$ or $V_{\rm O}$ = 5.5V	0	_	_	± 10	μA
Icc	Supply Current	V _I = 5.5V of GND I _O = 0	1.65V to 5.5V	_	_	10	μA
Δlcc	Additional Supply Current	Input at V _{CC} –0.6V	3V to 5.5V	_	_	500	μA



Electrical Characteristics $T_A = -40^{\circ}C$ to $+125^{\circ}C$ (All typical values are at $V_{CC} = 3.3V$, $T_A = +25^{\circ}C$)

Symbol	Parameter	Test Conditions	V _{CC}	Min	Тур.	Max	Unit
		_	1.65V	0.70	_	1.20	
		_	2.3V	1.11	_	1.60	_
V_{T^+}		_	3V	1.50	_	2.00	
	Theshold Voltage	_	4.5V	2.16	_	2.74	
		_	5.5V	2.61	_	3.33	
		—	1.65V	0.30	_	0.75	_
		_	2.3V	0.58	_	1.03	_
V _{T-}		_	3V	0.80	_	1.33	_
	Threshold Voltage	_	4.5V	1.21	_	1.95	_
		_	5.5V	1.45	_	2.35	_
		_	1.65V	0.30	_	0.62	_
		_	2.3V	0.37	_	0.80	
ΔV_{T}		_	3V	0.32	_	1.00	_
	(V _{T+} - V _{T-})	_	4.5V	0.50	_	1.20	_
		_	5.5V	0.55	_	1.40	_
		I _{OH} = -100μA	1.65V to 5.5V	V _{CC} -0.1	_	_	
		I _{OH} = -4mA	1.65V	0.95	_	_	
		I _{OH} = -8mA	2.3V	1.7	_	_	
V _{OH}	Investicit Voltage /T- Negative- Going Input Threshold Voltage VT Hysteresis (VT+- VT-) OH High Level Output Voltage 'OH High Level Output Voltage 'OL Low-Level Output Voltage II Input Current DFF Power Down Leakage Current CC Supply Current	I _{OH} = -12mA	2.7V	1.9	_	_	V
		I _{OH} = -16mA	<i></i>	1.9	_	_	
		I _{OH} = -24mA	- 3V	2.0	_	_	
		I _{OH} = -32mA	4.5V	3.4	_	_	
		I _{OL} = 100μΑ	1.65V to 5.5V	_	_	0.1	
		I _{OL} = 4mA	1.65V	_	_	0.7	
		I _{OL} = 8mA	2.3V	_	_	0.45	
V _{OL}	Low-Level Output Voltage	I _{OL} = 12mA	2.7V	_	_	0.6	V
		I _{OL} = 16mA	0.4	_	_	0.6	
		I _{OL} = 24mA	- 3V	_	_	0.8	
		I _{OL} = 32mA	4.5V	_	_	0.8	
II.	Input Current	V _I = 5.5V or GND	0 to 5.5V	_	_	± 100	μA
I _{OFF}	Power Down Leakage Current	$V_1 \text{ or } V_0 = 5.5 V$	0	_	_	± 200	μA
Icc	Supply Current	V _I = 5.5V of GND I _O = 0	1.65V to 5.5V	_	_	200	μA
Δlcc	Additional Supply Current	Input at V _{CC} –0.6V	3V to 5.5V	_	_	5000	μA



Package Characteristics (All typical values are at $V_{CC} = 3.3V$, $T_A = +25^{\circ}C$)

Symbol	Parameter	Test Conditions	Vcc	Min	Тур.	Max	Unit
		SOT25		—	204	_	
		SOT353		_	371	-	
		SOT553		_	231		
0	Thermal Resistance	X2-DFN0808-4		_	400	_	°044
θ _{JA}	Junction-to-Ambient	X1-DFN1010-6	(Note 9)	_	435	_	°C/W
		X2-DFN1010-6		_	445	_	
		X2-DFN1409-6		_	470	_	
		X2-DFN1410-6		_	460	_	
		SOT25		—	52	_	
		SOT353		_	143	-	°C/W
		SOT553		_	105	-	
0	Thermal Resistance	X2-DFN0808-4	(Ninte O)	_	225	_	
$\theta^{\rm JC}$	Junction-to-Case	X1-DFN1010-6	(Note 9)	_	250	_	
		X2-DFN1010-6		_	250	_	
		X2-DFN1409-6		_	275	_	
		X2-DFN1410-6		_	265	_	

Note: 9. Test condition for each of the 8 package types: Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

Switching Characteristics

T_A= -40°C to +85°C, C_L = 15pF as noted (see Figure 1)

Parameter	From Input	To Output	V _{CC} = 1.8V ± 0.15V		V _{CC} = 2.5V ± 0.2V		V _{CC} = 3.3V ± 0.3V		V _{CC} = 5V ± 0.5V		Unit
			Min	Max	Min	Max	Min	Max	Min	Max	
t _{pd}	А	Y	1.0	9.9	0.7	5.5	0.7	4.6	0.7	4.4	ns

$T_A = -40^{\circ}C$ to $+85^{\circ}C$, $C_L = 30$ or 50pF as noted (see Figure 2)

Parameter	From Input	To Output	V _{CC} = 1.8V ± 0.15V		V _{CC} = 2.5V ± 0.2V		V _{CC} = 3.3V ± 0.3V		V _{CC} = 5V ± 0.5V		Unit
			Min	Мах	Min	Max	Min	Max	Min	Max	
t _{pd}	A	Y	1.0	11	0.7	6.5	0.7	5.5	0.7	5	ns

T_A= -40°C to +125°C, C_L = 15pF as noted (see Figure 1)

Parameter	From Input	To Output	V _{CC} = 1.8V ± 0.15V		V _{CC} = 2.5V ± 0.2V		V _{CC} = 3.3V ± 0.3V		V _{CC} = 5V ± 0.5V		Unit
			Min	Мах	Min	Max	Min	Max	Min	Мах	
t _{pd}	А	Y	1.0	12.5	0.7	7.5	0.7	6.5	0.7	5.5	ns

$\textbf{T}_{\textbf{A}}\textbf{=}\textbf{-40^{\circ}C} \text{ to +125^{\circ}C}, \text{ } \text{C}_{\text{L}}\textbf{=}30 \text{ or 50pF} \text{ as noted (see Figure 2)}$

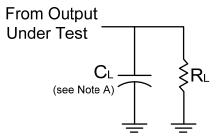
Parameter	From	To Output	V _{CC} = 1.8V ± 0.15V		V _{CC} = 2.5V ± 0.2V		V _{CC} = 3.3V ± 0.3V		V _{CC} = 5V ± 0.5V		Unit
	Input		Min	Max	Min	Max	Min	Max	Min	Max	
t _{pd}	А	Y	1.0	14.0	0.7	8.5	0.7	7.0	0.7	6.5	ns



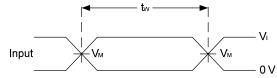
Operating Characteristics

T _A = +25°C							
	Parameter	Test	V _{CC} = 1.8V	V _{CC} = 2.5V	V _{CC} = 3.3V	V _{CC} = 5V	Unit
	Parameter	Conditions	Тур.	Тур.	Тур.	Тур.	Unit
C _{pd}	Power Dissipation Capacitance	f = 10 MHz	20	21	22	25	pF

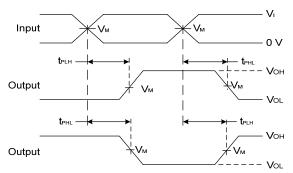
Parameter Measurement Information



Vcc	In	puts	V _M	CL	RL
VCC	VI	t _r /t _f	¥ M	0L	
1.8V±0.15V	V _{CC}	≤2ns	V _{CC} /2	15pF	1ΜΩ
2.5V±0.2V	V _{CC}	≤2ns	V _{CC} /2	15pF	1ΜΩ
3.3V±0.3V	3V	≤2.5ns	1.5V	15pF	1ΜΩ
5V±0.5V	V _{CC}	≤2.5ns	V _{CC} /2	15pF	1ΜΩ



Voltage Waveform Pulse Duration



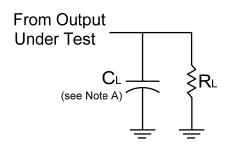
Voltage Waveform Propagation Delay Times Inverting and Non Inverting Outputs

Figure 1. Load Circuit and Voltage Waveforms

- Notes:
- A. Includes test lead and test apparatus capacitance.
 B. All pulses are supplied at pulse repetition rate ≤ 10MHz.
 C. Inputs are measured separately one transition per measurement.
 - D. t_{PLH} and t_{PHL} are the same as $t_{PD.}$



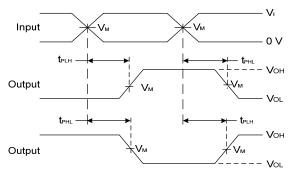
Parameter Measurement Information (Cont.)



V	Inj	outs	V	CL	RL
V _{cc}	VI	t _r /t _f	V _M		
1.8V±0.15V	V _{CC}	≤2ns	V _{CC} /2	30pF	1kΩ
2.5V±0.2V	V _{CC}	≤2ns	V _{CC} /2	30pF	500Ω
3.3V±0.3V	3V	≤2.5ns	1.5V	50pF	500Ω
5V±0.5V	V _{CC}	≤2.5ns	V _{CC} /2	50pF	500Ω



Voltage Waveform Pulse Duration



Voltage Waveform Propagation Delay Times Inverting and Non Inverting Outputs

Figure 2. Load Circuit and Voltage Waveforms

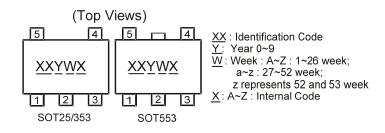
Notes:

- A. Includes test lead and test apparatus capacitance.
 B. All pulses are supplied at pulse repetition rate ≤ 10MHz.
 C. Inputs are measured separately one transition per measurement.
- D. t_{PLH} and t_{PHL} are the same as t_{PD} .



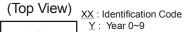
Marking Information

(1) SOT25, SOT353 and SOT553



Part Number	Package	Identification Code
74LVC1G14W5-7	SOT25	UP
74LVC1G14SE-7	SOT353	UP
74LVC1G14Z-7	SOT553	UP

(2) DFN packages





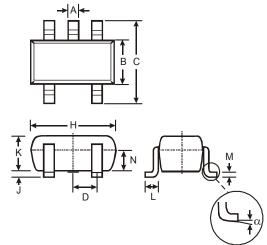
 $\underline{M} : \text{Neen 0-9}$ $\underline{M} : \text{Week : } A \sim Z : 1 \sim 26 \text{ week;}$ $a \sim z : 27 \sim 52 \text{ week;}$ z represents 52 and 53 week $\underline{X} : A \sim Z : \text{ Internal Code}$

Part Number	Package	Identification Code
74LVC1G14FS3-7	X2-DFN0808-4	WP
74LVC1G14FW5-7	X1-DFN1010-6	V8
74LVC1G14FW4-7	X2-DFN1010-6	UP
74LVC1G14FX4-7	X2-DFN1409-6	MG
74LVC1G14FZ4-7	X2-DFN1410-6	UP



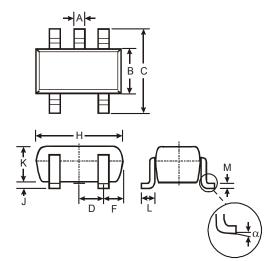
Package Outline Dimensions (All Dimensions in mm)

(1) Package Type: SOT25



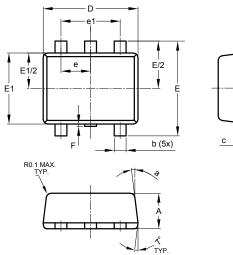
SOT25					
Dim	Min	Max	Тур		
Α	0.35	0.50	0.38		
в	1.50	1.70	1.60		
С	2.70	3.00	2.80		
D			0.95		
Н	2.90	3.10	3.00		
٦	0.013	0.10	0.05		
κ	1.00	1.30	1.10		
L	0.35	0.55	0.40		
М	0.10	0.20	0.15		
Ν	0.70	0.80	0.75		
α	0°	8°			
All D	imensi	ons in	mm		

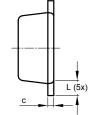
(2) Package Type: SOT353



SOT353					
Dim	Min	Max			
Α	0.10	0.30			
В	1.15	1.35			
С	2.00	2.20			
D	0.65 Тур				
F	0.40	0.45			
н	1.80	2.20			
J	0	0.10			
к	0.90	1.00			
L	0.25	0.40			
М	0.10	0.22			
α	0°	8°			
All Di	mensions	in mm			

(3) Package Type: SOT553



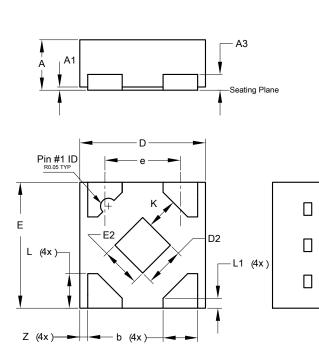


SOT553					
Dim	Min	Max	Тур		
Α	0.55	0.62	0.60		
b	0.15	0.30	0.20		
c	0.10	0.18	0.15		
D	1.50	1.70	1.60		
ш	1.55	1.70	1.60		
E1	1.10	1.25	1.20		
e	C	.50 BS(C		
e1	1	.00 BS0	0		
F	0.00	0.10			
L	0.10	0.30	0.20		
а	6°	8°	7°		
All	Dimens	ions in	mm		



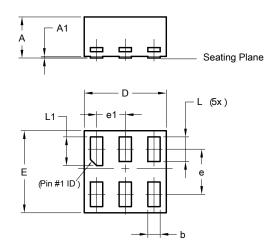
Package Outline Dimensions (cont.)

(4) Package Type X2-DFN0808-4



X2-DFN0808-4						
Dim	Min	Max	Тур			
Α	0.25	0.35	0.30			
A1	0	0.04	0.02			
A3	-	-	0.13			
b	0.17	0.27	0.22			
D	0.75	0.85	0.80			
D2	0.15	0.35	0.25			
Е	0.75	0.85	0.80			
E2	0.15	0.35	0.25			
e	-	-	0.48			
К	0.20	-	-			
L	0.17	0.27	0.22			
L1	0.02	0.12	0.07			
Z	-	-	0.05			
A	II Dimens	sions in r	nm			

(5) Package Type: X1-DFN1010-6

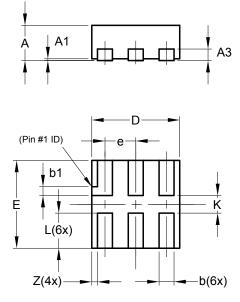


	X1-DFN1010-6					
Dim	Min	Max	Тур			
Α	-	0.50	0.39			
A1	1	0.04	-			
b	0.12	0.20	0.15			
D	0.95	1.050	1.00			
Е	0.95	1.050	1.00			
е		0.55 B	SC			
e1		0.35 B	SC			
Ĺ	0.27	0.35	0.30			
L1	0.32	0.40	0.35			
All	Dimen	sions	in mm			



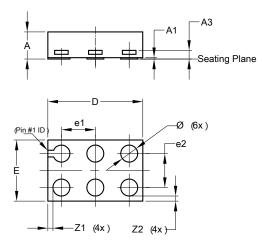
Package Outline Dimensions (cont.)

(6) Package Type X2-DFN1010-6



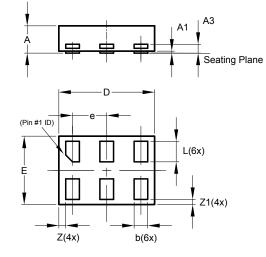
X2-DFN1010-6					
Dim	Min	Max	Тур		
Α		0.40	0.39		
A1	0.00	0.05	0.02		
A3			0.13		
b	0.14	0.20	0.17		
b1	0.05	0.15	0.10		
D	0.95	1.05	1.00		
ш	0.95	1.05	1.00		
e			0.35		
L	0.35	0.45	0.40		
Κ	0.15				
Ζ	_		0.065		
All	Dimens	ions in	mm		

(7) Package Type: X2-DFN1409-6 6 CHIP SCALE ALTERNATIVE



X2-DFN1409-6					
Dim	Min	Max	Тур		
Α	-	0.40	0.39		
A1	0	0.05	0.02		
A3	-	-	0.13		
Ø	0.20	0.30	0.25		
D	1.35	1.45	1.40		
Е	0.85	0.95	0.90		
e1	-	-	0.50		
e2	-	-	0.50		
Z1	-	-	0.075		
Z2	-	-	0.075		
All	Dimens	ions in	mm		

(8) Package Type: X2-DFN1410-6



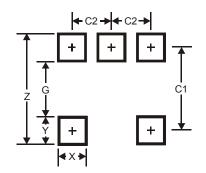
X2-DFN1410-6			
Dim	Min	Max	Тур
Α	_	0.40	0.39
A1	0.00	0.05	0.02
A3	_		0.13
b	0.15	0.25	0.20
D	1.35	1.45	1.40
Е	0.95	1.05	1.00
е			0.50
L	0.25	0.35	0.30
Z		_	0.10
Z1	0.045	0.105	0.075
All Dimensions in mm			



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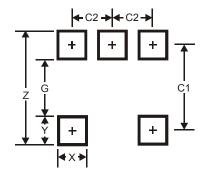
Suggested Pad Layout

(1) Package Type: SOT25



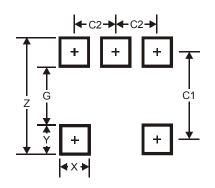
Dimensions	Value (in mm)
Z	3.20
G	1.60
х	0.55
Y	0.80
C1	2.40
C2	0.95

(2) Package Type: SOT353



Dimensions	Value (in mm)
Z	2.5
G	1.3
х	0.42
Y	0.6
C1	1.9
C2	0.65

(3) Package Type: SOT553

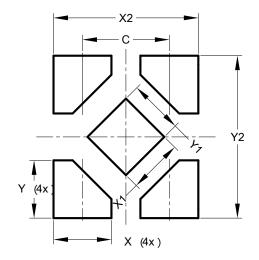


Dimensions	Value (in mm)
Z	2.2
G	1.2
X	0.375
Y	0.5
C1	1.7
C2	0.5



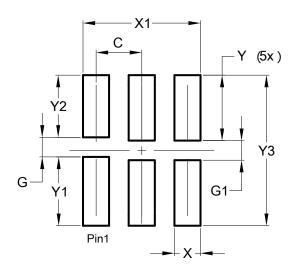
Suggested Pad Layout (cont.)

(4) Package Type X2-DFN0808-4



Dimensions	Value (in mm)
С	0.480
Х	0.320
X1	0.300
X2	0.800
Y	0.320
Y1	0.300
Y2	0.900

(5)(Package Type X1-DFN1010-6

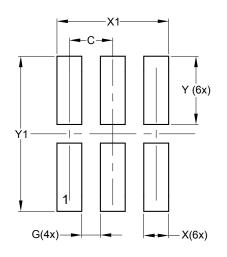


Dimensions	Value (in mm)
С	0.350
G	0.150
G1	0.150
Х	0.200
X1	0.900
Y	0.500
Y1	0.525
Y2	0.475
Y3	1.150



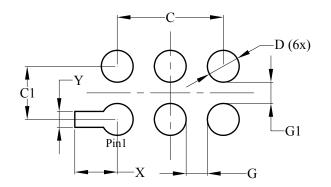
Suggested Pad Layout (cont.)

(6)(Package Type X2-DFN1010-6



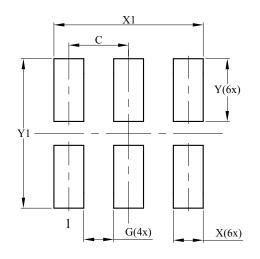
Dimensions	Value (in mm)
С	0.350
G	0.150
Х	0.200
X1	0.900
Y	0.550
Y1	1.250

(7) Package Type: X2-DFN1409-6



Dimensions	Value (in mm)
С	1.000
C1	0.500
D	0.300
G	0.200
G1	0.200
х	0.400
Y	0.150

(8) Package Type: X2-DFN1410-6



Dimensions	Value (in mm)
С	0.500
G	0.250
X	0.250
X1	1.250
Y	0.525
Y1	1.250



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