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# Low-Voltage CMOS Octal Buffer

# With 5 V-Tolerant Inputs and Outputs (3-State, Inverting)

The 74LVC240A is a high performance, inverting octal buffer operating from a 1.2 to 3.6 V supply. High impedance TTL compatible inputs significantly reduce current loading to input drivers while TTL compatible outputs offer improved switching noise performance. A  $V_{\rm I}$  specification of 5.5 V allows 74LVC240A inputs to be safely driven from 5 V devices. The 74LVC240A is suitable for memory address driving and all TTL level bus oriented transceiver applications.

Current drive capability is 24 mA at the outputs. The Output Enable  $(\overline{OE})$  input, when HIGH, disables the outputs by placing them in a HIGH Z condition.

#### **Features**

- Designed for 1.2 V to 3.6 V V<sub>CC</sub> Operation
- 5 V Tolerant Interface Capability With 5 V TTL Logic
- Supports Live Insertion and Withdrawal
- $I_{OFF}$  Specification Guarantees High Impedance When  $V_{CC} = 0 \text{ V}$
- 24 mA Output Sink and Source Capability
- Near Zero Static Supply Current in All Three Logic States (10 μA) Substantially Reduces System Power Requirements
- ESD Performance:
  - ♦ Human Body Model >2000 V
  - ◆ Machine Model >200 V
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant



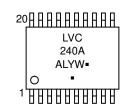
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#### MARKING DIAGRAM



TSSOP-20 DT SUFFIX CASE 948E



A = Assembly Location

L = Wafer Lot Y = Year W = Work Week • Pb-Free Package

(Note: Microdot may be in either location)

#### **ORDERING INFORMATION**

See detailed ordering and shipping information in the package dimensions section on page 7 of this data sheet.

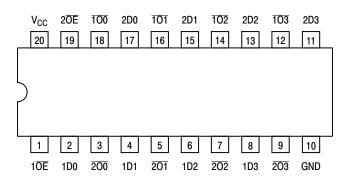
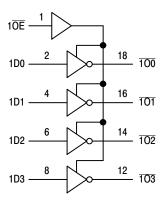


Figure 1. Pinout: 20-Lead (Top View)



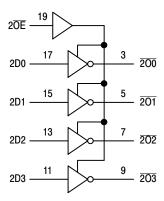


Figure 2. LOGIC DIAGRAM

#### **PIN NAMES**

Pins	Function
nOE	Output Enable Inputs
1Dn, 2Dn	Data Inputs
10n, 20n	3-State Outputs

#### **TRUTH TABLE**

INP	JTS	OUTPUTS
10E 20E	1Dn 2Dn	10n, 20n
L	L	Н
L	Н	L
Н	Х	Z

H = High Voltage Level
L = Low Voltage Level
Z = High Impedance State
X = High or Low Voltage Level

K = High or Low Voltage Level and Transitions Are Acceptable; for I<sub>CC</sub> reasons, DO NOT FLOAT Inputs

#### **MAXIMUM RATINGS**

Symbol	Parameter	Condition	Value	Unit
V <sub>CC</sub>	DC Supply Voltage		-0.5 to +6.5	V
VI	DC Input Voltage		$-0.5 \le V_1 \le +6.5$	V
V <sub>O</sub>	DC Output Voltage	Output in 3-State	$-0.5 \le V_O \le +6.5$	V
		Output in HIGH or LOW State (Note 1)	$-0.5 \le V_{O} \le V_{CC} + 0.5$	V
I <sub>IK</sub>	DC Input Diode Current	V <sub>I</sub> < GND	-50	mA
I <sub>OK</sub>	DC Output Diode Current	V <sub>O</sub> < GND	-50	mA
		V <sub>O</sub> > V <sub>CC</sub>	+50	mA
Io	DC Output Source/Sink Current		±50	mA
I <sub>CC</sub>	DC Supply Current Per Supply Pin		±100	mA
I <sub>GND</sub>	DC Ground Current Per Ground Pin		±100	mA
T <sub>STG</sub>	Storage Temperature Range		-65 to +150	°C
T <sub>L</sub>	Lead Temperature, 1 mm from Case for 10 Seconds		T <sub>L</sub> = 260	°C
TJ	Junction Temperature Under Bias		T <sub>J</sub> = 135	°C
$\theta_{JA}$	Thermal Resistance (Note 2)		110.7	°C/W
MSL	Moisture Sensitivity	Level 1		

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. I<sub>O</sub> absolute maximum rating must be observed.

2. Measured with minimum pad spacing on an FR4 board, using 10 mm-by-1 inch, 2 ounce copper trace no air flow.

#### RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Min	Тур	Max	Units
V <sub>CC</sub>	Supply Voltage Operating Functional	1.65 1.2		3.6 3.6	V
VI	Input Voltage	0		5.5	٧
V <sub>O</sub>	Output Voltage HIGH or LOW State 3-State	0		V <sub>CC</sub> 5.5	V
I <sub>OH</sub>	HIGH Level Output Current $V_{CC} = 3.0 \text{ V} - 3.6 \text{ V}$ $V_{CC} = 2.7 \text{ V} - 3.0 \text{ V}$			-24 -12	mA
I <sub>OL</sub>	LOW Level Output Current V <sub>CC</sub> = 3.0 V - 3.6 V V <sub>CC</sub> = 2.7 V - 3.0 V			24 12	mA
T <sub>A</sub>	Operating Free–Air Temperature	-40		+125	°C
Δt/ΔV	Input Transition Rise or Fall Rate V <sub>CC</sub> = 1.65 V to 2.7 V V <sub>CC</sub> = 2.7 V to 3.6 V	0 0		20 10	ns/V

Functional operation above the stresses listed in the Recommended Operating Ranges is not implied. Extended exposure to stresses beyond the Recommended Operating Ranges limits may affect device reliability.

#### DC ELECTRICAL CHARACTERISTICS

		-40°C to +85°C		5°C	-40	°C to +12	5°C		
Symbol	Parameter	Conditions	Min	Typ (Note 3)	Max	Min	Typ (Note 3)	Max	Unit
VIH	HIGH-level input	V <sub>CC</sub> = 1.2 V	1.08	-	-	1.08	-	-	V
	voltage	V <sub>CC</sub> = 1.65 V to 1.95 V	0.65 x V <sub>CC</sub>	-	-	0.65 x V <sub>CC</sub>	-	-	
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.7	-	-	1.7	-	-	
		V <sub>CC</sub> = 2.7 V to 3.6 V	2.0	_	_	2.0	-	-	
$V_{IL}$	LOW-level input	V <sub>CC</sub> = 1.2 V	-	_	0.12	-	-	0.12	V
	voltage	V <sub>CC</sub> = 1.65 V to 1.95 V	-	_	0.35 x V <sub>CC</sub>	-	-	0.35 x V <sub>CC</sub>	
		V <sub>CC</sub> = 2.3 V to 2.7 V	_	-	0.7	_		0.7	
		V <sub>CC</sub> = 2.7 V to 3.6 V	_	-	0.8	_		8.0	
V <sub>OH</sub>	HIGH-level output	$V_I = V_{IH}$	or V <sub>IL</sub>						٧
	voltage	$I_O = -100 \mu\text{A};$ $V_{CC} = 1.65 \text{V} \text{ to } 3.6 \text{V}$	V <sub>CC</sub> – 0.2	-	-	V <sub>CC</sub> – 0.3	-	-	
		$I_O = -4 \text{ mA}; V_{CC} = 1.65 \text{ V}$	1.2	-	-	1.05	-	-	
		$I_{O} = -8 \text{ mA}; V_{CC} = 2.3 \text{ V}$	1.8	-	-	1.65	-	-	
		$I_O = -12 \text{ mA}; V_{CC} = 2.7 \text{ V}$	2.2	-	-	2.05		-	
		$I_O = -18 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.4	-	-	2.25		-	
		$I_O = -24 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.2	-	-	2.0		-	
VOL	LOW-level output	$V_I = V_{IH}$	or V <sub>IL</sub>						٧
	voltage	$I_O = 100 \ \mu A;$ $V_{CC} = 1.65 \ V \ to \ 3.6 \ V$	-	_	0.2	-	-	0.3	
		I <sub>O</sub> = 4 mA; V <sub>CC</sub> = 1.65 V	-	-	0.45	-	-	0.65	
		$I_O = 8 \text{ mA}; V_{CC} = 2.3 \text{ V}$	-	-	0.6	_	-	8.0	
		$I_{O} = 12 \text{ mA}; V_{CC} = 2.7 \text{ V}$	ı	-	0.4	-	-	0.6	
		$I_O = 24 \text{ mA}; V_{CC} = 3.0 \text{ V}$	ı	_	0.55	_	-	8.0	
l <sub>l</sub>	Input leakage current	$V_I = 5.5V$ or GND $V_{CC} = 3.6 V$	ı	±0.1	±5	-	±0.1	±20	μΑ
l <sub>OZ</sub>	OFF-state output current	VI = VIH  or  VIL; $V_O = 5.5 \text{ V or GND}; V_{CC} = 3.6 \text{ V}$	-	±0.1	±5	_	±0.1	±20	μΑ
l <sub>OFF</sub>	Power-off leakage current	$V_{I}$ or $V_{O} = 5.5$ V; $V_{CC} = 0.0$ V	1	±0.1	±10	-	±0.1	±20	μΑ
I <sub>CC</sub>	Supply current	$V_I = V_{CC}$ or GND; $I_O = 0$ A; $V_{CC} = 3.6 \text{ V}$	-	0.1	10	_	0.1	40	μΑ
$\Delta I_{CC}$	Additional supply current	per input pin; V <sub>I</sub> = V <sub>CC</sub> - 0.6 V; I <sub>O</sub> = 0 A; V <sub>CC</sub> = 2.7 V to 3.6 V	-	5	500	_	5	5000	μΑ

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

3. All typical values are measured at  $T_A = 25^{\circ}C$  and  $V_{CC} = 3.3$  V, unless stated otherwise.

#### AC ELECTRICAL CHARACTERISTICS ( $t_R = t_F = 2.5 \text{ ns}$ )

			-40	°C to +8	5°C	–40°	°C to +12	25°C	
Symbol	Parameter	Conditions	Min	Typ <sup>1</sup>	Max	Min	Typ <sup>1</sup>	Max	Unit
t <sub>pd</sub>	Propagation Delay (Note 5)	V <sub>CC</sub> = 1.2 V	_	16.0	-	-	-	-	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	1.0	5.7	12.7	1.0	_	14.6	
		V <sub>CC</sub> = 2.3 V to 2.7 V	0.5	3.0	6.6	0.5	-	7.6	
		V <sub>CC</sub> = 2.7 V	1.5	3.1	7.0	1.5	-	9.0	
		V <sub>CC</sub> = 3.0 V to 3.6 V	1.3	2.6	5.5	1.3	-	7.0	
t <sub>en</sub>	Enable Time (Note 6)	V <sub>CC</sub> = 1.2 V	_	19.0	-	-	-	_	ns
	nOE to nOn	V <sub>CC</sub> = 1.65 V to 1.95 V	1.5	6.3	15.9	1.5	-	18.3	
		$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$	1.5	3.6	8.8	1.5	-	10.1	
		V <sub>CC</sub> = 2.7 V	1.0	3.7	8.5	1.0	-	11.0	
		$V_{CC} = 3.0 \text{ V to } 3.6 \text{ V}$	1.1	2.9	7.0	1.1	-	9.0	
t <sub>dis</sub>	Disable Time (Note 7)	V <sub>CC</sub> = 1.2 V	_	17.0	-	-	-	_	ns
	INOE to NOTI	V <sub>CC</sub> = 1.65 V to 1.95 V	2.3	4.1	9.9	2.3	-	11.4	
		$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$	1.0	3.4	5.6	1.0	-	6.5	
		V <sub>CC</sub> = 2.7 V	1.5	3.1	7.5	1.5	-	9.5	
		V <sub>CC</sub> = 3.0 V to 3.6 V	1.4	2.9	6.0	1.4	-	7.5	
t <sub>sk(0)</sub>	Output Skew Time (Note 8)		-	-	1.0	-	-	1.5	ns

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

- 4. Typical values are measured at TA = 25°C and Vcc = 3.3 V, unless stated otherwise.
- 5. t<sub>pd</sub> is the same as t<sub>PLH</sub> and t<sub>PHL</sub>.
- 6. ten is the same as tPZL and tPZH.
- 7.  $t_{dis}$  is the same as  $t_{PLZ}$  and  $t_{PHZ}$ .
- 8. Skew between any two outputs of the same package switching in the same direction. This parameter is guaranteed by design.

#### **DYNAMIC SWITCHING CHARACTERISTICS**

			T <sub>A</sub> = +25°C			
Symbol	Characteristic	Condition	Min	Тур	Max	Unit
V <sub>OLP</sub>	Dynamic LOW Peak Voltage (Note 9)	$\begin{array}{c} V_{CC} = 3.3 \text{ V, } C_L = 50 \text{ pF, } V_{IH} = 3.3 \text{ V, } V_{IL} = 0 \text{ V} \\ V_{CC} = 2.5 \text{ V, } C_L = 30 \text{ pF, } V_{IH} = 2.5 \text{ V, } V_{IL} = 0 \text{ V} \end{array}$		0.8 0.6		V
V <sub>OLV</sub>	Dynamic LOW Valley Voltage (Note 9)	$\begin{array}{c} V_{CC} = 3.3 \text{ V, } C_L = 50 \text{ pF, } V_{IH} = 3.3 \text{ V, } V_{IL} = 0 \text{ V} \\ V_{CC} = 2.5 \text{ V, } C_L = 30 \text{ pF, } V_{IH} = 2.5 \text{ V, } V_{IL} = 0 \text{ V} \end{array}$		-0.8 -0.6		V

<sup>9.</sup> Number of outputs defined as "n". Measured with "n-1" outputs switching from HIGH-to-LOW or LOW-to-HIGH. The remaining output is measured in the LOW state.

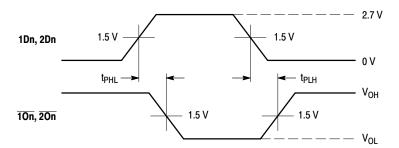
#### **CAPACITIVE CHARACTERISTICS**

Symbol	Parameter	Condition	Typical	Unit	
CIN	Input Capacitance	$V_{CC} = 3.3 \text{ V}, V_{I} = 0 \text{ V or } V_{CC}$	5.0	pF	
Соит	Output Capacitance	$V_{CC} = 3.3 \text{ V}, V_I = 0 \text{ V or } V_{CC}$	7.0	pF	
$C_{PD}$	Power Dissipation Capacitance	Per input; V <sub>I</sub> = GND or V <sub>CC</sub>			
	(Note 10)	V <sub>CC</sub> = 1.65 V to 1.95 V	2.0	1	
		V <sub>CC</sub> = 2.3 V to 2.7 V	5.2	1	
		V <sub>CC</sub> = 3.0 V to 3.6 V	8.1	1	

<sup>10.</sup>  $C_{PD}$  is used to determine the dynamic power dissipation ( $P_D$  in  $\mu W$ ).

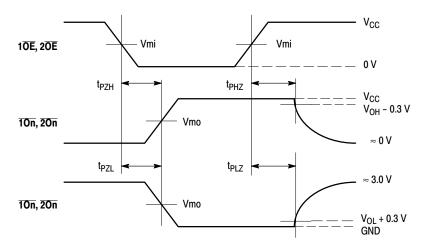
P<sub>D</sub> = C<sub>PD</sub> x V<sub>CC</sub><sup>2</sup> x fi x N +  $\Sigma$  (C<sub>L</sub> x V<sub>CC</sub><sup>2</sup> x fo) where: fi = input frequency in MHz; fo = output frequency in MHz C<sub>L</sub> = output load capacitance in pF V<sub>CC</sub> = supply voltage in Volts N = number of outputs switching

 $<sup>\</sup>Sigma(C_L \times V_{CC}^2 \times fo) = \text{sum of the outputs.}$ 



#### **WAVEFORM 1 - PROPAGATION DELAYS**

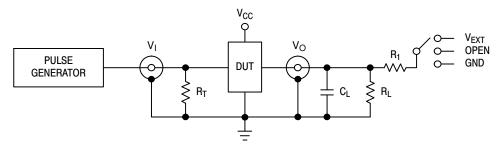
 $t_R = t_F = 2.5 \text{ ns}, 10\% \text{ to } 90\%; f = 1 \text{ MHz}; t_W = 500 \text{ ns}$ 



## WAVEFORM 2 – OUTPUT ENABLE AND DISABLE TIMES $t_R=t_F=2.5~\text{ns},~10\%$ to $90\%;~f=1~\text{MHz};~t_W=500~\text{ns}$

Figure 3. AC Waveforms

	V <sub>CC</sub>						
Symbol	3.3 V $\pm$ 0.3 V	2.7 V	V <sub>CC</sub> < 2.7 V				
Vmi	1.5 V	1.5 V	V <sub>CC</sub> /2				
Vmo	1.5 V	1.5 V	V <sub>CC</sub> /2				
V <sub>HZ</sub>	V <sub>OL</sub> + 0.3 V	V <sub>OL</sub> + 0.3 V	V <sub>OL</sub> + 0.15 V				
$V_{LZ}$	V <sub>OH</sub> – 0.3 V	V <sub>OH</sub> – 0.3 V	V <sub>OH</sub> – 015 V				



 $C_L$  includes jig and probe capacitance  $R_T$  =  $Z_{OUT}$  of pulse generator (typically 50  $\Omega)$   $R_1$  =  $R_L$ 

Supply Voltage	Inp	out	Load		V <sub>EXT</sub>		
V <sub>CC</sub> (V)	V <sub>I</sub>	t <sub>r</sub> , t <sub>f</sub>	CL	R <sub>L</sub>	t <sub>PLH</sub> , t <sub>PHL</sub>	t <sub>PLZ</sub> , t <sub>PZL</sub>	t <sub>PHZ</sub> , t <sub>PZH</sub>
1.2	V <sub>CC</sub>	≤ 2 ns	30 pF	1 kΩ	Open	2 x V <sub>CC</sub>	GND
1.65 – 1.95	V <sub>CC</sub>	≤ 2 ns	30 pF	1 kΩ	Open	2 x V <sub>CC</sub>	GND
2.3 – 2.7	V <sub>CC</sub>	≤ 2 ns	30 pF	500 Ω	Open	2 x V <sub>CC</sub>	GND
2.7	2.7 V	≤ 2.5 ns	50 pF	500 Ω	Open	2 x V <sub>CC</sub>	GND
3 – 3.6	2.7 V	≤ 2.5 ns	50 pF	500 Ω	Open	2 x V <sub>CC</sub>	GND

Figure 4. Test Circuit

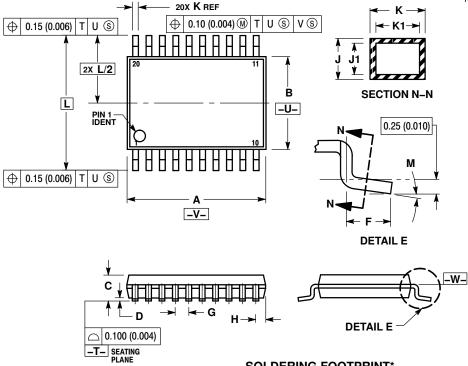
### **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
74LVC240ADTR2G	TSSOP-20 (Pb-Free)	2500 / Tape & Reel

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

#### PACKAGE DIMENSIONS

#### TSSOP-20 CASE 948E-02 **ISSUE C**



- NOTES:

  1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
  2. CONTROLLING DIMENSION:
  MILLIMETER.
  3. DIMENSION A DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS. MOLD FLASH OR GATE BURRS SHALL NOT EXCEED 0.15 (0.006) PER SIDE.
  4. DIMENSION B DOES NOT INCLUDE INTERLEAD FLASH OR PROTRUSION. INTERLEAD FLASH OR PROTRUSION. SHALL NOT EXCEED 0.25 (0.010) PER SIDE.
  5. DIMENSION K DOES NOT INCLUDE

  - SHALL NOT EXCEED 0.25 (0.101) PER S
    5. DIMENSION K DOES NOT INCLUDE
    DAMBAR PROTRUSION. ALLOWABLE
    DAMBAR PROTRUSION SHALL BE 0.08
    (0.003) TOTAL IN EXCESS OF THE K
    DIMENSION AT MAXIMUM MATERIAL
    CONDITION
  - CONDITION.

    6. TERMINAL NUMBERS ARE SHOWN FOR REFERENCE ONLY.

    7. DIMENSION A AND B ARE TO BE DETERMINED AT DATUM PLANE —W.—.

DETENTION TO AT DATON TEAME - VI								
	MILLIN	IETERS	INCHES					
DIM	MIN	MAX	MIN	MAX				
Α	6.40	6.60	0.252	0.260				
В	4.30	4.50	0.169	0.177				
С		1.20		0.047				
D	0.05	0.15	0.002	0.006				
F	0.50	0.75	0.020	0.030				
G	0.65	BSC	0.026 BSC					
Н	0.27	0.37	0.011	0.015				
J	0.09	0.20	0.004	0.008				
J1	0.09	0.16	0.004	0.006				
K	0.19	0.30	0.007	0.012				
K1	0.19	0.25	0.007	0.010				
L	6.40	BSC	0.252	BSC				

M 0° 8° 0° 8°

# **SOLDERING FOOTPRINT\*** 7.06 0.65 **PITCH** 16X 0.36 16X 1.26 DIMENSIONS: MILLIMETERS

\*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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