# imall

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74LVC2G06

#### Description

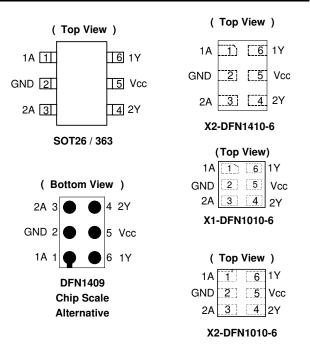
The 74LVC2G06 is a dual inverter gate with open drain outputs. The device is designed for operation with a power supply range of 1.65V to 5.5V. The input is tolerant to 5.5V allowing this device to be used in a mixed voltage environment. The device is fully specified for partial power down applications using  $I_{OFF}$ . The  $I_{OFF}$  circuitry disables the output preventing damaging current backflow when the device is powered down. The open-drain output can be connected to other open drain outputs to implement active-low wired-OR or active-high wired-AND functions. The maximum sink current is 32mA.

#### Features

- Wide Supply Voltage Range from 1.65V to 5.5V
- -24mA Output Drive at 3.0V
- CMOS Low Power Consumption
- IOFF Supports Partial-Power-Down Mode Operation
- Inputs Accept up to 5.5V
- ESD Protection Tested per JESD 22
  - Exceeds 200-V Machine Model (A115)
  - Exceeds 2000-V Human Body Model (A114)
  - Exceeds 1000-V Charged Device Model (C101)
- Latch-Up Exceeds 100mA per JESD 78, Class I
- DFN1409 package designed as a direct replacement for chip scale packaging.
- Range of Package Options SOT26, SOT363, X1-DFN1010-6, X2-DFN1010-6, X2-DFN1409-6, and X2-DFN1410-6
- Leadless Packages Named per JESD30E
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

#### **DUAL INVERTERS with OPEN DRAIN OUTPUTS**

#### Pin Assignments



#### Applications

- Voltage Level Shifting
- General Purpose Logic
- Power Down Signal Isolation
  - Wide array of products such as:
    - PCs, Networking, Notebooks, Netbooks, Tablets
    - Computer Peripherals, Hard Drives, SSD, CD/DVD ROM
    - TV, DVD, DVR, Set-Top Box
    - Cell Phones, Personal Navigation / GPS
    - MP3 Players, Cameras, Video Recorders

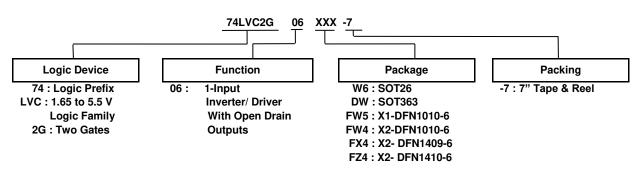
Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.



#### **Ordering Information**



Device	Package	Package	Package	7" Tape and	Reel (Note 5)
Device	Code	(Note 4)	Size	Quantity	Part Number Suffix
74LVC2G06W6-7	W6	SOT26	2.8mm X 2.2 mm X 1.1mm 0.95 mm lead pitch	3,000/Tape & Reel	-7
74LVC2G06DW-7	DW	SOT363	2.0mm X 2.0mm X 1.1mm 0.65 mm lead pitch	3,000/Tape & Reel	-7
74LVC2G06FW5-7	FW5	X1-DFN1010-6	1.0mm X 1.0mm X 0.5mm 0.35 mm pad pitch	5,000/Tape & Reel	-7
74LVC2G06FW4-7	FW4	X2-DFN1010-6	1.0mm X 1.0mm X 0.4mm 0.35 mm pad pitch	5,000/Tape & Reel	-7
74LVC2G06FX4-7	FX4	X2-DFN1409-6 Chip Scale Alternative	1.4mm X 0.9mm X 0.4mm 0.5 mm pad pitch	5,000/Tape & Reel	-7
74LVC2G06FZ4-7	FZ4	X2-DFN1410-6	1.4mm X 1.0mm X 0.4mm 0.5 mm pad pitch	5,000/Tape & Reel	-7

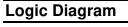
Notes: 4. Pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at

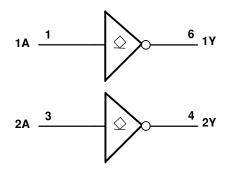
http://www.diodes.com/datasheets/ap02001.pdf.

5. The taping orientation is located on our website at http://www.diodes.com/datasheets/ap02007.pdf.

## **Pin Descriptions**

Pin Name	Pin.	Function
1A	1	Data Input
GND	2	Ground
2A	3	Data Input
2Y	4	Data Output Open Drain
V <sub>CC</sub>	5	Supply Voltage
1Y	6	Data Output Open Drain





#### **Function Table**

Inputs	Output
Α	Y
Н	L
L	Z



Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	kV
ESD CDM	Charged Device Model ESD Protection	1	kV
ESD MM	Machine Model ESD Protection	200	V
V <sub>CC</sub>	Supply Voltage Range	-0.5 to +6.5	V
VI	Input Voltage Range	-0.5 to +6.5	V
Vo	Voltage Applied to Output in High Impedance or IOFF State	-0.5 to +6.5	V
Vo	Voltage Applied to Output in High or Low State	-0.3 to V <sub>CC</sub> +0.5	V
I <sub>IK</sub>	Input Clamp Current VI < 0	-50	mA
I <sub>OK</sub>	Output Clamp Current V <sub>O</sub> < 0	-50	mA
lo	Continuous Output Current	-50	mA
	Continuous Current Through V <sub>DD</sub> or GND	±100	mA
TJ	Operating Junction Temperature	-40 to +150	°C
T <sub>STG</sub>	Storage Temperature	-65 to +150	°C

#### Absolute Maximum Ratings (Notes 6 & 7) (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Note

6. Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.7. Forcing the maximum allowed voltage could cause a condition exceeding the maximum current or conversely forcing the maximum current could cause a condition exceeding the maximum voltage. The ratings of both current and voltage must be maintained within the controlled range.

#### Recommended Operating Conditions (Note 8) (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Symbol		Parameter	Min	Max	Unit
N/		Operating	1.65	5.5	V
Vcc	Operating Voltage	Data Retention Only	1.5	—	V
		V <sub>CC</sub> = 1.65V to 1.95V	0.65 X V <sub>CC</sub>	_	
M	High Lovel Input Veltage	V <sub>CC</sub> = 2.3V to 2.7V	1.7	—	V
V <sub>IH</sub>	High-Level Input Voltage	V <sub>CC</sub> = 3V to 3.6V	2	—	v
		V <sub>CC</sub> = 4.5V to 5.5V	0.7 X V <sub>CC</sub>	—	
		V <sub>CC</sub> = 1.65V to 1.95V	—	0.35 X V <sub>CC</sub>	
V		V <sub>CC</sub> = 2.3V to 2.7V	—	0.7	v
VIL	Low-Level Input Voltage	V <sub>CC</sub> = 3V to 3.6V	—	0.8	
		V <sub>CC</sub> = 4.5V to 5.5V	_	0.3 X V <sub>CC</sub>	
VI	Input Voltage		0	5.5	V
Vo	Output Voltage		0	V <sub>CC</sub>	V
		V <sub>CC</sub> = 1.65V	—	4	
		V <sub>CC</sub> = 2.3V	_	8	
I <sub>OL</sub>	Low-Level Output Current		—	16	mA
		$V_{CC} = 3V$	_	24	
		V <sub>CC</sub> = 4.5V		32	
		V <sub>CC</sub> = 1.8V ± 0.15V, 2.5V ± 0.2V		20	
Δt/ΔV	Input Transition Rise or Fall Rate	$V_{CC} = 3.3V \pm 0.3V$		10	ns/V
		$V_{CC} = 5V \pm 0.5V$		10	
TA	Operating Free-Air Temperature		-40	+125	°C

Note: 8. Unused inputs should be held at V<sub>CC</sub> or Ground.



## **Electrical Characteristics**

Cumhal	Deveryoter	Test Canditions	V	-40°C t	o +85°C	-40°C to	+125°C	
Symbol Parameter		Test Conditions	V <sub>cc</sub>	Min	Max	Min	Max	Unit
		I <sub>OL</sub> = 100μA	1.65V to 5.5V	_	0.1	_	0.1	
		$I_{OL} = 4mA$	1.65V	_	0.45	_	0.70	
N/	Low-Level Output	I <sub>OL</sub> = 8mA	2.3V	_	0.3	_	0.45	v
V <sub>OL</sub>	Voltage	I <sub>OL</sub> = 16mA	2)/	_	0.4	_	0.60	v
		I <sub>OL</sub> = 24mA	3V		0.55	_	0.80	
		I <sub>OL</sub> = 32mA	4.5V		0.55	_	0.80	
l <sub>l</sub>	Input Current	V <sub>I</sub> = 5.5V or GND	0 to 5.5V	_	± 5	_	± 20	μA
loz	Z State Leakage Current	V <sub>O</sub> = 0 to 5.5V	3.6V	_	± 10	_	± 10	μA
I <sub>OFF</sub>	Power Down Leakage Current	$V_1 \text{ or } V_0 = 5.5 V$	0V	_	± 10	_	± 20	μA
Icc	Supply Current	$V_I = 5.5V$ or GND, $I_O = 0$	1.65V to 5.5V	—	10		40	μA
ΔI <sub>CC</sub>	Additional Supply Current	Input at V <sub>CC</sub> -0.6V	3V to 5.5V		500	_	5000	μA

#### Package Characteristics (All typical values are at V<sub>CC</sub> = 3.3V, T<sub>A</sub> = +25°C.)

Symbol	Parameter	Package	Conditions	Min	Тур	Max	Unit
Cı	Input Capacitance	Typical of All Packages	Vcc = 3.3V $V_I = V_{CC}$ or GND		3.5		pF
		SOT26			204	_	
		SOT363			371	_	
0	The word Desistence, hunsting to Ambient	X2-DFN1410-6	(Ninta O)		430	_	0000
θ <sub>JA</sub> ΤΙ	Thermal Resistance Junction-to-Ambient	X2-DFN1409-6	(Note 9)		450	_	°C/W
		X1-DFN1010-6			495	_	
		X2-DFN1010-6			510	_	
		SOT26			52	_	
		SOT363			143	_	
		X2-DFN1410-6			190	—	
θ <sub>JC</sub>	Thermal Resistance Junction-to-Case	X2-DFN1409-6	(Note 9)		225	—	°C/W
		X1-DFN1010-6			245	—	
		X2-DFN1010-6			250	_	

Note: 9. Test condition for all packages: Device mounted on FR-4 substrate PC board, 2oz copper with minimum recommended pad layout.

## Switching Characteristics

<b>T<sub>A</sub> = -40°C to +85°C</b> , C <sub>L</sub> = 30 or 50pF (See Figure 1)											
Parameter	From (Input)	TO (OUTPUT)	••	= 1.8V .15V	••	= 2.5V ).2V	••	= 3.3V ).3V	••	= 5V ).5V	Unit
	(input)	(001201)	Min	Max	Min	Max	Min	Max	Min	Max	ĺ
t <sub>pd</sub>	А	Y	0.5	6.5	0.5	3.9	0.5	3.4	0.5	2.9	ns

T<sub>A</sub> = -40°C to +125°C, C<sub>L</sub> = 30 or 50pF (See Figure 1)

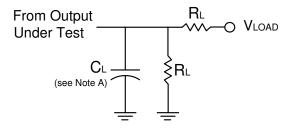
Parameter	From (Input)	TO (OUTPUT)		: 1.8V .15V		= 2.5V ).2V		= 3.3V ).3V	V <sub>CC</sub> ±0	= 5V 0.5V	Unit
	(input)		Min	Max	Min	Max	Min	Max	Min	Max	
t <sub>pd</sub>	А	Y	0.5	8.2	0.5	4.9	0.5	4.3	0.5	3.7	ns



#### **Operating Characteristics**

$T_A = +25^{\circ}C$								
Parameter	Test	V <sub>CC</sub> = 1.8V	V <sub>CC</sub> = 2.5V	$V_{CC} = 3.3V$	V <sub>CC</sub> = 5V	Unit		
	Falameter	Conditions	Тур Тур		Тур	Тур	Onit	
C <sub>pd</sub>	Power Dissipation Capacitance	f = 10 MHz	3	3	4	6	pF	

#### **Parameter Measurement Information**



TEST	Condition
t <sub>PLZ</sub> (See Notes D and E)	Vload
t <sub>PZL</sub> (See Notes D and F)	Vload

V <sub>cc</sub>	Inp	outs	V	М	•		MA
	VI	t <sub>r</sub> /t <sub>f</sub>	V <sub>M</sub>	V <sub>LOAD</sub>	CL	RL	VΔ
1.8V±0.15V	Vcc	≤2ns	V <sub>CC</sub> /2	2 X V <sub>CC</sub>	30pF	1kΩ	0.15V
2.5V±0.2V	V <sub>CC</sub>	≤2ns	V <sub>CC</sub> /2	2 X V <sub>CC</sub>	30pF	500Ω	0.15V
3.3V±0.3V	3V	≤2.5ns	1.5 V	6 V	50pF	500Ω	0.3V
5V±0.5V	V <sub>CC</sub>	≤2.5ns	V <sub>CC</sub> /2	2 X V <sub>CC</sub>	50pF	500Ω	0.3V

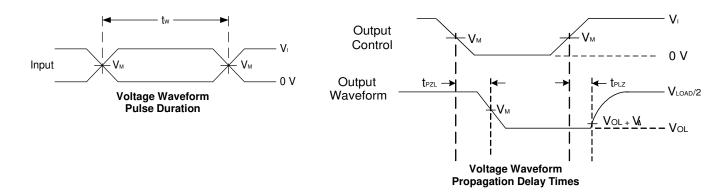


Figure 1 Load Circuit and Voltage Waveforms

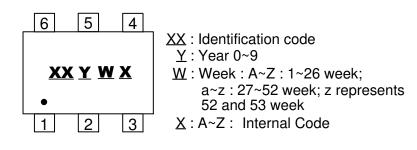
Notes: A. Includes test lead and test apparatus capacitance.

- B. All pulses are supplied at pulse repetition rate ≤ 10 MHz
- C. The inputs are measured one at a time with one transition per measurement.
- D. For the open drain device  $t_{PLZ}$  and  $t_{PZL}$  are the same as  $t_{PD}$ .
- E. t<sub>PZL</sub> is measured at V<sub>M</sub>.
- F.  $t_{PLZ}\,$  is measured at  $V_{OL}$  +V\_{\Delta}.



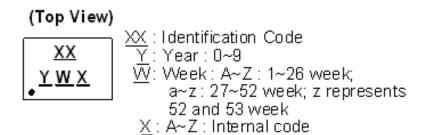
#### **Marking Information**

#### (1) SOT26, SOT363



Part Number	Package	Identification Code
74LVC2G06W6-7	SOT26	Z3
74LVC2G06DW-7	SOT363	Z3

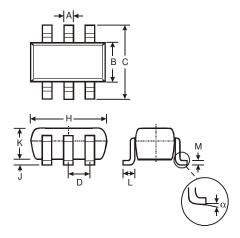
(2) X1-DFN1010-6, X2-DFN1010-6, X2-DFN1409-6, X2-DFN1410-6



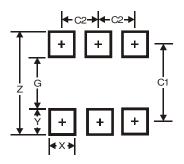
Part Number	Package	Identification Code
74LVC2G06FW4-7	X2-DFN1010-6	Z3
74LVC2G06FW5-7	X1-DFN1010-6	W3
74LVC2G06FX4-7	X2-DFN1409-6	X3
74LVC2G06FZ4-7	X2-DFN1410-6	Z3



## SOT26 Package Outline Dimensions and Suggested Pad Layout



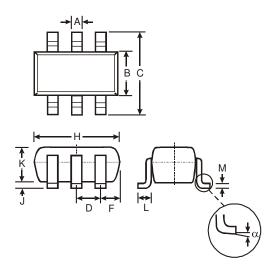
Dim A B	SO1 Min 0.35 1.50	<b>26</b> <b>Max</b> 0.50	<b>Typ</b>
Α	0.35		
		0.50	0 38
R	1 50		0.00
	1.50	1.70	1.60
С	2.70	3.00	2.80
D	_	_	0.95
Η	2.90	3.10	3.00
ſ	0.013	0.10	0.05
Κ	1.00	1.30	1.10
L	0.35	0.55	0.40
М	0.10	0.20	0.15
α	0°	8°	_
All Dimensions in mm			



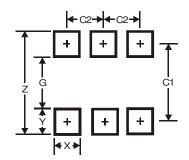
Dimensions	Value (in mm)
Z	3.20
G	1.60
Х	0.55
Y	0.80
C1	2.40
C2	0.95



## SOT363 Package Outline Dimensions and Suggested Pad Layout



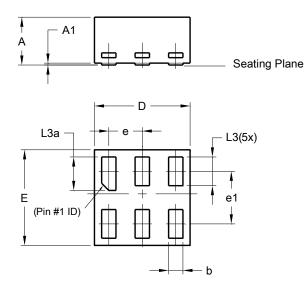
	SOT363				
Dim	Min	Max	Тур		
Α	0.10	0.30	0.25		
В	1.15	1.35	1.30		
С	2.00	2.20	2.10		
D	0.65 Typ				
F	0.40	0.45	0.425		
Н	1.80	2.20	2.15		
J	0	0.10	0.05		
Κ	0.90	1.00	1.00		
L	0.25	0.40	0.30		
Μ	0.10	0.22	0.11		
α	0°	8°	-		
All	All Dimensions in mm				



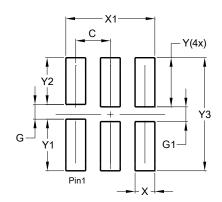
Dimensions	Value (in mm)	
Z	2.5	
G	1.3	
Х	0.42	
Y	0.6	
C1	1.9	
C2	0.65	



#### X1-DFN1010-6 (Type B) Package Outline Dimensions and Suggested Pad Layout



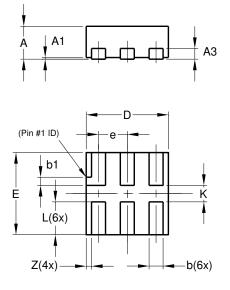
X1-DFN1010-6 (Type B)					
Dim	Min				
Α	-	0.50	0.39		
A1	-	0.04	-		
b	0.12	0.20	0.15		
D	0.95	1.050	1.00		
E	0.95	1.050	1.00		
е	0.35 BSC				
e1	0.55 BSC				
L3	0.27	0.30	0.30		
L3a	0.32	0.40	0.35		
All	All Dimensions in mm				



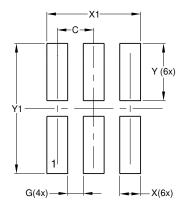
Dimensions	Value (in mm)
С	0.350
G	0.150
G1	0.150
Х	0.200
X1	0.900
Y	0.500
Y1	0.525
Y2	0.475
Y3	1.150



## X2-DFN1010-6 Package Outline Dimensions and Suggested Pad Layout



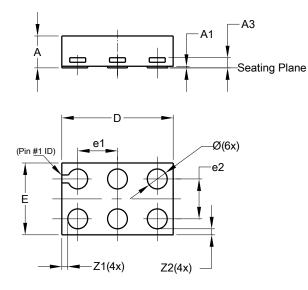
	X2-DFN1010-6				
Dim	Min	Max	Тур		
Α		0.40	0.39		
A1	0.00	0.05	0.02		
A3			0.13		
b	0.14	0.20	0.17		
b1	0.05	0.15	0.10		
D	0.95	1.05	1.00		
Е	0.95	1.05	1.00		
е			0.35		
L	0.35	0.45	0.40		
К	0.15				
Z			0.065		
All	All Dimensions in mm				



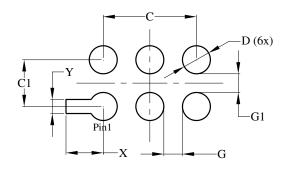
Dimensions	Value (in mm)	
С	0.350	
G	0.150	
Х	0.200	
X1	0.900	
Y	0.550	
Y1	1.250	



## X2-DFN1409-6 Package Outline Dimensions and Suggested Pad Layout



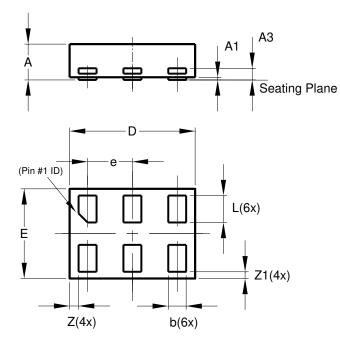
)	X2-DFN1409-6			
Dim	Min	Max	Тур	
Α	Ι	0.40	0.39	
A1	0	0.05	0.02	
A3	_		0.13	
Ø	0.20	0.30	0.25	
D	1.35	1.45	1.40	
Е	0.85	0.95	0.90	
e1		I	0.50	
e2	_	I	0.50	
Z1	_		0.075	
Z2		_	0.075	
All Dimensions in mm				



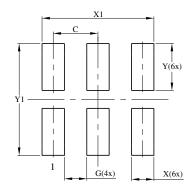
Dimensions	Value (in mm)
С	1.000
C1	0.500
D	0.300
G	0.200
G1	0.200
Х	0.400
Y	0.150



#### X2-DFN1410-6 Package Outline Dimensions and Suggested Pad Layout



X2-DFN1410-6				
Dim	Min	Max	Тур	
Α		0.40	0.39	
A1	0.00	0.05	0.02	
A3			0.13	
b	0.15	0.25	0.20	
D	1.35	1.45	1.40	
Е	0.95	1.05	1.00	
е			0.50	
L	0.25	0.35	0.30	
Z	_	_	0.10	
Z1	0.045	0.105	0.075	
All Dimensions in mm				



Dimensions	Value (in mm)	
С	0.500	
G	0.250	
Х	0.250	
X1	1.250	
Y	0.525	
Y1	1.250	



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