

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China













Description

The 74LVC2G17 is a dual Schmitt trigger buffer gate with standard push-pull outputs. The device is designed for operation with a power supply range of 1.65V to 5.5V. The inputs are tolerant to 5.5V allowing this device to be used in a mixed voltage environment. The device is fully specified for partial power down applications using $I_{\rm OFF}$. The $I_{\rm OFF}$ circuitry disables the output preventing damaging current backflow when the device is powered down.

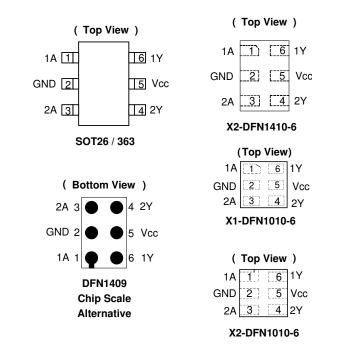
The gate performs the positive Boolean function:

Y = A

Features

- Wide Supply Voltage Range from 1.65V to 5.5V
- ±24mA Output Drive at 3.0V
- CMOS Low Power Consumption
- IOFF Supports Partial-Power-Down Mode Operation
- Inputs Accept up to 5.5V
- ESD Protection Tested per JESD 22
 - Exceeds 200-V Machine Model (A115)
 - Exceeds 2000-V Human Body Model (A114)
 - Exceeds 1000-V Charged Device Model (C101)
- Latch-Up Exceeds 100mA per JESD 78, Class I
- DFN1409 Package Designed as a Direct Replacement for Chip Scale Packaging
- Range of Package Options SOT26, SOT363, X1-DFN1010-6, X2-DFN1010-6, X2-DFN1409-6, and X2-DFN1410-6
- Leadless Packages Named per JESD30E
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Pin Assignments



Applications

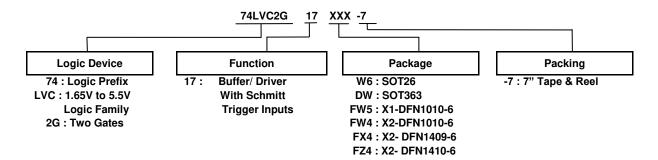
- Voltage Level Shifting
- General Purpose Logic
- Power Down Signal Isolation
- Wide Array of Products Such As:
 - PCs, Networking, Notebooks, Netbooks, Tablets
 - Computer Peripherals, Hard Drives, SSD, CD/DVD ROM
 - TV, DVD, DVR, Set Top Box
 - Cell Phones, Personal Navigation / GPS
 - MP3 Players ,Cameras, Video Recorders

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.



Ordering Information



| Part Number | Package Code | Package | Package | 7" Tape and | Reel (Note 5) |
|----------------|--|--------------|--|------------------|--------------------|
| Part Number | Fackage Code | (Note 4) | Size | Quantity | Part Number Suffix |
| 74LVC2G17W6-7 | W6 | SOT26 | 2.8mm X 2.2 mm X 1.1mm 0.95 mm lead pitch | 3000/Tape & Reel | -7 |
| 74LVC2G17DW-7 | DW | SOT363 | 2.0mm X 2.0mm X 1.1mm 0.65 mm lead pitch | 3000/Tape & Reel | -7 |
| 74LVC2G17FW5-7 | FW5 | X1-DFN1010-6 | 1.0mm X 1.0mm X 0.5mm 0.35 mm pad pitch | 5000/Tape & Reel | -7 |
| 74LVC2G17FW4-7 | FW4 | X2-DFN1010-6 | 1.0mm X 1.0mm X 0.4mm 0.35 mm pad pitch | 5000/Tape & Reel | -7 |
| 74LVC2G17FX4-7 | 74LVC2G17FX4-7 FX4 X2-DFN14 Chip Scale Al | | 1.4mm X 0.9mm X 0.4mm 0.5 mm pad pitch | 5000/Tape & Reel | -7 |
| 74LVC2G17FZ4-7 | FZ4 | X2-DFN1410-6 | 1.4mm X 1.0mm X 0.4mm 0.5 mm pad pitch | 5000/Tape & Reel | -7 |

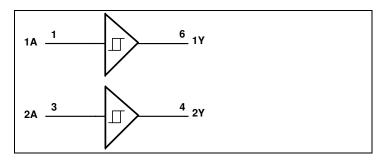
Notes: 4. Pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.

5. The taping orientation is located on our website at http://www.diodes.com/datasheets/ap02007.pdf.

Pin Descriptions

| Pin Name | Pin Number | Function | |
|-----------------|------------|----------------|--|
| 1A | 1 | Data Input | |
| GND | 2 | Ground | |
| 2A | 3 | Data Input | |
| 2Y | 4 | Data Output | |
| V _{CC} | 5 | Supply Voltage | |
| 1Y | 6 | Data Output | |

Logic Diagram



Function Table

| Inputs | Output |
|--------|--------|
| Α | Υ |
| Н | Н |
| L | L |



Absolute Maximum Ratings (Notes 6 and 7) (@T_A = +25°C, unless otherwise specified.)

| Symbol | Parameter | Rating | Unit |
|------------------|---|------------------------------|------|
| ESD HBM | Human Body Model ESD Protection | 2 | kV |
| ESD CDM | Charged Device Model ESD Protection | 1 | kV |
| ESD MM | Machine Model ESD Protection | 200 | V |
| V_{CC} | Supply Voltage Range | -0.5 to +6.5 | V |
| VI | Input Voltage Range | -0.5 to +6.5 | V |
| Vo | Voltage Applied to Output in High Impedance or I _{OFF} State | -0.5 to +6.5 | V |
| Vo | Voltage Applied to Output in High or Low State | -0.3 to V _{CC} +0.5 | V |
| l _{IK} | Input Clamp Current V _I < 0 | -50 | mA |
| lok | Output Clamp Current V _O < 0 | -50 | mA |
| Io | Continuous Output Current | -50 | mA |
| _ | Continuous Current Through V _{DD} or GND | ±100 | mA |
| TJ | Operating Junction Temperature | -40 to +150 | °C |
| T _{STG} | Storage Temperature | -65 to +150 | °C |

Note

Recommended Operating Conditions (Note 8) (@T_A = +25°C, unless otherwise specified.)

| Symbol | | Parameter | Min | Max | Unit | |
|-----------------|------------------------------------|---|------|-----------------|------|--|
| \/ | Operating Voltage | Operating | 1.65 | 5.5 | V | |
| V _{CC} | Operating Voltage | Data retention only | 1.5 | _ | V | |
| VI | Input Voltage | | 0 | 5.5 | V | |
| Vo | Output Voltage | | 0 | V _{CC} | V | |
| | | V _{CC} = 1.65V | _ | -4 | | |
| | | $V_{CC} = 2.3V$ | _ | -8 | | |
| Іон | High-Level Output Current | V _{CC} = 3V | _ | -16 | mA | |
| | | | _ | -24 | | |
| | | V _{CC} = 4.5V | _ | -32 | | |
| | | V _{CC} = 1.65V | _ | 4 | | |
| | | $V_{CC} = 2.3V$ | _ | 8 | | |
| I _{OL} | Low-Level Output Current | V 0V | _ | 16 | mA | |
| | | $V_{CC} = 3V$ | _ | 24 | | |
| | | V _{CC} = 4.5V | _ | 32 | | |
| | | V _{CC} = 1.8V ± 0.15V, 2.5V ± 0.2V | _ | 20 | | |
| Δt/ΔV | Input Transition Rise or Fall Rate | $V_{CC} = 3.3V \pm 0.3V$ | _ | 10 | ns/V | |
| | | $V_{CC} = 5V \pm 0.5V$ | _ | 5 | | |
| T _A | Operating Free-air Temperature | - | -40 | +125 | °C | |

Note: 8. Unused inputs should be held at V_{CC} or Ground.

^{6.} Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.

To Forcing the maximum allowed voltage could cause a condition exceeding the maximum current or conversely forcing the maximum current could cause a condition exceeding the maximum voltage. The ratings of both current and voltage must be maintained within the controlled range.



Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

| | _ | | ., | -40°C to | +85°C | -40°C to | +125°C | |
|------------------|--|--------------------------------|---|----------|-----------------------|----------|--------|------|
| Symbol | Parameter | Test Conditions | V _{CC} | Min | Max | Min | Max | Unit |
| | | | 1.8V | 0.70 | 1.50 | 0.70 | 1.70 | |
| | | | 2.3V | 1.00 | 1.80 | 1.00 | 2.00 | |
| V_{T+} | V _{T+} Positive-going Input Threshold Voltage | _ | 3V | 1.30 | 2.20 | 1.30 | 2.40 | V |
| | Threshold Voltage | | 4.5V | 1.90 | 3.10 | 1.90 | 3.30 | |
| | | | 5.5V | 2.20 | 3.60 | 2.20 | 3.80 | |
| | | | 1.65V | 0.25 | 0.90 | 0.39 | 1.10 | |
| | | | 2.3V | 0.40 | 1.15 | 0.25 | 0.87 | |
| V_{T-} | Negative-going Input Threshold Voltage | _ | 3V | 0.60 | 1.50 | 0.40 | 1.35 | V |
| | Threshold Voltage | | 4.5V | 1.00 | 2.00 | 0.60 | 1.70 | |
| | | | 5.5V | 1.20 | 2.30 | 1.00 | 2.50 | |
| | | | 1.8V | 0.15 | 1.00 | 0.37 | 1.20 | |
| | ΔV_T Hysteresis (V_{T+} - V_{T-}) | | 2.3V | 0.25 | 1.10 | 0.15 | 1.30 |] |
| ΔV_T | | _ | 3V | 0.40 | 1.20 | 0.40 | 1.40 | V |
| | | | 4.5V | 0.60 | 1.50 | 0.60 | 1.70 | |
| | | | 5.5V | 0.70 | 1.70 | 0.70 | 1.90 | |
| | | $I_{OH} = -100\mu A$ | 1.65V to 4.5V V _{CC} - 0.1 - V _{CC} | | V _{CC} – 0.1 | 1 | | |
| | | $I_{OH} = -4mA$ | 1.65V | 1.2 | l | 0.95 | 1 | |
| M | Link Lavel Output Valtage | $I_{OH} = -8mA$ | 2.3V | 1.9 | - | 1.7 | - | V |
| V _{OH} | High-Level Output Voltage | $I_{OH} = -16mA$ | 3V | 2.4 | 1 | 1.9 | 1 | V |
| | | $I_{OH} = -24mA$ | 3 V | 2.3 | | 2.0 | - | |
| | | $I_{OH} = -32mA$ | 4.5V | 3.8 | l | 3.4 | 1 | |
| | | $I_{OL} = 100 \mu A$ | 1.65V to 4.5V | _ | 0.1 | _ | 0.10 | |
| | | $I_{OL} = 4mA$ | 1.65V | _ | 0.45 | _ | 0.70 | |
| ., | Law Lavel Output Valtage | $I_{OL} = 8mA$ | 2.3V | _ | 0.3 | _ | 0.45 | V |
| V_{OL} | Low-Level Output Voltage | $I_{OL} = 16mA$ | 3V | _ | 0.4 | _ | 0.60 | V |
| | | $I_{OL} = 24mA$ | 3 V | _ | 0.55 | _ | 0.80 | |
| | | $I_{OL} = 32mA$ | 4.5V | _ | 0.55 | _ | 0.80 | |
| l _l | Input Current | V _I = 5.5V or GND | 0 to 5.5V | _ | ± 5 | _ | ± 20 | μΑ |
| l _{OFF} | Power Down Leakage Current | V_I or $V_O = 5.5V$ | 0 | _ | ± 10 | _ | ± 20 | μA |
| Icc | Supply Current | $V_I = 5.5V$ or GND, $I_O = 0$ | 1.65V to 5.5V | _ | 10 | _ | 40 | μΑ |



Package Characteristics ((@ $T_A = +25$ °C, $V_{CC} = 3.3V$, unless otherwise specified.)

| Symbol | Parameter | Package | Conditions | Min | Тур | Max | Unit |
|--|---------------------------------|-------------------------|--|-----|----------|------|------|
| Cı | Input Capacitance | Typical of all packages | $V_{CC} = 3.3V$ $V_{I} = V_{CC}$ or GND | 1 | 3.5 | | pF |
| | | SOT26 | | _ | 204 | _ | |
| | | SOT363 | | | 371 | _ | |
| θ _{JA} Thermal Resistance Junction-to- Ambient | X2-DFN1410-6 | | _ | 430 | _ | | |
| | X2-DFN1409-6 | (Note 9) | _ | 450 | <u> </u> | °C/W | |
| | | X1-DFN1010-6 | | _ | 495 | _ | |
| | | X2-DFN1010-6 | | _ | 510 | _ | |
| | | SOT26 | | _ | 52 | _ | |
| | | SOT363 | | _ | 143 | _ | |
| _ | Thermal Resistance Junction-to- | X2-DFN1410-6 | | _ | 190 | _ | |
| θ _{JC} Ca | Case | X2-DFN1409-6 | (Note 9) | _ | 225 | _ | °C/W |
| | | X1-DFN1010-6 | | _ | 245 | _ | |
| | | X2-DFN1010-6 | | 1 | 250 | _ | |

Note: 9. Test condition for all packages: Device mounted on FR-4 substrate PC board, 2oz copper with minimum recommended pad layout.

Switching Characteristics

 $T_A = -40$ °C to +85°C, $C_L = 30$ or 50pF (see Figure 1)

| Parameter | From TO (Input) (OUTPUT | From (Input) | | | = 1.8V .15V | | : 2.5V).2V | V _{CC} = ± 0 | : 3.3V).3V | | = 5V).5V | Unit |
|-----------------|-------------------------|-----------------|-----|------|----------------|-----|----------------|-----------------------|----------------|-----|--------------|------|
| | (iliput) | (OUTPUT) | Min | Max | Min | Max | Min | Max | Min | Max | | |
| t _{PD} | Α | Υ | 0.5 | 10.5 | 0.5 | 6.5 | 0.5 | 5.7 | 0.5 | 4.3 | ns | |

 $T_A = -40$ °C to +125°C, $C_L = 30$ or 50pF (see Figure 1)

| Parameter | From (Input) | - | | = 1.8V .15V | | : 2.5V).2V | V _{CC} = ± 0 | : 3.3V :3V | V _{CC} ± 0 | = 5V).5V | Unit |
|-----------------|-----------------|----------|-----|----------------|-----|----------------|-----------------------|---------------|------------------------|--------------|------|
| | (iliput) | (001701) | Min | Max | Min | Max | Min | Max | Min | Max | |
| t _{PD} | Α | Υ | 0.5 | 13.1 | 0.5 | 8.5 | 0.5 | 7.1 | 0.5 | 5.4 | ns |

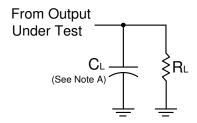
Operating Characteristics

 $T_A = +25$ °C

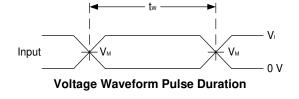
| | Parameter | Test Conditions | V _{CC} = 1.8V Typ | V _{CC} = 2.5V Typ | V _{CC} = 3.3V Typ | V _{CC} = 5V Typ | Unit |
|----------|----------------------------------|--------------------|-------------------------------|-------------------------------|-------------------------------|-----------------------------|------|
| C_{PD} | Power Dissipation Capacitance | f = 10MHz | 17 | 19 | 20 | 21 | pF |

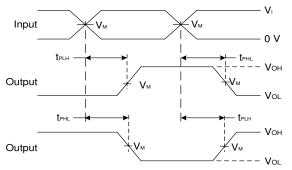


Parameter Measurement Information



| V | Inp | uts | V | 0. | D. | |
|------------|-----------------|--------------------------------|--------------------|------|-------|--|
| Vcc | VI | t _R /t _F | V _M | CL | R_L | |
| 1.8V±0.15V | Vcc | ≤2ns | V _{CC} /2 | 30pF | 1kΩ | |
| 2.5V±0.2V | V _{CC} | ≤2ns | V _{CC} /2 | 30pF | 500Ω | |
| 3.3V±0.3V | 3V | ≤2.5ns | 1.5V | 50pF | 500Ω | |
| 5V±0.5V | V _{CC} | ≤2.5ns | V _{CC} /2 | 50pF | 500Ω | |





Voltage Waveform Propagation Delay Times Inverting and Non Inverting Outputs

Figure 1. Load Circuit and Voltage Waveforms

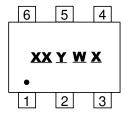
Notes:

- A. Includes test lead and test apparatus capacitance.B. All pulses are supplied at pulse repetition rate ≤ 10MHz.
- C. Inputs are measured separately one transition per measurement.
- D. t_{PLH} and t_{PHL} are the same as t_{PD} .



Marking Information

(1) SOT26, SOT363



XX: Identification code

Y: Year 0~9

 \underline{W} : Week: A~Z: 1~26 week;

a~z: 27~52 week; z represents 52 and 53 week

X: A~Z: Internal Code

| Part Number | Package | Identification Code |
|---------------|---------|---------------------|
| 74LVC2G17W6-7 | SOT26 | Z6 |

SOT363

(2) X1-DFN1010-6, X2-DFN1010-6, X2-DFN1409-6, X2-DFN1410-6

74LVC2G17DW-7

(Top View)



 $\frac{XX}{Y}$: Identification Code $\frac{X}{Y}$: Year: 0~9

₩: Week: A~Z: 1~26 week; a~z: 27~52 week; z represents

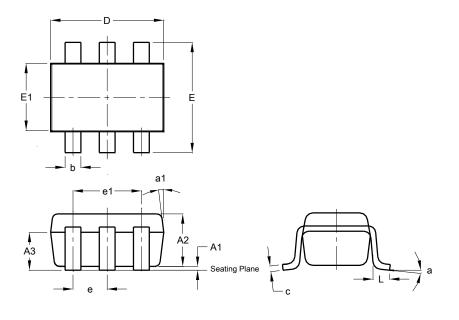
Z6

52 and 53 week X: A~Z: Internal code

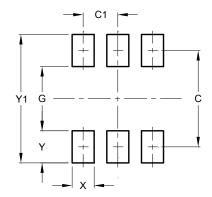
| Part Number | Package | Identification Code |
|----------------|--------------|---------------------|
| 74LVC2G17FW4-7 | X2-DFN1010-6 | Z6 |
| 74LVC2G17FW5-7 | X1-DFN1010-6 | W6 |
| 74LVC2G17FX4-7 | X2-DFN1409-6 | X6 |
| 74LVC2G17FZ4-7 | X2-DFN1410-6 | Z6 |



SOT26 Package Outline Dimensions and Suggested Pad Layout



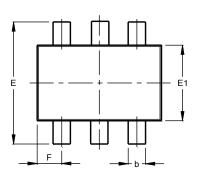
| | SOT26 | | | |
|----------------------|-------|------|------|--|
| Dim | Min | Max | Тур | |
| A1 | 0.013 | 0.10 | 0.05 | |
| A2 | 1.00 | 1.30 | 1.10 | |
| A3 | 0.70 | 0.80 | 0.75 | |
| b | 0.35 | 0.50 | 0.38 | |
| С | 0.10 | 0.20 | 0.15 | |
| D | 2.90 | 3.10 | 3.00 | |
| е | - | - | 0.95 | |
| e1 | - | ı | 1.90 | |
| E | 2.70 | 3.00 | 2.80 | |
| E1 | 1.50 | 1.70 | 1.60 | |
| L | 0.35 | 0.55 | 0.40 | |
| а | - | _ | 8° | |
| a1 | - | - | 7° | |
| All Dimensions in mm | | | | |

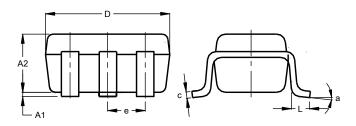


| Dimensions | Value (in mm) |
|------------|---------------|
| С | 2.40 |
| C1 | 0.95 |
| G | 1.60 |
| Х | 0.55 |
| Υ | 0.80 |
| Y1 | 3.20 |

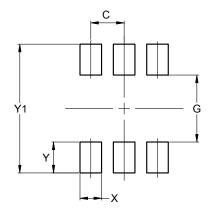


SOT363 Package Outline Dimensions and Suggested Pad Layout





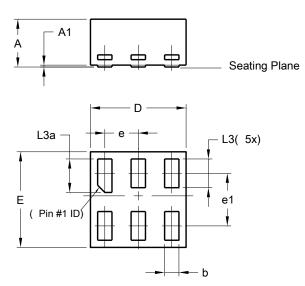
| SOT363 | | | | | | |
|----------------------|------|-------------|-------|--|--|--|
| Dim | Min | Min Max Typ | | | | |
| A1 | 0.00 | 0.10 | 0.05 | | | |
| A2 | 0.90 | 1.00 | 1.00 | | | |
| b | 0.10 | 0.30 | 0.25 | | | |
| С | 0.10 | 0.22 | 0.11 | | | |
| D | 1.80 | 2.20 | 2.15 | | | |
| Е | 2.00 | 2.20 | 2.10 | | | |
| E1 | 1.15 | 1.35 | 1.30 | | | |
| е | 0 |).650 B | SC | | | |
| F | 0.40 | 0.45 | 0.425 | | | |
| L | 0.25 | 0.40 | 0.30 | | | |
| а | a 8° | | | | | |
| All Dimensions in mm | | | | | | |



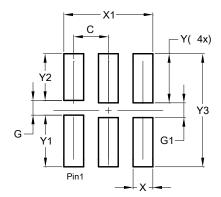
| Dimensions | Value | |
|------------|---------|--|
| Dimensions | (in mm) | |
| C | 0.650 | |
| G | 1.300 | |
| X | 0.420 | |
| Υ | 0.600 | |
| Y1 | 2 500 | |



X1-DFN1010-6 (Type B) Package Outline Dimensions and Suggested Pad Layout



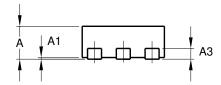
| | X1-DFN1010-6 (Type B) | | | | |
|----------------------|--------------------------|-------|------|--|--|
| Dim | Min Max Typ | | | | |
| Α | - | 0.50 | 0.39 | | |
| A1 | - | 0.04 | - | | |
| b | 0.12 | 0.20 | 0.15 | | |
| D | 0.95 | 1.050 | 1.00 | | |
| Е | 0.95 | 1.050 | 1.00 | | |
| е | 0.35 BSC | | | | |
| e1 | 0.55 BSC | | | | |
| L3 | 0.27 | 0.30 | 0.30 | | |
| L3a | 0.32 | 0.40 | 0.35 | | |
| All Dimensions in mm | | | | | |

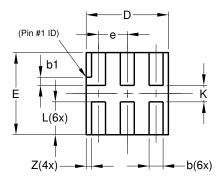


| Dimensions | Value | |
|------------|---------|--|
| | (in mm) | |
| C | 0.350 | |
| G | 0.150 | |
| G1 | 0.150 | |
| Х | 0.200 | |
| X1 | 0.900 | |
| Υ | 0.500 | |
| Y1 | 0.525 | |
| Y2 | 0.475 | |
| Y3 | 1.150 | |

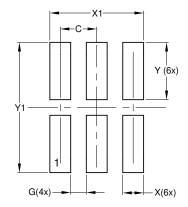


X2-DFN1010-6 Package Outline Dimensions and Suggested Pad Layout





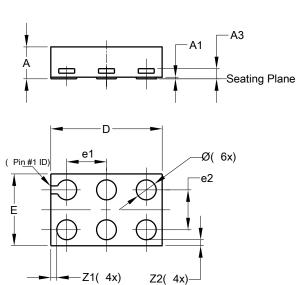
| | X2-DFN1010-6 | | | | |
|----------------------|--------------|------|-------|--|--|
| Dim | Min | Max | Тур | | |
| Α | | 0.40 | 0.39 | | |
| A1 | 0.00 | 0.05 | 0.02 | | |
| A3 | | | 0.13 | | |
| b | 0.14 | 0.20 | 0.17 | | |
| b1 | 0.05 | 0.15 | 0.10 | | |
| D | 0.95 | 1.05 | 1.00 | | |
| Е | 0.95 | 1.05 | 1.00 | | |
| е | _ | _ | 0.35 | | |
| L | 0.35 | 0.45 | 0.40 | | |
| K | 0.15 | | | | |
| Z | | | 0.065 | | |
| All Dimensions in mm | | | | | |



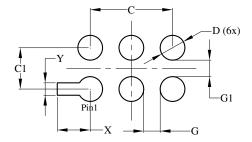
| Dimensions | Value (in mm) | |
|------------|------------------|--|
| С | 0.350 | |
| G | 0.150 | |
| Х | 0.200 | |
| X1 | 0.900 | |
| Y | 0.550 | |
| Y1 | 1.250 | |



X2-DFN1409-6 Package Outline Dimensions and Suggested Pad Layout



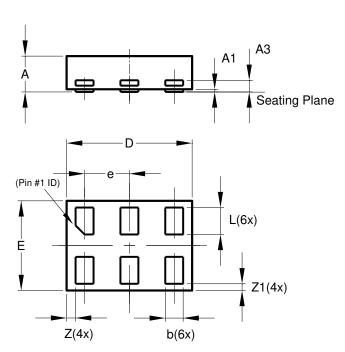
| X2-DFN1409-6 | | | | | | |
|----------------------|-----------------|------|-------|--|--|--|
| Dim | Dim Min Max Typ | | | | | |
| Α | _ | 0.40 | 0.39 | | | |
| A 1 | 0 | 0.05 | 0.02 | | | |
| А3 | _ | _ | 0.13 | | | |
| Ø | 0.20 | 0.30 | 0.25 | | | |
| D | 1.35 | 1.45 | 1.40 | | | |
| Е | 0.85 | 0.95 | 0.90 | | | |
| e1 | _ | l | 0.50 | | | |
| e2 | | - | 0.50 | | | |
| Z 1 | _ | | 0.075 | | | |
| Z2 | _ | | 0.075 | | | |
| All Dimensions in mm | | | | | | |



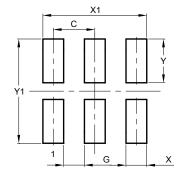
| Dimensions | Value (in mm) |
|------------|------------------|
| С | 1.000 |
| C1 | 0.500 |
| D | 0.300 |
| G | 0.200 |
| G1 | 0.200 |
| Х | 0.400 |
| Υ | 0.150 |



X2-DFN1410-6 Package Outline Dimensions and Suggested Pad Layout



| X2-DFN1410-6 | | | |
|----------------------|-------|-------|-------|
| Dim | Min | Max | Тур |
| Α | _ | 0.40 | 0.39 |
| A1 | 0.00 | 0.05 | 0.02 |
| A3 | | | 0.13 |
| b | 0.15 | 0.25 | 0.20 |
| D | 1.35 | 1.45 | 1.40 |
| Е | 0.95 | 1.05 | 1.00 |
| е | | | 0.50 |
| L | 0.25 | 0.35 | 0.30 |
| Z | _ | | 0.10 |
| Z 1 | 0.045 | 0.105 | 0.075 |
| All Dimensions in mm | | | |



| Dimensions | Value |
|------------|---------|
| | (in mm) |
| С | 0.500 |
| G | 0.250 |
| Х | 0.250 |
| X1 | 1.250 |
| Υ | 0.525 |
| Y1 | 1.250 |



IMPORTANT NOTICE

DIODES INCORPORATED MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).

Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. Diodes Incorporated does not assume any liability arising out of the application or use of this document or any product described herein; neither does Diodes Incorporated convey any license under its patent or trademark rights, nor the rights of others. Any Customer or user of this document or products described herein in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on Diodes Incorporated website, harmless against all damages.

Diodes Incorporated does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel. Should Customers purchase or use Diodes Incorporated products for any unintended or unauthorized application, Customers shall indemnify and hold Diodes Incorporated and its representatives harmless against all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized application.

Products described herein may be covered by one or more United States, international or foreign patents pending. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks.

This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes Incorporated.

LIFE SUPPORT

Diodes Incorporated products are specifically not authorized for use as critical components in life support devices or systems without the express written approval of the Chief Executive Officer of Diodes Incorporated. As used herein:

- A. Life support devices or systems are devices or systems which:
 - 1. are intended to implant into the body, or
 - 2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.
- B. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or to affect its safety or effectiveness.

Customers represent that they have all necessary expertise in the safety and regulatory ramifications of their life support devices or systems, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of Diodes Incorporated products in such safety-critical, life support devices or systems, notwithstanding any devices- or systems-related information or support that may be provided by Diodes Incorporated. Further, Customers must fully indemnify Diodes Incorporated and its representatives against any damages arising out of the use of Diodes Incorporated products in such safety-critical, life support devices or systems.

Copyright © 2015, Diodes Incorporated

www.diodes.com