



Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of “Quality Parts,Customers Priority,Honest Operation,and Considerate Service”,our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China



74LVC2G66

Bilateral switch

Rev. 10 — 13 April 2017

Product data sheet

1 General description

The 74LVC2G66 is a low-power, low-voltage, high-speed Si-gate CMOS device.

The 74LVC2G66 provides two single pole, single-throw analog switch functions. Each switch has two input/output terminals (nY and nZ) and an active HIGH enable input (nE). When nE is LOW, the analog switch is turned off.

Schmitt trigger action at the enable inputs makes the circuit tolerant of slower input rise and fall times across the entire V_{CC} range from 1.65 V to 5.5 V.

2 Features and benefits

- Wide supply voltage range from 1.65 V to 5.5 V
- Very low ON resistance:
 - 7.5 Ω (typical) at $V_{CC} = 2.7$ V
 - 6.5 Ω (typical) at $V_{CC} = 3.3$ V
 - 6 Ω (typical) at $V_{CC} = 5$ V
- Switch current capability of 32 mA
- High noise immunity
- CMOS low power consumption
- TTL interface compatibility at 3.3 V
- Latch-up performance meets requirements of JESD78 Class I
- ESD protection:
 - HBM JESD22-A114F exceeds 2000 V
 - MM JESD22-A115-A exceeds 200 V
- Enable input accepts voltages up to 5.5 V
- Multiple package options
- Specified from -40 °C to +85 °C and -40 °C to +125 °C

3 Ordering information

Table 1. Ordering information

Type number	Package			Version
	Temperature range	Name	Description	
74LVC2G66DP	-40 °C to +125 °C	TSSOP8	plastic thin shrink small outline package; 8 leads; body width 3 mm; lead length 0.5 mm	SOT505-2
74LVC2G66DC	-40 °C to +125 °C	VSSOP8	plastic very thin shrink small outline package; 8 leads; body width 2.3 mm	SOT765-1
74LVC2G66GT	-40 °C to +125 °C	XSON8	plastic extremely thin small outline package; no leads; 8 terminals; body 1 x 1.95 x 0.5 mm	SOT833-1
74LVC2G66GD	-40 °C to +125 °C	XSON8	plastic extremely thin small outline package; no leads; 8 terminals; body 3 x 2 x 0.5 mm	SOT996-2
74LVC2G66GM	-40 °C to +125 °C	XQFN8	plastic, extremely thin quad flat package; no leads; 8 terminals; body 1.6 x 1.6 x 0.5 mm	SOT902-2
74LVC2G66GN	-40 °C to +125 °C	XSON8	extremely thin small outline package; no leads; 8 terminals; body 1.2 x 1.0 x 0.35 mm	SOT1116

4 Marking

Table 2. Marking codes

Type number	Marking code ^[1]
74LVC2G66DP	V66
74LVC2G66DC	V66
74LVC2G66GT	V66
74LVC2G66GD	V66
74LVC2G66GM	V66
74LVC2G66GN	VL

[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

5 Functional diagram

Figure 1. Logic symbol

Figure 2. IEC logic symbol

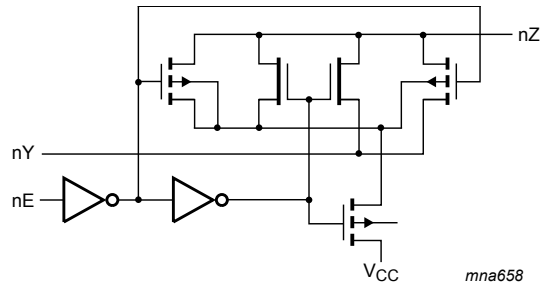


Figure 3. Logic diagram (one switch)

6 Pinning information

6.1 Pinning

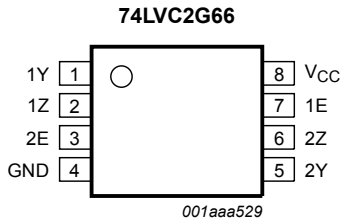


Figure 4. Pin configuration SOT505-2 and SOT765-1

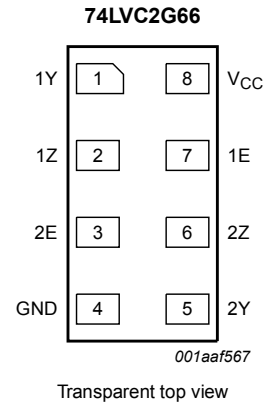
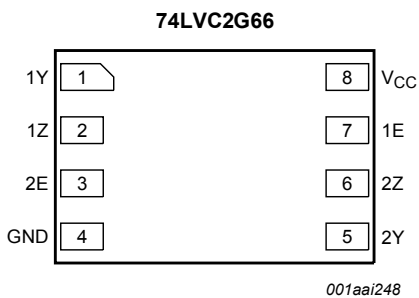
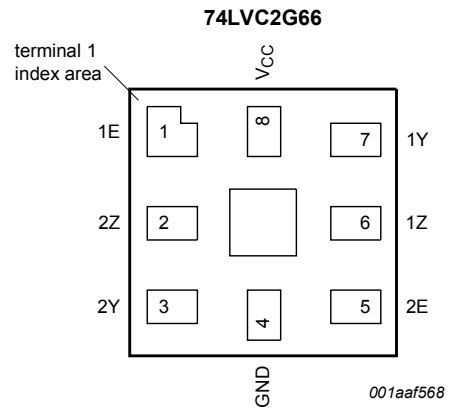


Figure 5. Pin configuration SOT833-1 and SOT1116



Transparent top view

Figure 6. Pin configuration SOT996-2



Transparent top view

Figure 7. Pin configuration SOT902-2

6.2 Pin description

Table 3. Pin description

Symbol	Pin		Description
	SOT505-2, SOT765-1, SOT996-2, SOT833-1 and SOT1116	SOT902-2	
1Y	1	7	independent input or output
1Z	2	6	independent input or output
2E	3	5	enable input (active HIGH)
GND	4	4	ground (0 V)
2Y	5	3	independent input or output
2Z	6	2	independent input or output
1E	7	1	enable input (active HIGH)
V _{CC}	8	8	supply voltage

7 Functional description

Table 4. Function table ^[1]

Input nE	Switch
L	OFF-state
H	ON-state

[1] H = HIGH voltage level;
L = LOW voltage level.

8 Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{CC}	supply voltage		-0.5	+6.5	V
V_I	input voltage	[1]	-0.5	+6.5	V
I_{IK}	input clamping current	$V_I < -0.5\text{ V}$ or $V_I > V_{CC} + 0.5\text{ V}$	-50	-	mA
I_{SK}	switch clamping current	$V_I < -0.5\text{ V}$ or $V_I > V_{CC} + 0.5\text{ V}$	-	± 50	mA
V_{SW}	switch voltage	enable and disable mode [2]	-0.5	$V_{CC} + 0.5$	V
I_{SW}	switch current	$V_{SW} > -0.5\text{ V}$ or $V_{SW} < V_{CC} + 0.5\text{ V}$	-	± 50	mA
I_{CC}	supply current		-	100	mA
I_{GND}	ground current		-100	-	mA
T_{stg}	storage temperature		-65	+150	°C
P_{tot}	total power dissipation	$T_{amb} = -40\text{ °C}$ to $+125\text{ °C}$ [3]	-	250	mW

[1] The minimum input voltage rating may be exceeded if the input current rating is observed.

[2] The minimum and maximum switch voltage ratings may be exceeded if the switch clamping current rating is observed.

[3] For TSSOP8 package: above 55 °C the value of P_{tot} derates linearly with 2.5 mW/K.

For VSSOP8 package: above 110 °C the value of P_{tot} derates linearly with 8 mW/K.

For XSON8 and XQFN8 packages: above 118 °C the value of P_{tot} derates linearly with 7.8 mW/K.

9 Recommended operating conditions

Table 6. Operating conditions

Symbol	Parameter	Conditions	Min	Max	Unit
V_{CC}	supply voltage		1.65	5.5	V
V_I	input voltage		0	5.5	V
V_{SW}	switch voltage	[1] [2]	0	V_{CC}	V
T_{amb}	ambient temperature		-40	+125	°C
$\Delta t/\Delta V$	input transition rise and fall rate	$V_{CC} = 1.65\text{ V}$ to 2.7 V [3]	-	20	ns/V
		$V_{CC} = 2.7\text{ V}$ to 5.5 V	-	10	ns/V

[1] To avoid sinking GND current from terminal nZ when switch current flows in terminal nY, the voltage drop across the bidirectional switch must not exceed 0.4 V. If the switch current flows into terminal nZ, no GND current will flow from terminal nY. In this case, there is no limit for the voltage drop across the switch.

[2] For overvoltage tolerant switch voltage capability, refer to 74LVCV2G66.

[3] Applies to control signal levels.

10 Static characteristics

Table 7. Static characteristics

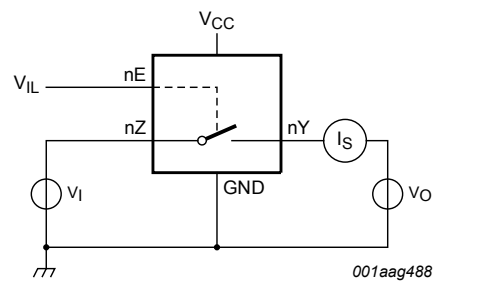
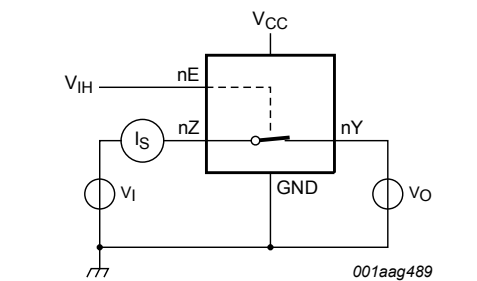
At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	-40 °C to +85 °C			-40 °C to +125 °C		Unit
			Min	Typ ^[1]	Max	Min	Max	
V _{IH}	HIGH-level input voltage	V _{CC} = 1.65 V to 1.95 V	0.65 × V _{CC}	-	-	0.65 × V _{CC}	-	V
		V _{CC} = 2.3 V to 2.7 V	1.7	-	-	1.7	-	V
		V _{CC} = 2.7 V to 3.6 V	2.0	-	-	2.0	-	V
		V _{CC} = 4.5 V to 5.5 V	0.7 × V _{CC}	-	-	0.7 × V _{CC}	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 1.65 V to 1.95 V	-	-	0.35 × V _{CC}	-	0.35 × V _{CC}	V
		V _{CC} = 2.3 V to 2.7 V	-	-	0.7	-	0.7	V
		V _{CC} = 2.7 V to 3.6 V	-	-	0.8	-	0.8	V
		V _{CC} = 4.5 V to 5.5 V	-	-	0.3 × V _{CC}	-	0.3 × V _{CC}	V
I _I	input leakage current	pin nE; V _I = 5.5 V or GND; V _{CC} = 0 V to 5.5 V ^[2]	-	±0.1	±1	-	±1	μA
I _{S(OFF)}	OFF-state leakage current	V _{CC} = 5.5 V; see Figure 8 . ^[2]	-	±0.1	±0.2	-	±0.5	μA
I _{S(ON)}	ON-state leakage current	V _{CC} = 5.5 V; see Figure 9 . ^[2]	-	±0.1	±1	-	±2	μA
I _{CC}	supply current	V _I = 5.5 V or GND; V _{SW} = GND or V _{CC} ; V _{CC} = 1.65 V to 5.5 V ^[2]	-	0.1	4	-	4	μA
ΔI _{CC}	additional supply current	pin nE; V _I = V _{CC} - 0.6 V; V _{SW} = GND or V _{CC} ; V _{CC} = 5.5 V ^[2]	-	5	500	-	500	μA
C _I	input capacitance		-	2.0	-	-	-	pF
C _{S(OFF)}	OFF-state capacitance		-	5.0	-	-	-	pF
C _{S(ON)}	ON-state capacitance		-	9.5	-	-	-	pF

[1] All typical values are measured at T_{amb} = 25 °C.

[2] These typical values are measured at V_{CC} = 3.3 V.

10.1 Test circuits

 <p style="text-align: right; font-size: small;">001aag488</p> <p>$V_I = V_{CC}$ or GND and $V_O = GND$ or V_{CC}.</p> <p>Figure 8. Test circuit for measuring OFF-state leakage current</p>	 <p style="text-align: right; font-size: small;">001aag489</p> <p>$V_I = V_{CC}$ or GND and $V_O =$ open circuit.</p> <p>Figure 9. Test circuit for measuring ON-state leakage current</p>
--	--

10.2 ON resistance

Table 8. ON resistance

At recommended operating conditions; voltages are referenced to GND (ground 0 V); for graphs see [Figure 11](#) to [Figure 16](#).

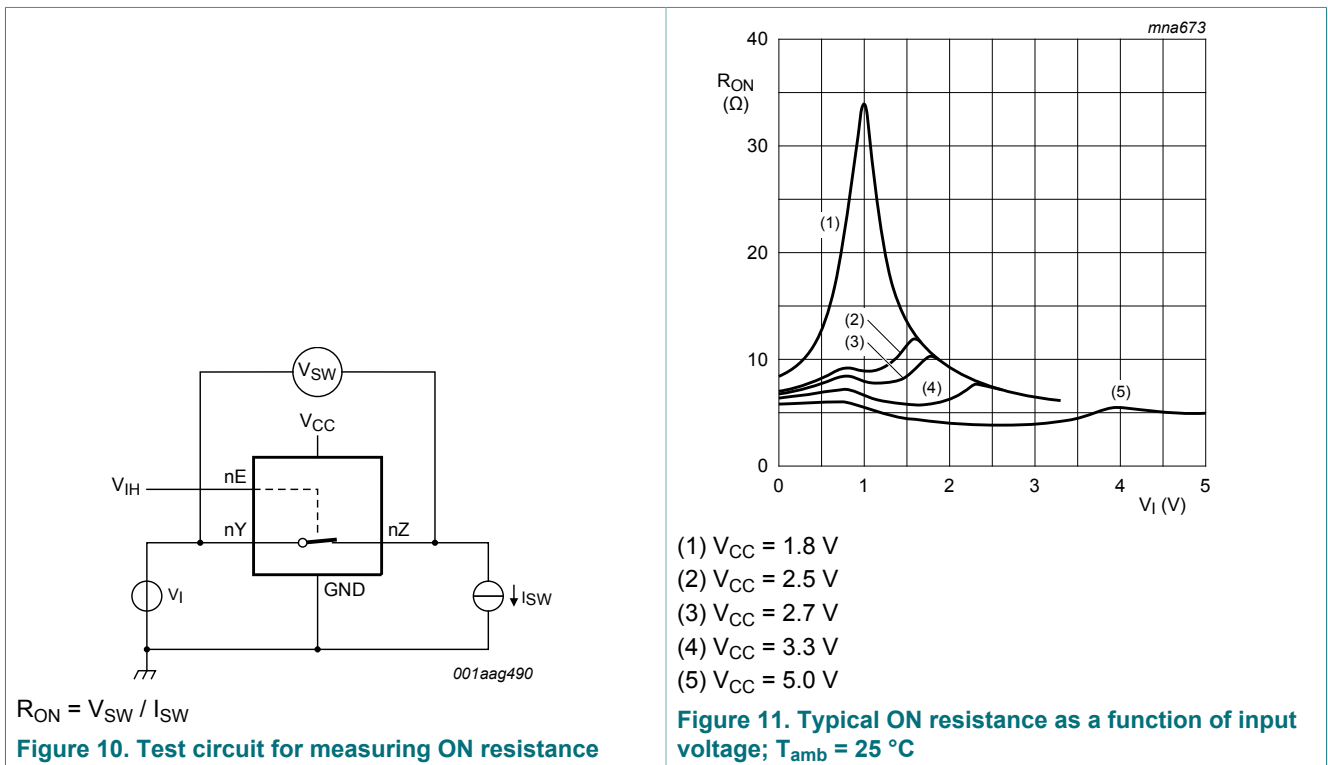
Symbol	Parameter	Conditions	-40 °C to +85 °C			-40 °C to +125 °C		Unit
			Min	Typ ^[1]	Max	Min	Max	
R _{ON(peak)}	ON resistance (peak)	$V_I = GND$ to V_{CC} ; see Figure 10 .						
		$I_{SW} = 4$ mA; $V_{CC} = 1.65$ V to 1.95 V	-	34.0	130	-	195	Ω
		$I_{SW} = 8$ mA; $V_{CC} = 2.3$ V to 2.7 V	-	12.0	30	-	45	Ω
		$I_{SW} = 12$ mA; $V_{CC} = 2.7$ V	-	10.4	25	-	38	Ω
		$I_{SW} = 24$ mA; $V_{CC} = 3$ V to 3.6 V	-	7.8	20	-	30	Ω
		$I_{SW} = 32$ mA; $V_{CC} = 4.5$ V to 5.5 V	-	6.2	15	-	23	Ω
R _{ON(rail)}	ON resistance (rail)	$V_I = GND$; see Figure 10						
		$I_{SW} = 4$ mA; $V_{CC} = 1.65$ V to 1.95 V	-	8.2	18	-	27	Ω
		$I_{SW} = 8$ mA; $V_{CC} = 2.3$ V to 2.7 V	-	7.1	16	-	24	Ω
		$I_{SW} = 12$ mA; $V_{CC} = 2.7$ V	-	6.9	14	-	21	Ω
		$I_{SW} = 24$ mA; $V_{CC} = 3$ V to 3.6 V	-	6.5	12	-	18	Ω
		$I_{SW} = 32$ mA; $V_{CC} = 4.5$ V to 5.5 V	-	5.8	10	-	15	Ω
		$V_I = V_{CC}$; see Figure 10						
		$I_{SW} = 4$ mA; $V_{CC} = 1.65$ V to 1.95 V	-	10.4	30	-	45	Ω
		$I_{SW} = 8$ mA; $V_{CC} = 2.3$ V to 2.7 V	-	7.6	20	-	30	Ω
		$I_{SW} = 12$ mA; $V_{CC} = 2.7$ V	-	7.0	18	-	27	Ω
		$I_{SW} = 24$ mA; $V_{CC} = 3$ V to 3.6 V	-	6.1	15	-	23	Ω
		$I_{SW} = 32$ mA; $V_{CC} = 4.5$ V to 5.5 V	-	4.9	10	-	15	Ω

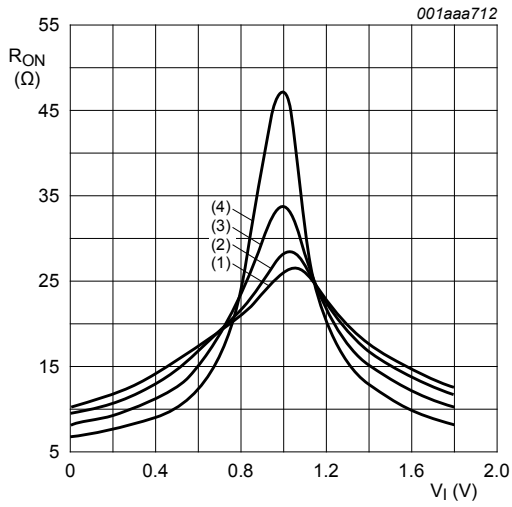
Symbol	Parameter	Conditions	-40 °C to +85 °C			-40 °C to +125 °C		Unit
			Min	Typ ^[1]	Max	Min	Max	
R _{ON(flat)}	ON resistance (flatness)	V _I = GND to V _{CC} ^[2]						
		I _{SW} = 4 mA; V _{CC} = 1.65 V to 1.95 V	-	26.0	-	-	-	Ω
		I _{SW} = 8 mA; V _{CC} = 2.3 V to 2.7 V	-	5.0	-	-	-	Ω
		I _{SW} = 12 mA; V _{CC} = 2.7 V	-	3.5	-	-	-	Ω
		I _{SW} = 24 mA; V _{CC} = 3 V to 3.6 V	-	2.0	-	-	-	Ω
		I _{SW} = 32 mA; V _{CC} = 4.5 V to 5.5 V	-	1.5	-	-	-	Ω

[1] Typical values are measured at T_{amb} = 25 °C and nominal V_{CC}.

[2] Flatness is defined as the difference between the maximum and minimum value of ON resistance measured at identical V_{CC} and temperature.

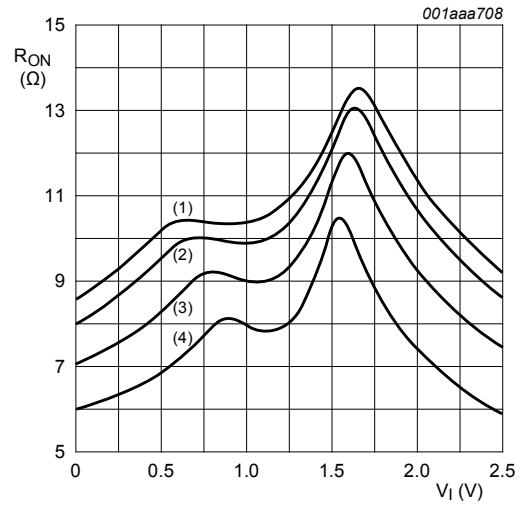
10.3 ON resistance test circuit and graphs





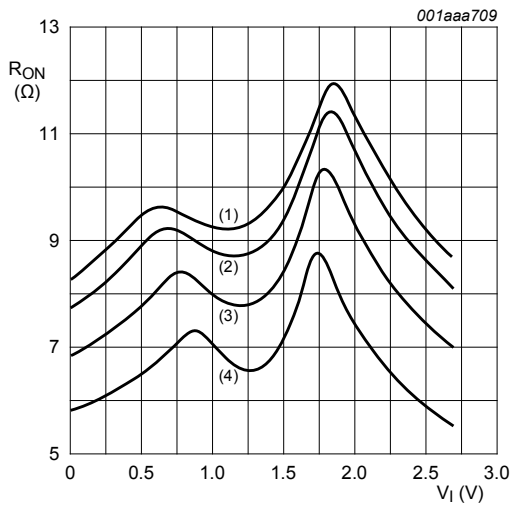
- (1) $T_{amb} = 125\text{ }^{\circ}\text{C}$
- (2) $T_{amb} = 85\text{ }^{\circ}\text{C}$
- (3) $T_{amb} = 25\text{ }^{\circ}\text{C}$
- (4) $T_{amb} = -40\text{ }^{\circ}\text{C}$

Figure 12. ON resistance as a function of input voltage; $V_{CC} = 1.8\text{ V}$



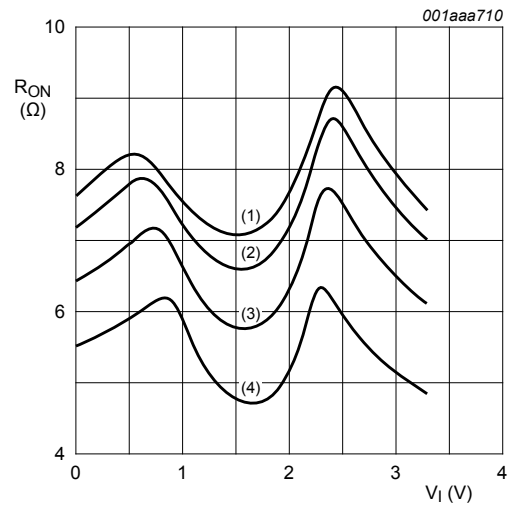
- (1) $T_{amb} = 125\text{ }^{\circ}\text{C}$
- (2) $T_{amb} = 85\text{ }^{\circ}\text{C}$
- (3) $T_{amb} = 25\text{ }^{\circ}\text{C}$
- (4) $T_{amb} = -40\text{ }^{\circ}\text{C}$

Figure 13. ON resistance as a function of input voltage; $V_{CC} = 2.5\text{ V}$



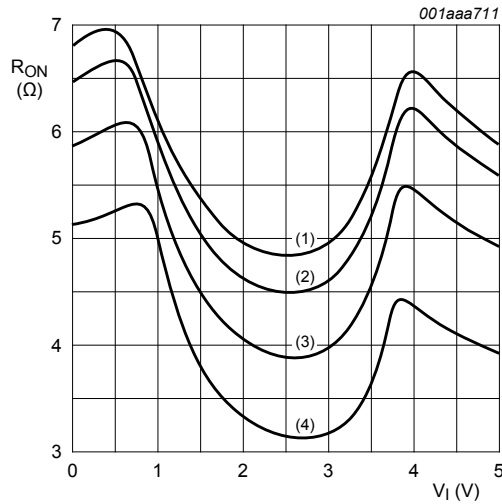
- (1) $T_{amb} = 125\text{ }^{\circ}\text{C}$
- (2) $T_{amb} = 85\text{ }^{\circ}\text{C}$
- (3) $T_{amb} = 25\text{ }^{\circ}\text{C}$
- (4) $T_{amb} = -40\text{ }^{\circ}\text{C}$

Figure 14. ON resistance as a function of input voltage; $V_{CC} = 2.7\text{ V}$



- (1) $T_{amb} = 125\text{ }^{\circ}\text{C}$
- (2) $T_{amb} = 85\text{ }^{\circ}\text{C}$
- (3) $T_{amb} = 25\text{ }^{\circ}\text{C}$
- (4) $T_{amb} = -40\text{ }^{\circ}\text{C}$

Figure 15. ON resistance as a function of input voltage; $V_{CC} = 3.3\text{ V}$



- (1) $T_{amb} = 125\text{ °C}$
- (2) $T_{amb} = 85\text{ °C}$
- (3) $T_{amb} = 25\text{ °C}$
- (4) $T_{amb} = -40\text{ °C}$

Figure 16. ON resistance as a function of input voltage; $V_{CC} = 5.0\text{ V}$

11 Dynamic characteristics

Table 9. Dynamic characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V); for test circuit see Figure 19.

Symbol	Parameter	Conditions	-40 °C to +85 °C			-40 °C to +125 °C		Unit
			Min	Typ ^[1]	Max	Min	Max	
t_{pd}	propagation delay	nY to nZ or nZ to nY; see Figure 17. ^{[2] [3]}						
		$V_{CC} = 1.65\text{ V to }1.95\text{ V}$	-	0.8	2.0	-	3.0	ns
		$V_{CC} = 2.3\text{ V to }2.7\text{ V}$	-	0.4	1.2	-	2.0	ns
		$V_{CC} = 2.7\text{ V}$	-	0.4	1.0	-	1.5	ns
		$V_{CC} = 3.0\text{ V to }3.6\text{ V}$	-	0.3	0.8	-	1.5	ns
		$V_{CC} = 4.5\text{ V to }5.5\text{ V}$	-	0.2	0.6	-	1.0	ns
t_{en}	enable time	nE to nY or nZ; see Figure 18. ^[4]						
		$V_{CC} = 1.65\text{ V to }1.95\text{ V}$	1.0	4.6	10	1.0	13.0	ns
		$V_{CC} = 2.3\text{ V to }2.7\text{ V}$	1.0	2.7	5.6	1.0	7.5	ns
		$V_{CC} = 2.7\text{ V}$	1.0	2.7	5.0	1.0	6.5	ns
		$V_{CC} = 3.0\text{ V to }3.6\text{ V}$	1.0	2.4	4.4	1.0	6.0	ns
		$V_{CC} = 4.5\text{ V to }5.5\text{ V}$	1.0	1.8	3.9	1.0	5.0	ns
t_{dis}	disable time	nE to nY or nZ; see Figure 18. ^[5]						
		$V_{CC} = 1.65\text{ V to }1.95\text{ V}$	1.0	3.8	9.0	1.0	11.5	ns
		$V_{CC} = 2.3\text{ V to }2.7\text{ V}$	1.0	2.1	5.5	1.0	7.0	ns

Symbol	Parameter	Conditions	-40 °C to +85 °C			-40 °C to +125 °C		Unit
			Min	Typ ^[1]	Max	Min	Max	
		V _{CC} = 2.7 V	1.0	3.5	6.5	1.0	8.5	ns
		V _{CC} = 3.0 V to 3.6 V	1.0	3.0	6.0	1.0	8.0	ns
		V _{CC} = 4.5 V to 5.5 V	1.0	2.2	5.0	1.0	6.5	ns
C _{PD}	power dissipation capacitance	C _L = 50 pF; f _i = 10 MHz; V _I = GND to V _{CC} ^[6]						
		V _{CC} = 2.5 V	-	9.0	-	-	-	pF
		V _{CC} = 3.3 V	-	11.0	-	-	-	pF
		V _{CC} = 5.0 V	-	15.7	-	-	-	pF

[1] Typical values are measured at T_{amb} = 25 °C and nominal V_{CC}.

[2] t_{pd} is the same as t_{PLH} and t_{PHL}.

[3] Propagation delay is the calculated RC time constant of the typical ON resistance of the switch and the specified capacitance when driven by an ideal voltage source (zero output impedance).

[4] t_{en} is the same as t_{PZH} and t_{PZL}.

[5] t_{dis} is the same as t_{PLZ} and t_{PHZ}.

[6] C_{PD} is used to determine the dynamic power dissipation (P_D in μW).

$$P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \sum\{(C_L + C_{S(ON)}) \times V_{CC}^2 \times f_o\}$$

f_i = input frequency in MHz; f_o = output frequency in MHz;

C_L = output load capacitance in pF;

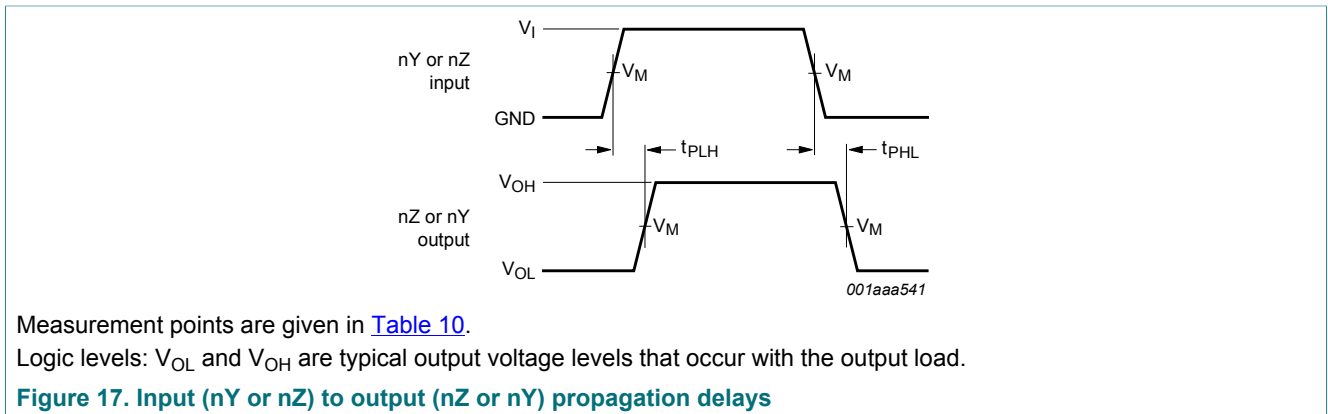
C_{S(ON)} = maximum ON-state switch capacitance in pF;

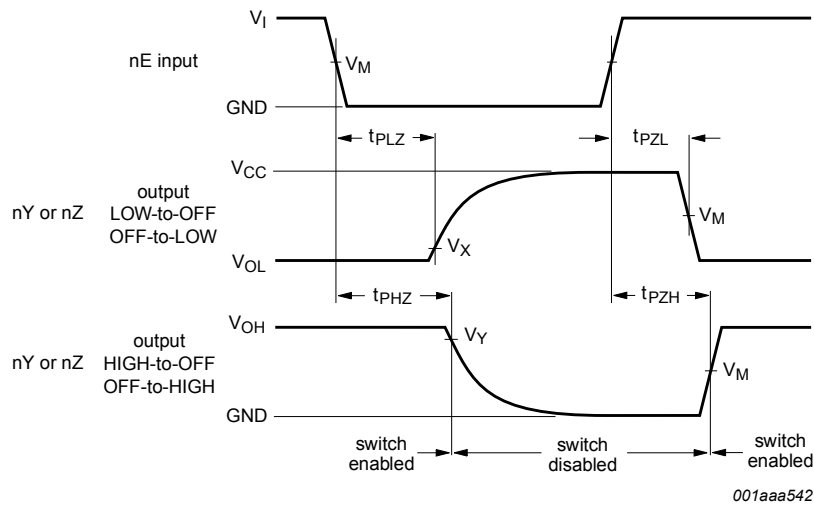
V_{CC} = supply voltage in V;

N = number of inputs switching;

∑{(C_L + C_{S(ON)}) × V_{CC}² × f_o} = sum of the outputs.

11.1 Waveforms and test circuit





Measurement points are given in [Table 10](#).

Logic levels: V_{OL} and V_{OH} are typical output voltage levels that occur with the output load.

Figure 18. Enable and disable times

Table 10. Measurement points

Supply voltage	Input	Output		
V_{CC}	V_M	V_M	V_X	V_Y
1.65 V to 1.95 V	$0.5 \times V_{CC}$	$0.5 \times V_{CC}$	$V_{OL} + 0.15 \text{ V}$	$V_{OH} - 0.15 \text{ V}$
2.3 V to 2.7 V	$0.5 \times V_{CC}$	$0.5 \times V_{CC}$	$V_{OL} + 0.15 \text{ V}$	$V_{OH} - 0.15 \text{ V}$
2.7 V	1.5 V	1.5 V	$V_{OL} + 0.3 \text{ V}$	$V_{OH} - 0.3 \text{ V}$
3.0 V to 3.6 V	1.5 V	1.5 V	$V_{OL} + 0.3 \text{ V}$	$V_{OH} - 0.3 \text{ V}$
4.5 V to 5.5 V	$0.5 \times V_{CC}$	$0.5 \times V_{CC}$	$V_{OL} + 0.3 \text{ V}$	$V_{OH} - 0.3 \text{ V}$

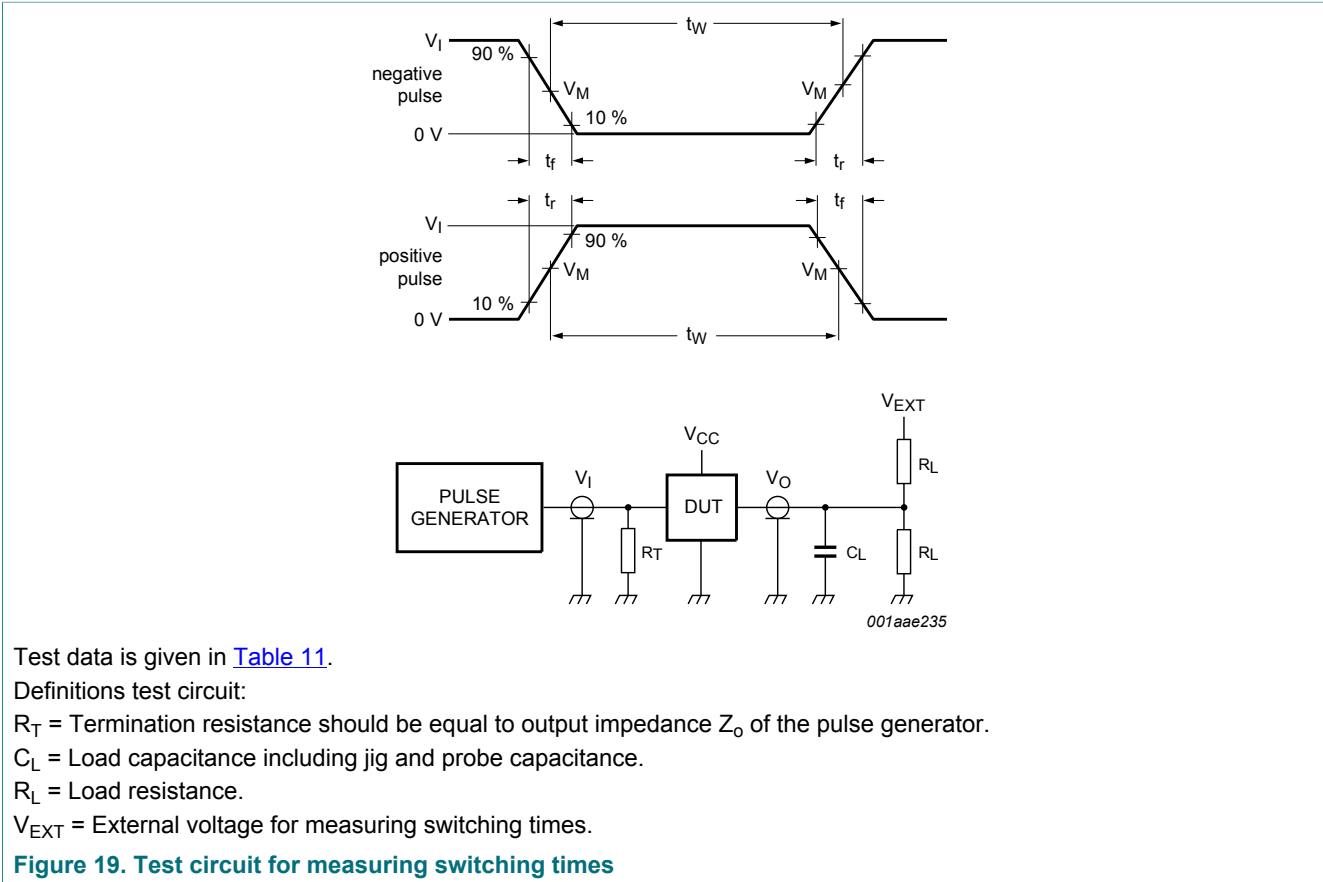


Table 11. Test data

Supply voltage	Input		Load		V_{EXT}		
V_{CC}	V_I	t_r, t_f	C_L	R_L	t_{PLH}, t_{PHL}	t_{PZH}, t_{PHZ}	t_{PZL}, t_{PLZ}
1.65 V to 1.95 V	V_{CC}	≤ 2.0 ns	30 pF	1 k Ω	open	GND	$2 \times V_{CC}$
2.3 V to 2.7 V	V_{CC}	≤ 2.0 ns	30 pF	500 Ω	open	GND	$2 \times V_{CC}$
2.7 V	2.7 V	≤ 2.5 ns	50 pF	500 Ω	open	GND	6 V
3.0 V to 3.6 V	2.7 V	≤ 2.5 ns	50 pF	500 Ω	open	GND	6 V
4.5 V to 5.5 V	V_{CC}	≤ 2.5 ns	50 pF	500 Ω	open	GND	$2 \times V_{CC}$

11.2 Additional dynamic characteristics

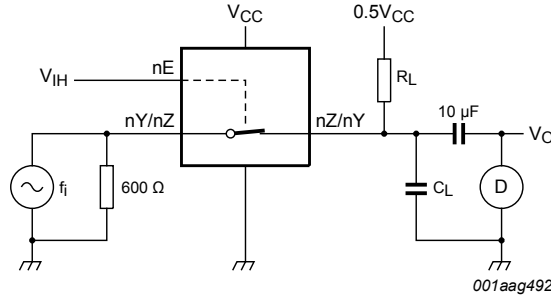
Table 12. Additional dynamic characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V); $T_{amb} = 25\text{ }^{\circ}\text{C}$.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit	
THD	total harmonic distortion	$R_L = 10\text{ k}\Omega$; $C_L = 50\text{ pF}$; $f_i = 1\text{ kHz}$; see Figure 20 .					
		$V_{CC} = 1.65\text{ V}$	-	0.032	-	%	
		$V_{CC} = 2.3\text{ V}$	-	0.008	-	%	
		$V_{CC} = 3.0\text{ V}$	-	0.006	-	%	
		$V_{CC} = 4.5\text{ V}$	-	0.005	-	%	
		$R_L = 10\text{ k}\Omega$; $C_L = 50\text{ pF}$; $f_i = 10\text{ kHz}$; see Figure 20 .					
		$V_{CC} = 1.65\text{ V}$	-	0.068	-	%	
		$V_{CC} = 2.3\text{ V}$	-	0.009	-	%	
		$V_{CC} = 3.0\text{ V}$	-	0.008	-	%	
		$V_{CC} = 4.5\text{ V}$	-	0.006	-	%	
$f_{(-3\text{dB})}$	-3 dB frequency response	$R_L = 600\text{ }\Omega$; $C_L = 50\text{ pF}$; see Figure 21 .					
		$V_{CC} = 1.65\text{ V}$	-	135	-	MHz	
		$V_{CC} = 2.3\text{ V}$	-	145	-	MHz	
		$V_{CC} = 3.0\text{ V}$	-	150	-	MHz	
		$V_{CC} = 4.5\text{ V}$	-	155	-	MHz	
		$R_L = 50\text{ }\Omega$; $C_L = 10\text{ pF}$; see Figure 21 .					
		$V_{CC} = 1.65\text{ V}$	-	200	-	MHz	
		$V_{CC} = 2.3\text{ V}$	-	350	-	MHz	
		$V_{CC} = 3.0\text{ V}$	-	410	-	MHz	
		$V_{CC} = 4.5\text{ V}$	-	440	-	MHz	
		$R_L = 50\text{ }\Omega$; $C_L = 5\text{ pF}$; see Figure 21 .					
		$V_{CC} = 1.65\text{ V}$	-	> 500	-	MHz	
		$V_{CC} = 2.3\text{ V}$	-	> 500	-	MHz	
		$V_{CC} = 3.0\text{ V}$	-	> 500	-	MHz	
$V_{CC} = 4.5\text{ V}$	-	> 500	-	MHz			
α_{iso}	isolation (OFF-state)	$R_L = 600\text{ }\Omega$; $C_L = 50\text{ pF}$; $f_i = 1\text{ MHz}$; see Figure 22 .					
		$V_{CC} = 1.65\text{ V}$	-	-46	-	dB	
		$V_{CC} = 2.3\text{ V}$	-	-46	-	dB	
		$V_{CC} = 3.0\text{ V}$	-	-46	-	dB	
		$V_{CC} = 4.5\text{ V}$	-	-46	-	dB	
		$R_L = 50\text{ }\Omega$; $C_L = 5\text{ pF}$; $f_i = 1\text{ MHz}$; see Figure 22 .					
		$V_{CC} = 1.65\text{ V}$	-	-37	-	dB	

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
		$V_{CC} = 2.3\text{ V}$	-	-37	-	dB
		$V_{CC} = 3.0\text{ V}$	-	-37	-	dB
		$V_{CC} = 4.5\text{ V}$	-	-37	-	dB
V_{ct}	crosstalk voltage	between digital inputs and switch; $R_L = 600\ \Omega$; $C_L = 50\text{ pF}$; $f_i = 1\text{ MHz}$; $t_r = t_f = 2\text{ ns}$; see Figure 23 .				
		$V_{CC} = 1.65\text{ V}$	-	-	-	mV
		$V_{CC} = 2.3\text{ V}$	-	91	-	mV
		$V_{CC} = 3.0\text{ V}$	-	119	-	mV
		$V_{CC} = 4.5\text{ V}$	-	205	-	mV
Xtalk	crosstalk	between switches; $R_L = 600\ \Omega$; $C_L = 50\text{ pF}$; $f_i = 1\text{ MHz}$; see Figure 24 .				
		$V_{CC} = 1.65\text{ V}$	-	-	-	dB
		$V_{CC} = 2.3\text{ V}$	-	-56	-	dB
		$V_{CC} = 3.0\text{ V}$	-	-56	-	dB
		$V_{CC} = 4.5\text{ V}$	-	-56	-	dB
		between switches; $R_L = 50\ \Omega$; $C_L = 5\text{ pF}$; $f_i = 1\text{ MHz}$; see Figure 24 .				
		$V_{CC} = 1.65\text{ V}$	-	-	-	dB
		$V_{CC} = 2.3\text{ V}$	-	-29	-	dB
		$V_{CC} = 3.0\text{ V}$	-	-28	-	dB
		$V_{CC} = 4.5\text{ V}$	-	-28	-	dB
Q_{inj}	charge injection	$C_L = 0.1\text{ nF}$; $V_{gen} = 0\text{ V}$; $R_{gen} = 0\ \Omega$; $f_i = 1\text{ MHz}$; $R_L = 1\text{ M}\Omega$; see Figure 25 .				
		$V_{CC} = 1.8\text{ V}$	-	3.3	-	pC
		$V_{CC} = 2.5\text{ V}$	-	4.1	-	pC
		$V_{CC} = 3.3\text{ V}$	-	5.0	-	pC
		$V_{CC} = 4.5\text{ V}$	-	6.4	-	pC
		$V_{CC} = 5.5\text{ V}$	-	7.5	-	pC

11.3 Test circuits



Test conditions:

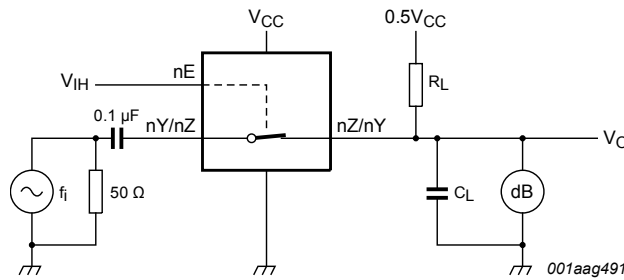
$V_{CC} = 1.65\text{ V}$: $V_i = 1.4\text{ V (p-p)}$

$V_{CC} = 2.3\text{ V}$: $V_i = 2\text{ V (p-p)}$

$V_{CC} = 3\text{ V}$: $V_i = 2.5\text{ V (p-p)}$

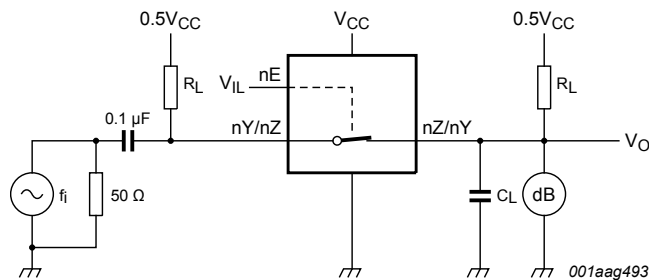
$V_{CC} = 4.5\text{ V}$: $V_i = 4\text{ V (p-p)}$

Figure 20. Test circuit for measuring total harmonic distortion



Adjust f_i voltage to obtain 0 dBm level at output. Increase f_i frequency until dB meter reads -3 dB.

Figure 21. Test circuit for measuring the frequency response when switch is in ON-state



Adjust f_i voltage to obtain 0 dBm level at input.

Figure 22. Test circuit for measuring isolation (OFF-state)

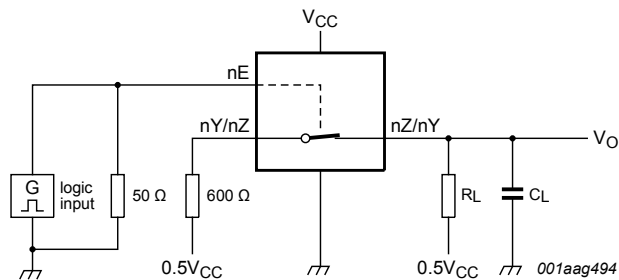
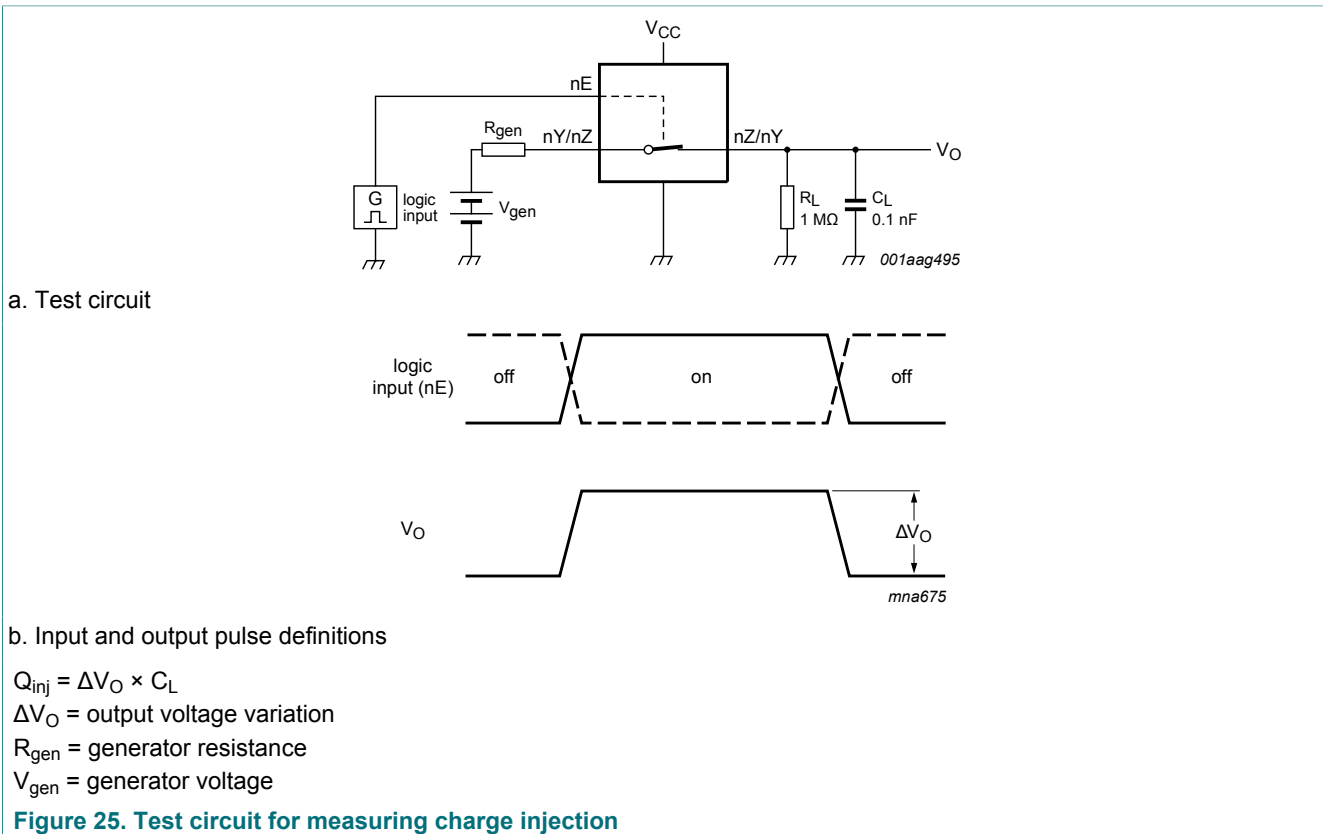
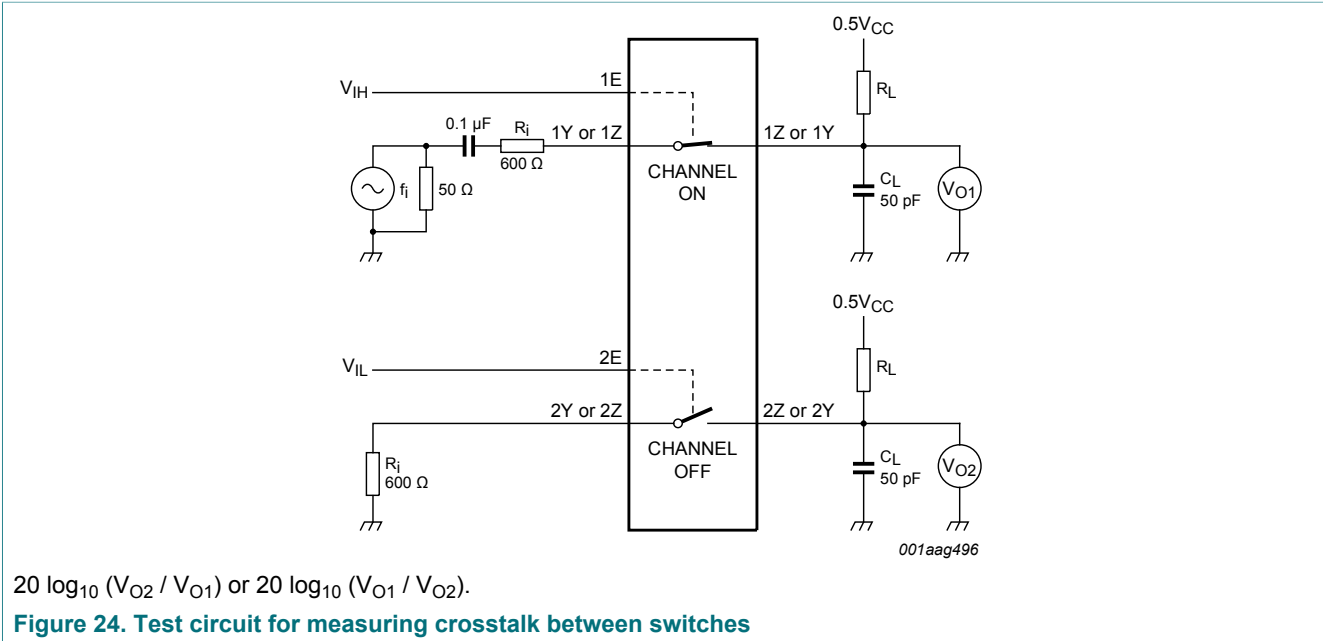
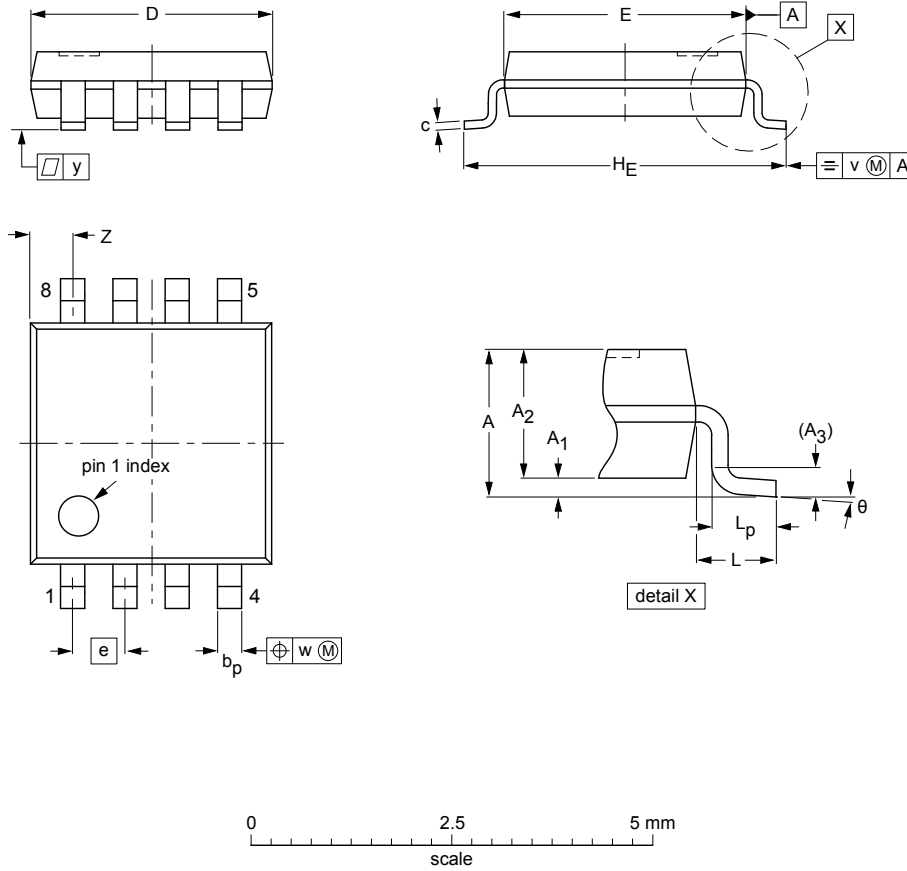


Figure 23. Test circuit for measuring crosstalk voltage (between digital inputs and switch)



12 Package outline

TSSOP8: plastic thin shrink small outline package; 8 leads; body width 3 mm; lead length 0.5 mm SOT505-2



DIMENSIONS (mm are the original dimensions)

UNIT	A max.	A ₁	A ₂	A ₃	b _p	c	D ⁽¹⁾	E ⁽¹⁾	e	H _E	L	L _p	v	w	y	Z ⁽¹⁾	θ
mm	1.1	0.15 0.00	0.95 0.75	0.25	0.38 0.22	0.18 0.08	3.1 2.9	3.1 2.9	0.65	4.1 3.9	0.5	0.47 0.33	0.2	0.13	0.1	0.70 0.35	8° 0°

Note

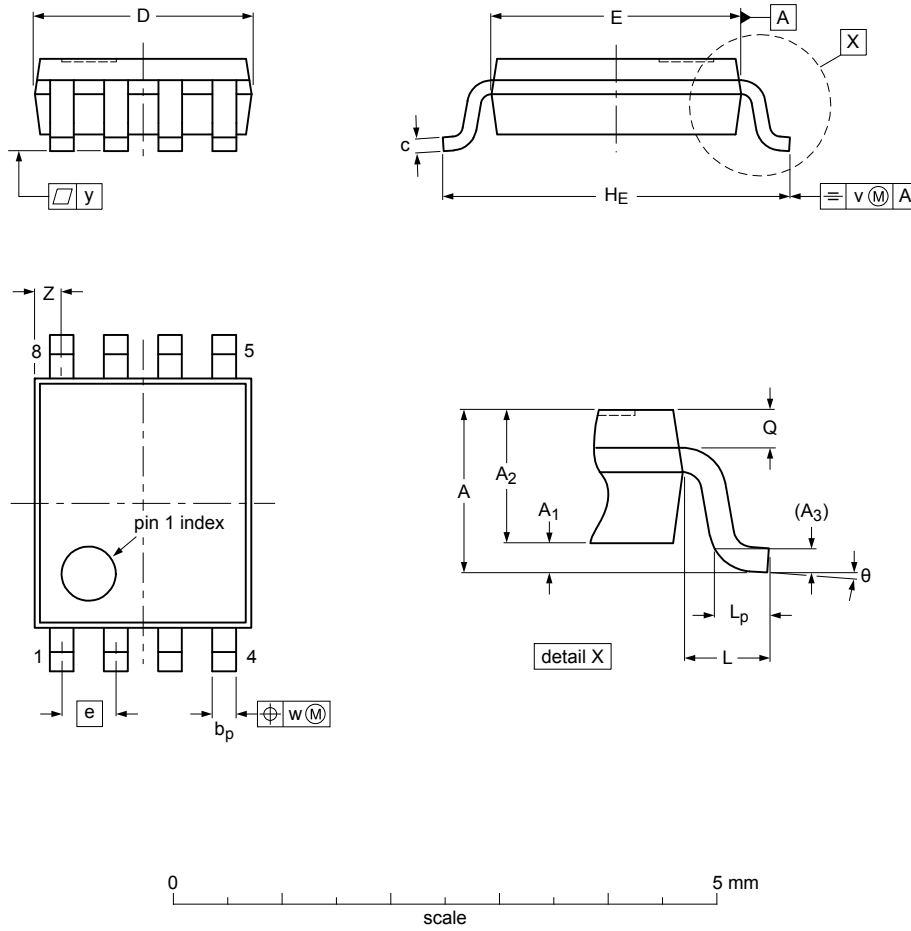
1. Plastic or metal protrusions of 0.15 mm maximum per side are not included.

OUTLINE VERSION	REFERENCES			EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	JEITA		
SOT505-2		---			02-01-16

Figure 26. Package outline SOT505-2 (TSSOP8)

VSSOP8: plastic very thin shrink small outline package; 8 leads; body width 2.3 mm

SOT765-1



Dimensions (mm are the original dimensions)

Unit	A	A ₁	A ₂	A ₃	b _p	c	D ⁽¹⁾	E ⁽²⁾	e	H _E	L	L _p	Q	v	w	y	Z ⁽¹⁾	θ
max		0.15	0.85		0.27	0.23	2.1	2.4		3.2		0.40	0.21				0.4	8°
nom	1			0.12					0.5		0.4			0.2	0.08	0.1		
min		0.00	0.60		0.17	0.08	1.9	2.2		3.0		0.15	0.19				0.1	0°

Note

1. Plastic or metal protrusions of 0.15 mm maximum per side are not included.
2. Plastic or metal protrusions of 0.25 mm maximum per side are not included.

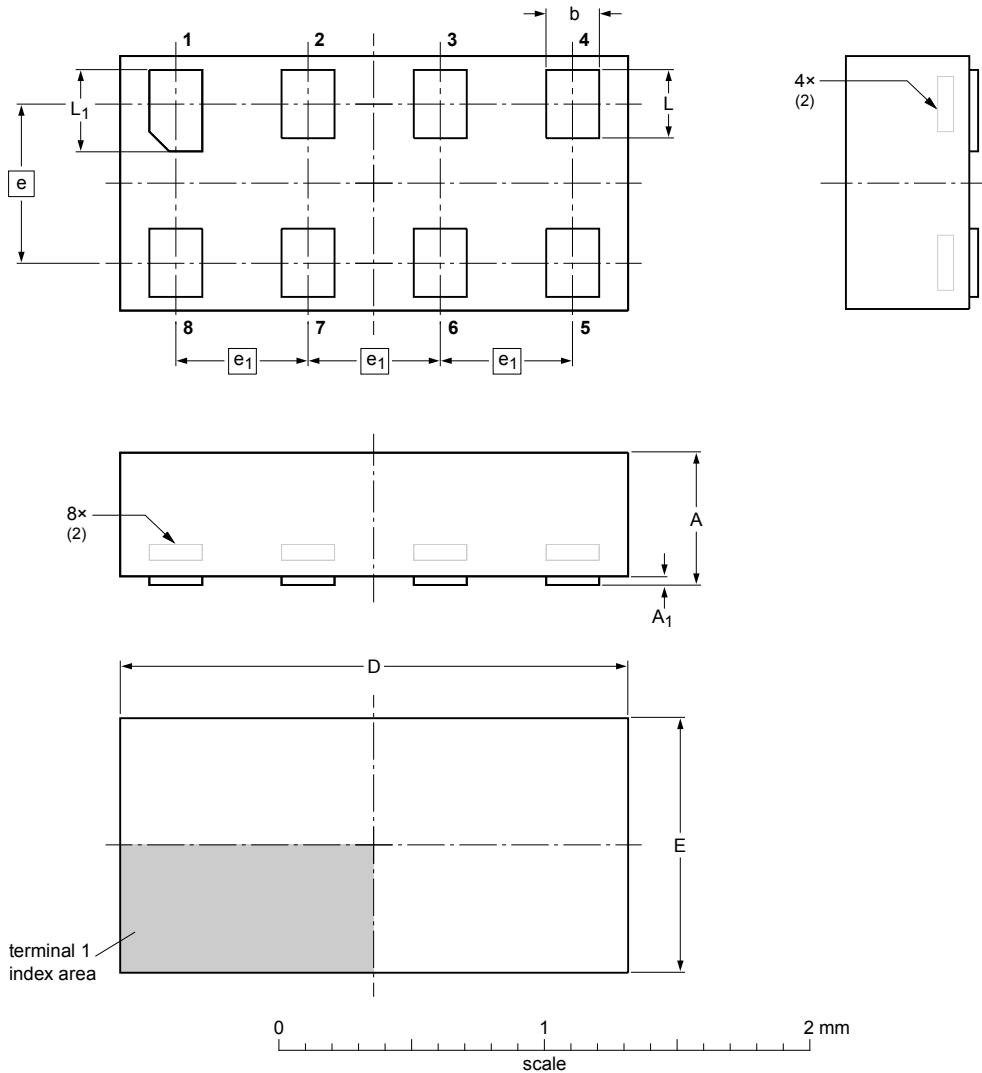
sot765-1_po

Outline version	References				European projection	Issue date
	IEC	JEDEC	JEITA			
SOT765-1		MO-187				07-06-02 16-05-31

Figure 27. Package outline SOT765-1 (VSSOP8)

XSON8: plastic extremely thin small outline package; no leads; 8 terminals; body 1 x 1.95 x 0.5 mm

SOT833-1



DIMENSIONS (mm are the original dimensions)

UNIT	A ⁽¹⁾ max	A ₁ max	b	D	E	e	e ₁	L	L ₁
mm	0.5	0.04	0.25 0.17	2.0 1.9	1.05 0.95	0.6	0.5	0.35 0.27	0.40 0.32

Notes

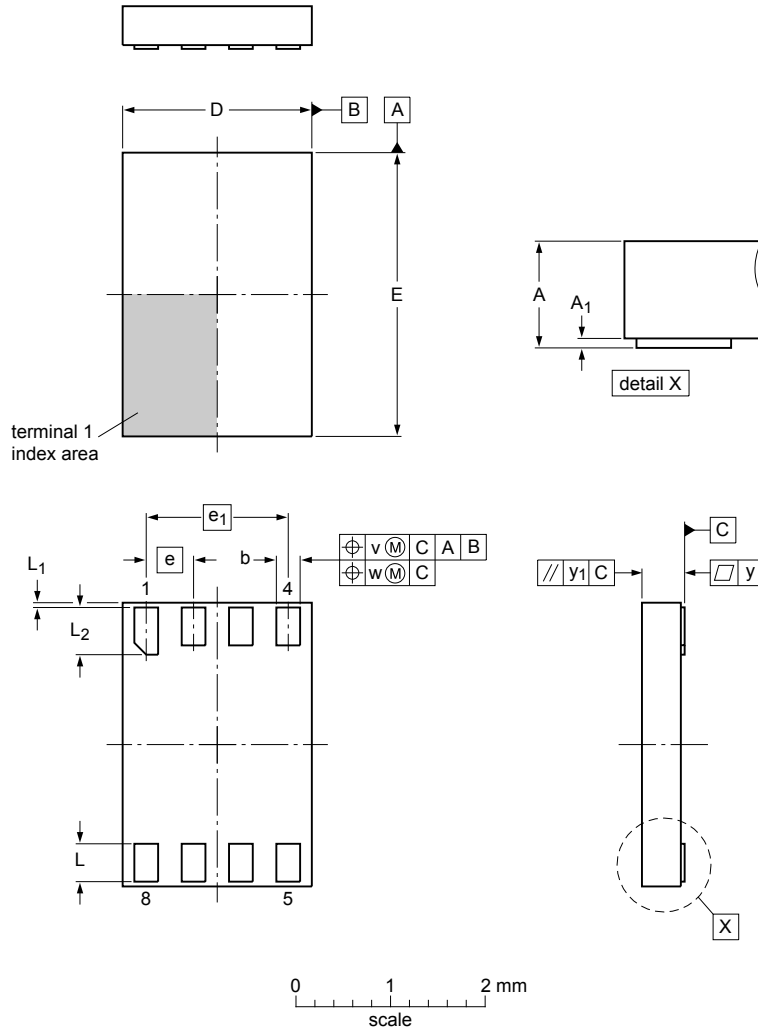
- Including plating thickness.
- Can be visible in some manufacturing processes.

OUTLINE VERSION	REFERENCES			EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	JEITA		
SOT833-1	---	MO-252	---		07-11-14 07-12-07

Figure 28. Package outline SOT833-1 (XSON8)

XSON8: plastic extremely thin small outline package; no leads;
8 terminals; body 3 x 2 x 0.5 mm

SOT996-2



Dimensions (mm are the original dimensions)

Unit ⁽¹⁾	A	A ₁	b	D	E	e	e ₁	L	L ₁	L ₂	v	w	y	y ₁
max		0.05	0.35	2.1	3.1			0.5	0.15	0.6				
nom	0.5					0.5	1.5				0.1	0.05	0.05	0.1
min		0.00	0.15	1.9	2.9			0.3	0.05	0.4				

sot996-2_po

Outline version	References				European projection	Issue date
	IEC	JEDEC	JEITA			
SOT996-2						-07-12-21- 12-11-20

Figure 29. Package outline SOT996-2 (XSON8)

XQFN8: plastic, extremely thin quad flat package; no leads;
8 terminals; body 1.6 x 1.6 x 0.5 mm

SOT902-2

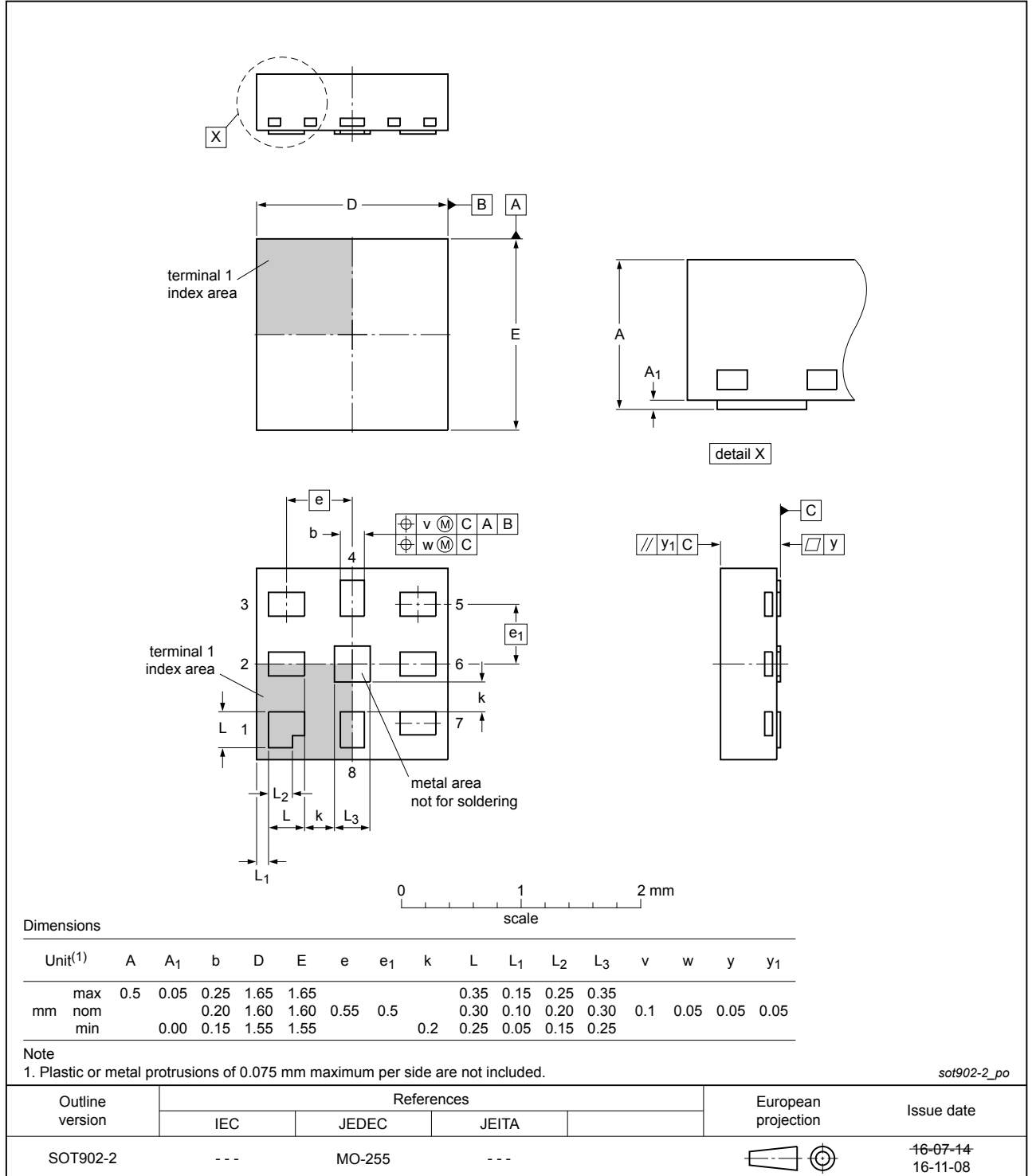
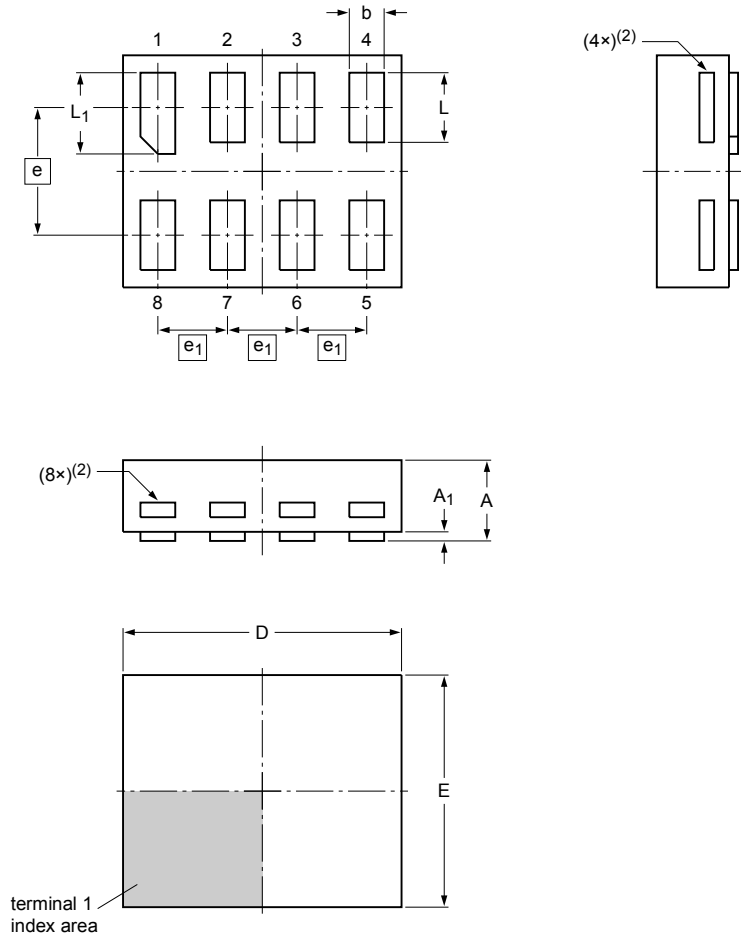


Figure 30. Package outline SOT902-2 (XQFN8)

XSON8: extremely thin small outline package; no leads;
8 terminals; body 1.2 x 1.0 x 0.35 mm

SOT1116



Dimensions

Unit	A ⁽¹⁾	A ₁	b	D	E	e	e ₁	L	L ₁
max	0.35	0.04	0.20	1.25	1.05			0.35	0.40
nom			0.15	1.20	1.00	0.55	0.3	0.30	0.35
min			0.12	1.15	0.95			0.27	0.32

Note

- Including plating thickness.
- Visible depending upon used manufacturing technology.

sot1116_po

Outline version	References				European projection	Issue date
	IEC	JEDEC	JEITA			
SOT1116						-10-04-02- 10-04-07

Figure 31. Package outline SOT1116 (XSON8)

13 Abbreviations

Table 13. Abbreviations

Acronym	Description
CMOS	Complementary Metal-Oxide Semiconductor
DUT	Device Under Test
ESD	ElectroStatic Discharge
HBM	Human Body Model
MM	Machine Model
TTL	Transistor-Transistor Logic

14 Revision history

Table 14. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
74LVC2G66 v.10	20170413	Product data sheet	-	74LVC2G66 v.9
Modifications:	<ul style="list-style-type: none"> The format of this data sheet has been redesigned to comply with the new identity guidelines of Nexperia. Legal texts have been adapted to the new company name where appropriate. Type number 74LVC2G66GN (XSON8/SOT1116) has been added. 			
74LVC2G66 v.9	20161215	Product data sheet	-	74LVC2G66 v.8
Modifications:	<ul style="list-style-type: none"> Table 7: The maximum limits for leakage current and supply current have changed. 			
74LVC2G66 v.8	20130402	Product data sheet	-	74LVC2G66 v.7
Modifications:	<ul style="list-style-type: none"> For type number 74LVC2G66GD XSON8U has changed to XSON8. 			
74LVC2G66 v.7	20120622	Product data sheet	-	74LVC2G66 v.6
Modifications:	<ul style="list-style-type: none"> For type number 74LVC2G66GM the SOT code has changed to SOT902-2. 			
74LVC2G66 v.6	20111129	Product data sheet	-	74LVC2G66 v.5
Modifications:	<ul style="list-style-type: none"> Legal pages updated. 			
74LVC2G66 v.5	20100616	Product data sheet	-	74LVC2G66 v.4
74LVC2G66 v.4	20080701	Product data sheet	-	74LVC2G66 v.3
74LVC2G66 v.3	20080310	Product data sheet	-	74LVC2G66 v.2
74LVC2G66 v.2	20070828	Product data sheet	-	74LVC2G66 v.1
74LVC2G66 v.1	20040629	Product data sheet	-	-

15 Legal information

15.1 Data sheet status

Document status ^{[1][2]}	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <http://www.nexperia.com>.

15.2 Definitions

Draft — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. Nexperia does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Short data sheet — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local Nexperia sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

Product specification — The information and data provided in a Product data sheet shall define the specification of the product as agreed between Nexperia and its customer, unless Nexperia and customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the Nexperia product is deemed to offer functions and qualities beyond those described in the Product data sheet.

15.3 Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, Nexperia does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. Nexperia takes no responsibility for the content in this document if provided by an information source outside of Nexperia. In no event shall Nexperia be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory. Notwithstanding any damages that customer might incur for any reason whatsoever, Nexperia's aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the Terms and conditions of commercial sale of Nexperia.

Right to make changes — Nexperia reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — Nexperia products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical

systems or equipment, nor in applications where failure or malfunction of an Nexperia product can reasonably be expected to result in personal injury, death or severe property or environmental damage. Nexperia and its suppliers accept no liability for inclusion and/or use of Nexperia products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. Nexperia makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification. Customers are responsible for the design and operation of their applications and products using Nexperia products, and Nexperia accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the Nexperia product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products. Nexperia does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using Nexperia products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). Nexperia does not accept any liability in this respect.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

Terms and conditions of commercial sale — Nexperia products are sold subject to the general terms and conditions of commercial sale, as published at <http://www.nexperia.com/profile/terms>, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. Nexperia hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of Nexperia products by customer.

No offer to sell or license — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.