



Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of “Quality Parts,Customers Priority,Honest Operation,and Considerate Service”,our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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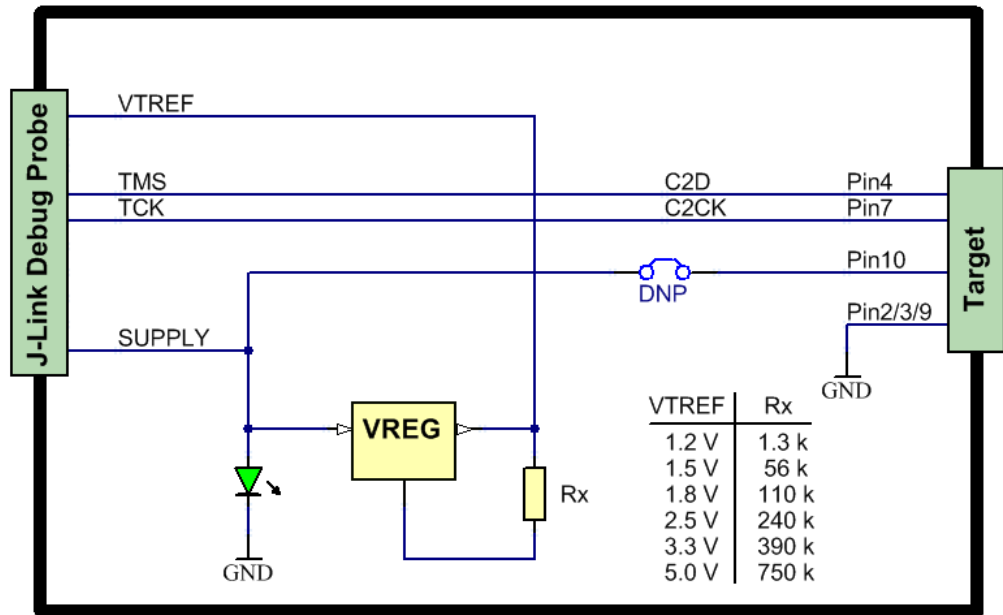
[J-Link Debug Probes](#) [Adapters](#) [SiLabs C2 Adapter](#)

J-Link SiLabs C2 Adapter

The J-Link SiLabs C2 Adapter allows debug connections between J-Link and target hardware which comes with Silicon Labs C2 debug interface. It adapts from the 20-pin 0.1" standard debug connector to a 10-pin 0.1" connector pinout compatible with Silicon Labs eval boards.

Since V_{TRef} is not present on all eval boards, it is provided by the J-Link SiLabs C2 Adapter. Default V_{TRef} voltage is 3.3 V. For increased flexibility other target voltages in the range from 1.2 V to 5.0 V can be generated by replacing a single resistor on the adapter.

The J-Link SiLabs C2 Adapter is compatible with [J-Link](#), [J-Link PRO](#), [J-Link ULTRA+](#) and [J-Link PLUS](#).



Block diagram of the J-Link SiLabs C2 Adapter