



Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of “Quality Parts,Customers Priority,Honest Operation,and Considerate Service”,our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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1.1. Content

1.2. Method

2. REQUIREMENTS

2.1. Shoulder Cutoff and Burr

A. Shoulder Cutoff

Shoulder cutoff shall meet the requirements of the product drawing. Shoulder shall be symmetrical to the post within the limits shown in Figure 2.

B. Burr

Burr on shoulder shall not exceed the limits shown in Figure 1.

2.2. Clearance

Maximum clearance between the post shoulder and the printed circuit board shall not exceed the limits of Figure 3.


2.3. Rotation

Post rotation shall be within the limits shown in Figure 4.

2.4. Perpendicularity at Post Tips

Perpendicularity of posts shall be within the limits shown in Figure 5 for AMPMODU Mod I posts and Figure 6 for Mod II posts.

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				CHK <i>S. Baker</i> 10/8/75	
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				NAME	
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				1 OF <u>2</u>	
					POST, AMPMODU MOD I AND II, APPLICATION OF
LTR	REVISION RECORD	APP	DATE		

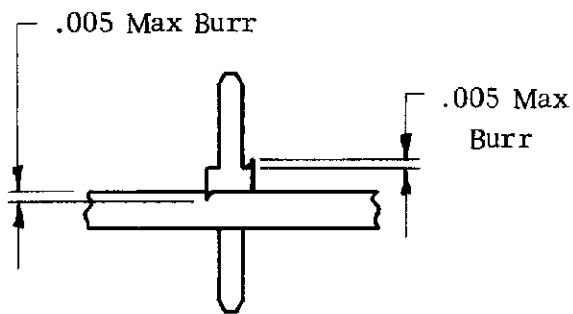


Figure 1

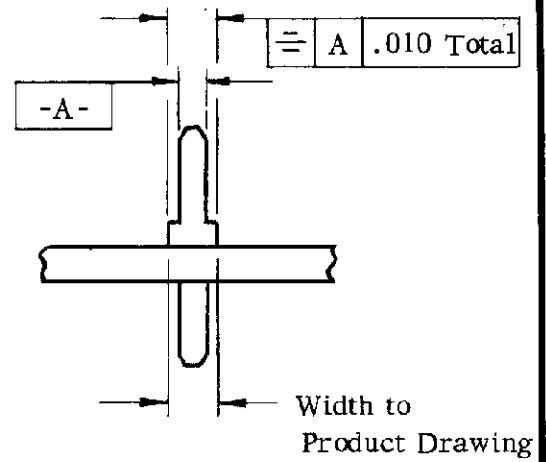


Figure 2

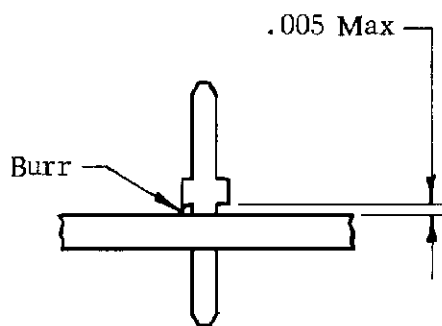


Figure 3

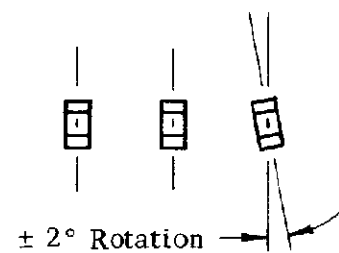


Figure 4

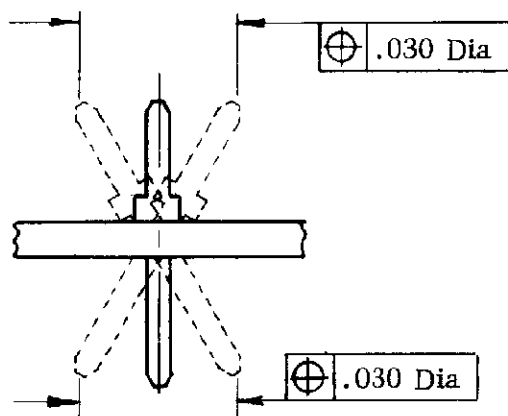


Figure 5
Mod I

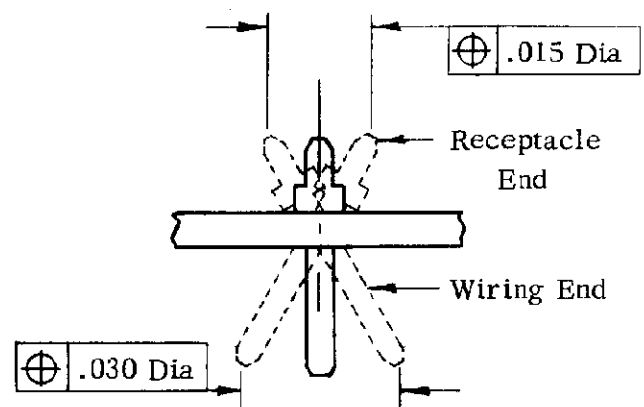


Figure 6
Mod II



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