## imall

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



## Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832 Email & Skype: info@chipsmall.com Web: www.chipsmall.com Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China



8

6

3M™ TWIN AXIAL PCI EXPRESS X8 MALE-TO-MALE CABLE ASSEMBLY

 $\overline{\phantom{a}}$ 

8

 $\square$ 

В

A

PCIE X8 EXTENDER PCB-PART NO. 78-9101-6470-2

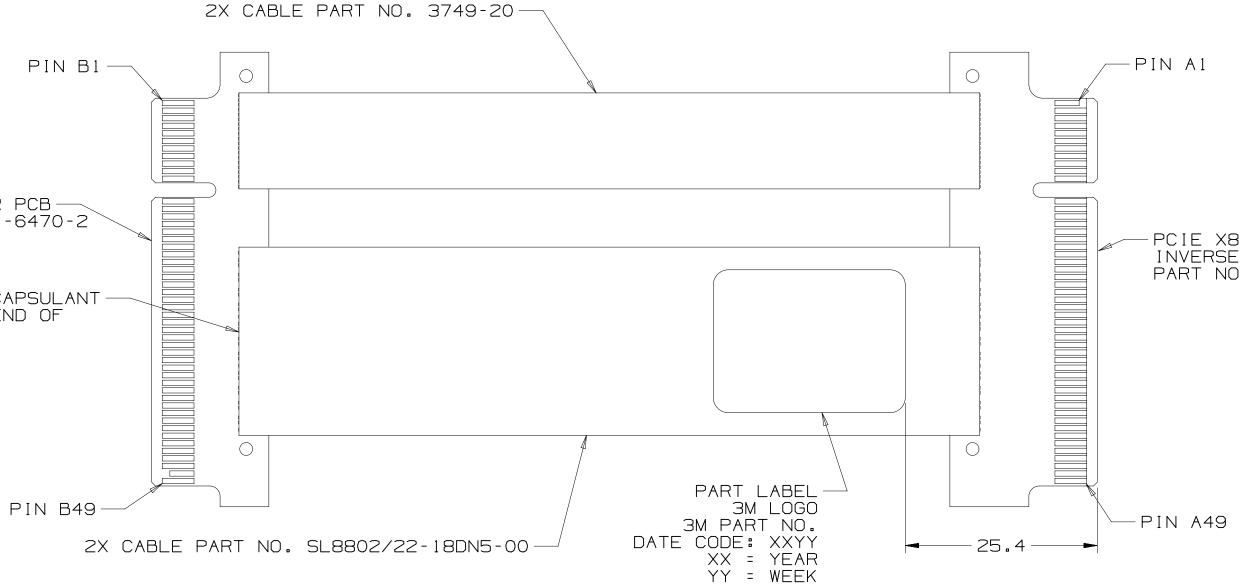
6

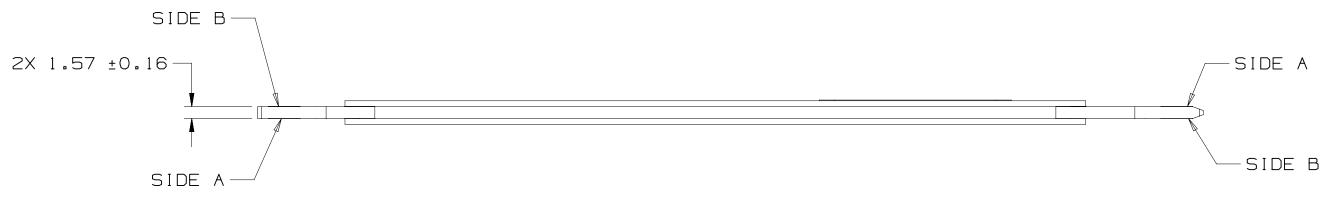
ADHESIVE ENCAPSULANT ----APPLIED TO END OF EACH CABLE

BILL OF MATERIALS ITEM DESCRIPTION QTY PCIE X8 EXTENDER (98 POS) CARD-EDGE 1.0 INVERSE PCB 1 3M P/N: 78-9101-8105-2 PCIE X8 EXTENDER (98 POS) CARD-EDGE 2.0 PCB 3M P/N: 78-9101-6470-2 RIBBON TWIN AX CABLE CABLE З.О 2 3M P/N: SL8802/22-18DN5-00 RIBBON CABLE, 20 POSITION 3M P/N: 3749-20 CABLE 2 4.0 STRAIN AS ADHESIVE 5.0 REQUIRED el Ief

ADHESIVE ENCAPSULANT -APPLIED TO END OF EACH CABLE









4

З

5

	2			1			
				NOTES			
		1	DIMENSIONS ARE IN MILLIMETERS.				
			30 IMF OVE	M TWIN AXIAL CABLE DESCRIPTION: AWG, SILVER PLATED SIGNAL WIRE PEDANCE: 85 ±5 OHM ERALL RIBBON WIDTH: 24.90 MM ERALL RIBBON THICKNESS: 0.75 MM			
		1	INF CON WWW	HS COMPLIANT. SEE REGULATORY FORMATION APPENDIX IN "ROHS MPLIANCE" SECTION AT W.3MCONNECTORS.COM & C1 APPLY)			
ε	EXTENDER PCB		ALU USE IN NEC APF	IS CABLE CONSTRUCTION HAS A THIN JMINUM LAYER AT EACH EDGE. ER SHOULD EVALUATE ITS USE THEIR APPLICATION AND, IF CESSARY, INSULATING TAPE MAY BE PLIED TO COVER THE ALUMINUM YER, AS USER DEEMS APPROPRIATE.			
	78-9101-8105-2		PRC TWI	PLICABLE SPECIFICATIONS: DDUCT SPECIFICATION NUMBER: N AX CABLE: PS-0106 BLE ASSEMBLY: PS-0137			
				DER BY APPLICABLE 3M PART NUMBER: 13-0734-XXXX			
			XXX	(X = DIM 'A' IN MILLIMETERS (1000 = 1 METER)			
			8KF	NDARD LENGTH (DIM 'A') 13-0734-0250 (250 MM) 13-0734-0500 (500 MM)			
)		l	UPC	N-STANDARD LENGTHS AVAILABLE ON SPECIAL REQUEST. MAY REQUIRE GHER MOQS AND LONGER LEAD TIMES.			
		•	⊤WI	AMMABILITY RATING: In ax cable: ul94 hb 3s: ul94v-o	$\square$		
		1	0.7 HAF	DDLECARD FINGER GOLD THICKNESS: 76 µm [30 µ"] MIN ELECTROLYTIC RD GOLD OVER 1.27 µm [50 µ"] N ELECTROLYTIC NICKEL.			

78-51 DRAWING NU В

LDS TS B 47199 APR 18,2013 ADDED PINOUT TABLES ON SHEET 2 LDS TS A 45126 MAR 04,2013 INITIAL RELEASE ISSUE DATE AND DESCRIPTION DRFT CHKD DESIGN REFERENCE NEXT ASSEMBLY REV ECO JAN 03,2013 MFG DATE SCHMIDT DATE JAN 07,2013 © 3M COPYRIGHT **2013** This document and the information it contains are 3M property and may not be reproduced or further distributed without 3M permission, or used or disclosed other than for 3M authorized purposes. All rights reserved. DIVISION CODE DIVISION Center **3**M DO NOT Scale Drawing  $\frac{SCALE}{1}$ Paul, TOLERANC EXCEPT A NOTED 55144 INCHES 8KH3-0734-XXXX, CABLE 00 ASSY, MALE PCIÉ X8 TO сл Г THIRD ANGLE PROJECTION 1000 + MALE PCIE X8 INTERPRET PER ASME Y14.5 - 1994 MILLIMETERS 0 ± 1 MAX SURFACE ROUGHNESS 125 ALL SURFACES .00 ±.05 .000 ±.005 D 78-5100-2569-1 CAGE NUMBER В MARKED ONLY ANGLES ±1 • MODEL LISTS IYES NO SHT 1 OF 2

2

8

7

3M 3749-20 Cable

6

Γ	
L	_

 $\square$ 

 $\longrightarrow$ 

B

A

8

(	
r	
$\sim$	

	3M 3749-20 Lable							<u>3M 3749-20 Lab</u>	
Wire #	Pin Attachment #							Pin Attachment #	Wire 7
01	Ground Layer							Ground Layer	01
02	B01 - B02							AO 1	02
03	B01 - B02							Ground Layer	03
04	B01 - B02			PCI-Express	X8 Pin-Out		]	A02 - A03	04
05	B01 - B02	-	Pin #	Side B Description	Side A Description	Pin #		AO2 - AO3	05
					Hot plug presence				
06	B01 - B02		BO1	+12 volt power	detect	AO 1		A02 - A03	06
07	B01 - B02		B02	+12 volt power	+12 volt power	A02		A02 - A03	07
08	Ground Layer		B03	+12 volt power	+12 volt power	AO3		Ground Layer	08
09	B05		B03	Ground	Ground	A04		A05	09
10	B06						-	A06	10
1 1	Ground Layer		B05	SMBus clock	TCK	A05	-	A07	1 1
12	B08		B06	SMBus data	TDI	A06	-	AO8	12
13	B08		B07	Ground	TDO	A07	-	Ground Layer	13
1 4	B09		- <b>B</b> 08	+3.3 volt power	TMS	A08		A09 - A10	14
			B09	+TRST#	+3.3 volt power	A09			
15	B10		- <b>⊳</b> B10	3.3v volt power	+3.3 volt power	A10		A09 - A10	15
16	B10		B11	Link Reactivation	Power Good	A 1 1		A09 - A10	16
1 /	Ground Layer			Mechani		I	1	A09 - A10	
18	B11		B12	Reserved	Ground	A12		Ground Layer	18
19	Ground Layer		B13	Ground	Reference Clock	A13		Ground Layer	19
20	B12		B14					A 1 1	20
				Transmitter Lane 0,	Differential pair	A 1 4	4		
	3M SL8802 Cable		B15	Differential pair	Ground	A15	4	3M SL8802 Cabl	
			B16	Ground	Receiver Lane 0,	A16			i
Wire #	Pin Attachment #		B17	Hotplug detect	Differential pair	A17		Pin Attachment #	Wire
GND	Ground Layer		B18	Ground	Ground	A18		Ground Layer	GND
or_01	B14		B19	Transmitter Lane 1,	Reserved	A19		A13	01
or_01	B15		/B20	Differential pair	Ground	A20		A14	pr_01
or_02	B17		B21	Ground	Receiver Lane 1,	A21	]	A16	pr_02
or_02	Ground Layer		B22	Ground	Differential pair	A22		A17	pr_02
or_03	B19		B23	Transmitter Lane 2,	Ground	A23		Ground Layer	pr_03
or_03	B20		B24	Differential pair	Ground	A24		A19	pr03
or_04	B23		B25	Ground	Receiver Lane 2,	A25		A21	pr04
or_04	B24		B26					A22	pr04
<u>gnd</u>	Ground Layer			Ground	Differential pair	A26			GND
			B27	Transmitter Lane 3,	Ground	A27		Ground Layer	
or_05	B27		B28	Differential pair	Ground	A28		A25	05
or_05	B28		B29	Ground	Receiver Lane 3,	A29		A26	pn05
or_06	B30		B30	Reserved	Differential pair	0EA		A29	06
or_06	B31		B31	Hot plug detect	Ground	A31		A30	06
or_07	B33		B32	Ground	Reserved	A32		A32	pr_07
or_07	B34		B33	Transmitter Lane 4,	Reserved	A33	1	A33	pr_07
GND	Ground Layer		B34	Differential pair	Ground	A34	1	Ground Layer	GND
or_08	B37		B35	Ground	Receiver Lane 4,	A35	1	A35	pr_08
or_08	B38		B36	Ground	Differential pair	A36		A36	pr_08
or_09	841	$\neg$	B37			A37	1	A39	pr_09
or_09	B42			Transmitter Lane 5,	Ground			A40	
		-	B38	Differential pair	Ground	A38			pr_09
or_10	B45	-	B39	Ground	Receiver Lane 5,	A39		A43	pr_10
or_10	B46	-	B40	Ground	Differential pair	A40		A44	pr_10
or_11	B48	$ \rightarrow $	B41	Transmitter Lane 6,	Ground	A41		A47	pr_11
or_11	Ground Layer		B42	Differential pair	Ground	A42		A48	pr11
GND	Ground Layer		B43	Ground	Receiver Lane 6,	A43		Ground Layer	GND
			B44	Ground	Differential pair	A44			
			B45	Transmitter Lane 7,	Ground	A45	1 /		
			B46	Differential pair	Ground	A46	1 /		
			B47	Ground	Receiver Lane 7,	A47	1		
			B48		Differential pair	A48			
				Hot plug detect			4		
			B49	Ground	Ground	A49	1		

5

3M 3749-20 Ca	
Pin Attachment #	Wire #
Ground Layer	01
AO 1	02
Ground Layer	63
A02 - A03	04
A02 - A03	05
A02 - A03	06
A02 - A03	07
Ground Layer	08
A05	09
A06	10
A07	1 1
A08	12
Ground Layer	13
A09 - A10	14
A09 - A10	15
A09 - A10	16
A09 - A10	17
Ground Layer	18
Ground Layer	19
A 1 1	20

4

4

Э

3

2

B 47199 APR 18,2013 LDS TS   ADDED PINOUT TABLES INDET 2   A 45126 MAR 04,2013 LDS TS   INITIAL RELEASE INITIAL RELEASE   DESIGN REFERENCE NEXT ASSEMBLY   REV ECO   DIVISION DIVISION CODE											
A   45126   MAR 04,2013   LDS TS     INITIAL RELEASE   INITIAL RELEASE     DESIGN REFERENCE   NEXT ASSEMBLY   REV   ECO   ISSUE DATE AND DESCRIPTION   DRT   CHKD     ACCESS   OFFT   DATE   DATE <td></td> <td></td> <td></td> <td>В</td> <td>47199</td> <td>APR 18</td> <td>,2013</td> <td>LDS</td> <td>TS</td> <td>0</td>				В	47199	APR 18	,2013	LDS	TS	0	
A   45126   MAR 04,2013   LDS TS     INITIAL RELEASE   INITIAL RELEASE     DESIGN REFERENCE   NEXT ASSEMBLY   REV   ECO   ISSUE DATE AND DESCRIPTION   DRT   CHKD     ACCESS   OFFT   DATE   DATE <td></td> <td></td> <td></td> <td></td> <td></td> <td>ADDED PIN</td> <td>OUT TABLES</td> <td></td> <td></td> <td></td>						ADDED PIN	OUT TABLES				
INITIAL RELEASE     DESIGN REFERENCE   NEXT ASSEMBLY     REV   ECO   ISSUE DATE AND DESCRIPTION   DAFT     CODES   DATE   DATE   DATE     DIVISION   DIVISION CODE   DATE   DATE   DATE     DO NOT   SCALE   TOLERANCES   EXCEPT AS   NOTED   Macenter     DO NOT   SCALE   TOLERANCES   EXCEPT AS   NOTED   This document and the information it contains are disclosed of the reproduced or further distributed without 3M permission, or used or distributed without 3M					ON SHEET 2					0	
DESIGN REFERENCE NEXT ASSEMBLY REV ECO ISSUE DATE AND DESCRIPTION DRFT CHKD ACCESS A				А	45126	MAR 04	,2013	LDS	TS	- - -	
CODES   L" SCHMIDT   JAN 03,2013   M"O   DATE     DIVISION   DIVISION CODE   DATE   APPVL   JAN 07,2013   APPVL     DIVISION   DIVISION CODE   DATE   TSUNIGA   JAN 07,2013   APPVL     DO NOT   SCALE   TOLERANCES   TOLERANCES   St. Poul, MN 55144   Tis document and the information it contains are   Mithed the information it contains are     DRAWING   1   TOLERANCES   TITLE   St. Poul, MN 55144   Tis document and the information it contains are   Mithed the information it contains are     DRAWING   1   TITLE   St. Poul, MN 55144   Tis document and the information it contains are   Mithed the information it contains are     THIRD ANGLE PROJECTION   INCHES   TITLE   SKH3 - 0734 - XXXX g   CABLE     ALSSY g   MALE   PCIE X8   NO     MAX SURFACE ROUGHNESS   0 ± 1   0 ± 1   MALE   DRAWING NO.     INMER   ALL SURFACES   OO ± 05   DRAWING NO.   TREV.     MAX SURFACE ROUGHNESS   0 ± 1.05   NODE   DRAWING NO.   REV.     INMARE   ALL SURFACES   NODE   DRAWING NO.   DET </td <td></td> <td></td> <td></td> <td></td> <td></td> <td>INITIAL F</td> <td>RELEASE</td> <td></td> <td></td> <td></td>						INITIAL F	RELEASE				
CODES   L" SCHMIDT   JAN 03,2013   M"O   DATE     DIVISION   DIVISION CODE   DATE   APPVL   JAN 07,2013   APPVL     DIVISION   DIVISION CODE   DATE   TSUNIGA   JAN 07,2013   APPVL     DO NOT   SCALE   TOLERANCES   TOLERANCES   St. Poul, MN 55144   Tis document and the information it contains are   Mithed the information it contains are     DRAWING   1   TOLERANCES   TITLE   St. Poul, MN 55144   Tis document and the information it contains are   Mithed the information it contains are     DRAWING   1   TITLE   St. Poul, MN 55144   Tis document and the information it contains are   Mithed the information it contains are     THIRD ANGLE PROJECTION   INCHES   TITLE   SKH3 - 0734 - XXXX g   CABLE     ALSSY g   MALE   PCIE X8   NO     MAX SURFACE ROUGHNESS   0 ± 1   0 ± 1   MALE   DRAWING NO.     INMER   ALL SURFACES   OO ± 05   DRAWING NO.   TREV.     MAX SURFACE ROUGHNESS   0 ± 1.05   NODE   DRAWING NO.   REV.     INMARE   ALL SURFACES   NODE   DRAWING NO.   DET </td <td></td> <td></td> <td>ASSEMBLY</td> <td>REV</td> <td>ECO</td> <td>ISSUE DATE AN</td> <td>ND DESCRIPTION</td> <td>DRFT</td> <td>CHKD</td> <td>Z</td>			ASSEMBLY	REV	ECO	ISSUE DATE AN	ND DESCRIPTION	DRFT	CHKD	Z	
DIVISION DIVISION CODE DIVISION CO					CHMIDT	JAN 03,2013	MFG	DATE			
DIVISION DIVISION CODE DIVISION CO				СНКД		DATE	APPVL T SUNIGA	JAN 07	7,2013	ΔO	
DO NOT   SCALE   TOLERANCES     SCALE   1   TOLERANCES   St. Paul,     DRAWING   1   Inches     Image: Scale   1   Inches     DRAWING   1   Inches     Image: Scale   1   Inches     Image: Scale   1   Inches     Image: Scale   1		DIVISION	DIVISION CODE	_	© 3M COPYRIGHT 2013						
SCALE   1   EXCEPT AS NOTED   disclosed other than for 3M authorized purposes.     DRAWING   1   INCHES   INCHES   disclosed other than for 3M authorized purposes.     INTERPRET PER ASME Y14.5 - 1994   INCLIMETERS   0   t   BKH3 - 0734 - XXXX, CABLE     MAX SURFACE ROUGHNESS   0   t   0   t   MALE PCIE X8   NO     125   ALL SURFACES   00   t.005   SIZE   DRAWING NO.   REV.					st Rawl AM property and may not be reproduced on further						
THIRD ANGLE PROJECTION   .00   ±     ASSY, MALE PCIE X8 TO     INTERPRET PER ASME Y14.5 - 1994   MILLIMETERS 0 ± 1     .00   ±     MAX SURFACE ROUGHNESS   .0     125   ALL SURFACES     .00   ±.005     MODEL   MODEL	ECALE 1 EXCEPT AS			P	MN 55144 disclosed other than for 3M authorized purposes.						
THIRD ANGLE PROJECTION   .00   ±     ASSY, MALE PCIE X8 TO     INTERPRET PER ASME Y14.5 - 1994   MILLIMETERS 0 ± 1     .00   ±     MAX SURFACE ROUGHNESS   .0     125   ALL SURFACES     .00   ±.005     MODEL   MODEL					All rights reserved.						
THIRD ANGLE PROJECTION   .00   ±     ASSY, MALE PCIE X8 TO     INTERPRET PER ASME Y14.5 - 1994   MILLIMETERS 0 ± 1     .00   ±     MAX SURFACE ROUGHNESS   .0     125   ALL SURFACES     .00   ±.005     MODEL   MODEL		(							_	α,	
INTERPRET PER ASME Y14.5 - 1994   MILLIMETERS 0 ± 1   MALE PCIE X8     MAX SURFACE ROUGHNESS 125   0 ± .5 .00 ± .005   CAGE SIZE DRAWING NO. NUMBER D 78 - 5100 - 2569 - 1   REV. B				В	КНЗ	-0/34->	XXXX, L	ABI		ப்	
INTERPRET PER ASME Y14.5 - 1994   MILLIMETERS 0 ± 1   MALE PCIE X8     MAX SURFACE ROUGHNESS 125   0 ± .5 .00 ± .005   CAGE SIZE DRAWING NO. NUMBER D 78 - 5100 - 2569 - 1   REV. B		+		А	$\Box \Box Y$	MALE	PRIF X	Ϋ́Ω΄	$T \cap$	$\sim$	
ASME Y14.5 - 1994 0 $\pm 1$ MAX SURFACE ROUGHNESS 00 $\pm .05$ 125 ALL SURFACES 000 $\pm .005$ MODEL DET REV.	THIRD ANGLE PROJECTION 0000 ±			/ \		·		$\sim$			
						MALE P	CIE X8				
		ASME 114.5 - 1994								P O	
		MAX SURFACE ROUGHNESS				DRAWING NO.		1	REV.		
				D/8-2100-2269-1  B						D m C	
2 1			ANGLES ±1 °	MODE	ΞL		ISTS 🗆 YES 🛛 NO S	6HT <b>2</b>	OF 2	_	
							1		Į		
		$\leq$					$\bot$				

NO REVIS 00 - 2569 - 1 <sup>MBER</sup> 78-51 DRAWING NU В

 $\square$ 

 $\square$